

IFLP-4040DZ-01

Vishay Dale

Low Profile, High Current Inductors - DC Resistance Tolerance 3 %

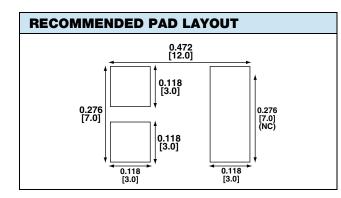


Patents Pending

STANDARD ELECTRICAL SPECIFICATIONS							
L ₀ INDUCTANCE ± 20 % AT 100 kHz, 0.25 V, 0 A (μH)	DCR ± 3 % AT 25 °C (mΩ)	HEAT RATING CURRENT DC TYP. (A) ⁽³⁾	SATURATION CURRENT DC TYP. (A) ⁽⁴⁾				
0.34		32	36				
0.42	0.88		30				
0.50			25				
0.62			20				

Notes

- (1) All test data is referenced to 25 °C ambient
- ⁽²⁾ Operating temperature range -55 °C to +125 °C
- ⁽³⁾ DC current (A) that will cause an approximate ΔT of 40 °C
- $\overset{(4)}{\longrightarrow}$ DC current (A) that will cause L_0 to drop approximately 20 %
- (5) The part temperature (ambient + temp. rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.



DESCRIPTION

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IFLP-4040DZ-01	0.42 μH	± 20 %	ER	e3	
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE	JEDEC [®] LEAD (Pb)-FREE STANDARD	
GLOBAL PART	NUMBER				
I F L	P 4 0	4 0 D Z	ERR	4 2 M	0 1
PRODUCT FAM	IILY	SIZE	PACKAGE IN CODE	NDUCTANCE TOL VALUE	SERIES
Revision: 07-Feb-17		1		Docume	ent Number: 34200



- Handles high transient current spikes without saturation
 FREE
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

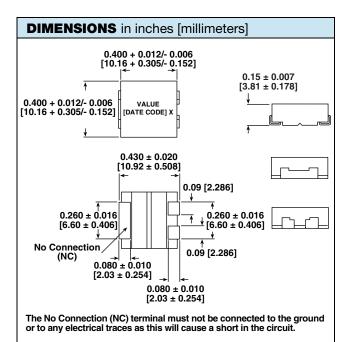
FEATURES

Shielded construction

· Frequency range up to 5.0 MHz

Lowest DCR/µH, in this package size

- Notebook/desktop/server applications
- High current POL converters
- · Low profile, high current power supplies
- Battery powered devices
- DC/DC converters in distributed power systems



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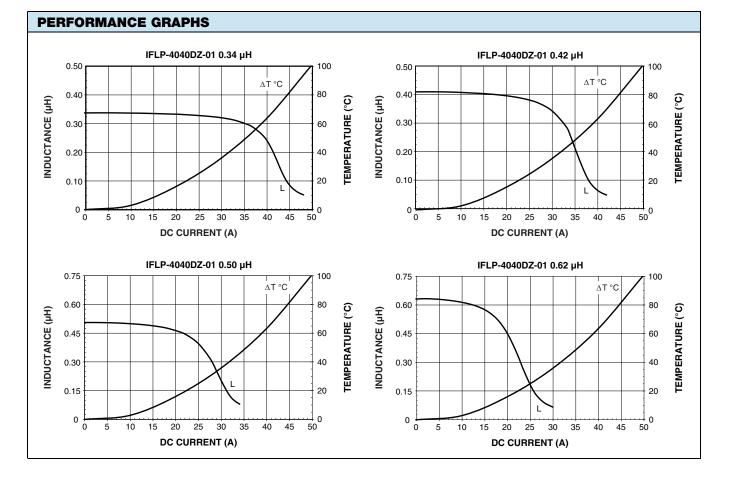
Not for New Designs



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