






# SPECIFICATIONS

**CUSTOMER** : \_\_\_\_\_  
**MODEL NO.** : **GFC2002P-GPAEJUS**  
**VERSION** : **B**  
**DATE** : **2022.11.15**  
**CERTIFICATION** : **ROHS**

Customer Sign	Approved By	Prepared By	Prepared By
			

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## 1. SCOPE

This specification covers the engineering requirements for the GFC2002P-GPAEJUS liquid crystal module.

## 2. PRODUCT SPECIFICATIONS

### 2.1 General

- 20 × 2 dot matrix LCD
- STN(Gray), Positive mode LCD panel
- Transflective , Wide temperature type
- 6 o'clock
- Multiplexing driving : 1/16duty, 1/5bias
- Controller IC : UCI7066CBXB
- Backlight: Yellow Green(Edge)

### 2.2 Mechanical Characteristics

Item	Value	Unit
Number of Characters	20X2	Character
Character Format	5X8 Dots	-
Character Pitch	2.5(W)X5.34(H)	mm
Character Size	1.95(W) X4.51(H)	mm
Dot size	0.35(W)X0.52(H)	mm
Dot pitch	0.4(W)X0.57(H)	mm
Module dimension	72 (W)X32(H)X11.5 MAX(T)	mm
Active Area	49.45(W)X9.85(H)	mm
Viewing Area	54(W) X 15(H)	mm
Module	--	



## 2.3 Absolute Maximum Ratings (Without LED back-light)

Characteristic	Symbol	Unit	Value
Operating Voltage (logic)	$V_{DD}$	V	-0.3 to +5.0
Input Voltage	$V_{IN}$	V	-0.3 to $V_{DD}+0.3$

Note 1: Referenced to  $V_{SS}=0V$

## 2.4 Electrical Characteristics (Without LED back-light)

Characteristic	Symbol	Condition	Min.	Typ.	Max.	Unit
Operating Voltage(logic)	$V_{DD}-V_{SS}$	--	4.5	5.0	5.5	V
Input Voltage	$V_{IH}$	--	$0.8V_{DD}$	--	$V_{DD}$	V
	$V_{IL}$	--	$V_{SS}$	--	$0.2V_{DD}$	
Output Voltage	$V_{OH}$	$I_{OH}=-0.1mA$	$0.8V_{DD}$	--	$V_{DD}$	V
	$V_{HL}$	$I_{OL}=0.1mA$	$V_{SS}$	--	$0.2V_{DD}$	
Current Consumption	$I_{DD}$	$V_{IN}=V_{DD}$	--	0.05	1	mA

## 2.5 Optical Characteristics Absolute maximum ratings

Item	Symbol	Rating	Unit
Operating temperature range	Top	-20~70	°C
Storage temperature range	Tst	-30~80	°C

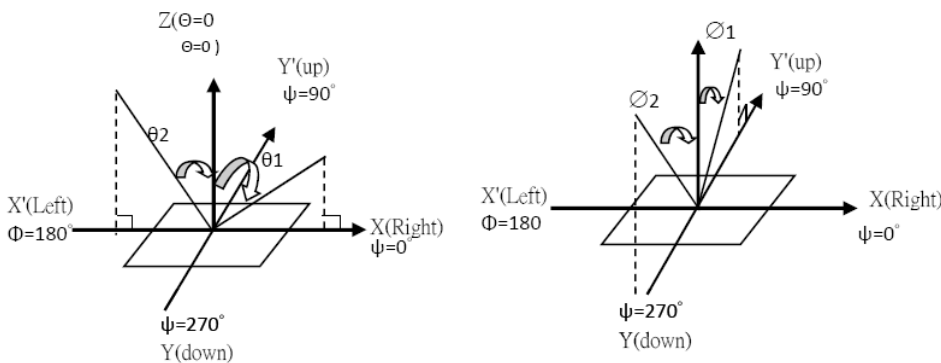


## 2.6. Optical Characteristics

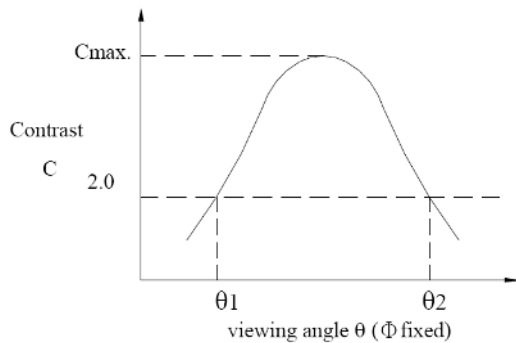
1/16 duty, 1/5 bias,  $V_{op}=4.4V$ ,  $T_a=25^\circ C$

Item	Symbol	Conditions	Min.	Typ.	Max	Reference
Driving voltage	$V_{op}$		4.1	4.4	4.7	
Viewing angle	$\theta_1$ 、 $\theta_2$	$C \geq 2.0, \varnothing=0^\circ C$	$30^\circ$	-		Notes 1 & 2
Contrast	$C$	$\theta=5^\circ, \varnothing=0^\circ$	3.0	-	-	Note 3
Response time(rise)	$t_{on}$	$\theta=5^\circ, \varnothing=0^\circ$	-	80	160ms	Note 4
Response time(fall)	$t_{off}$	$\theta=5^\circ, \varnothing=0^\circ$	-	100	200ms	Note 4

Note 1: Definition of angles  $\theta$  and  $\varnothing$

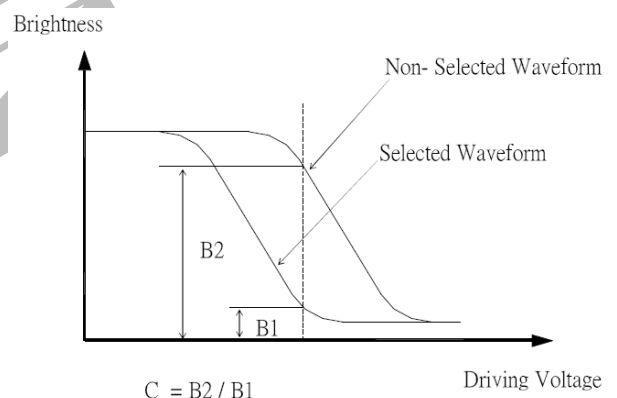


Note 2: Definition of viewing angles  $\theta_1$  and  $\theta_2$

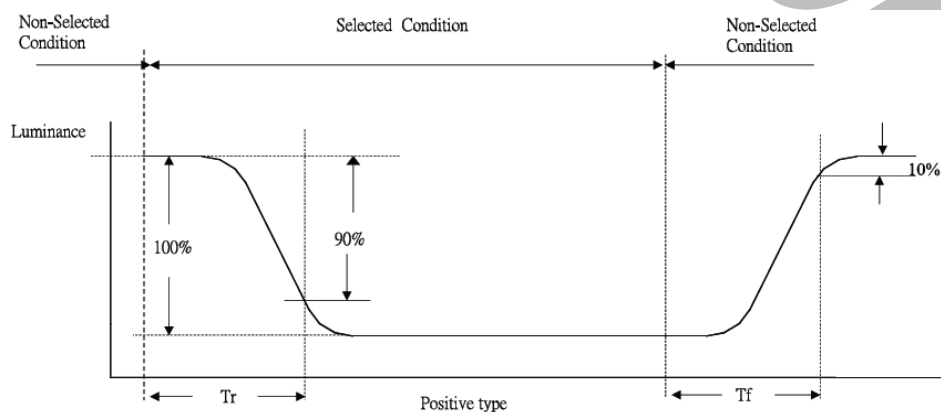


Note : Optimum viewing angle with the naked eye and viewing angle  $\theta$  at  $C_{max}$ . Above are not always the same

Note 3: Definition of contrast  $C$



Note 4: Definition of response time





## 2.7 LED Back-light Characteristics

### 2.7.1 Electrical / optical specifications

Ta = 25°C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Forward voltage	$V_f$	If=20mA, Yellow Green	3.7	4.0	4.3	V
*Luminous Intensity	$I_v$	If=20mA, Yellow Green	40	60	--	cd/m <sup>2</sup>
Peak Emission Wavelength	$\lambda_P$	If=20mA, Yellow Green	566	--	575	nm
Uniformity	Avg	If=20mA, Yellow Green	70	--	--	%
Reverse Current	$I_R$	VR=5V, Yellow Green	--	10	--	uA

Note: \* Measured at the bare LED back-light unit.

### 2.7.2 LED Maximum Operating Range

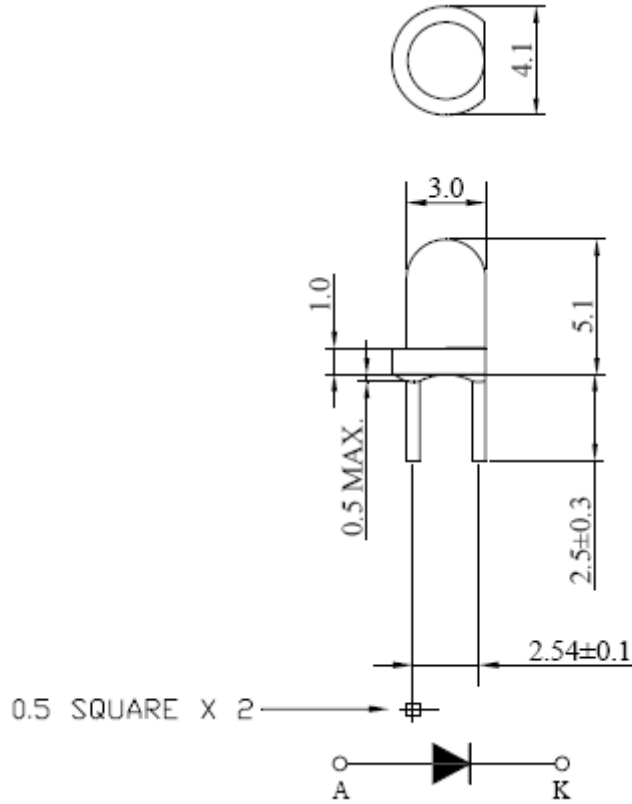
Item	Symbol	WHITE	Unit
Power Dissipation	$P_{AD}$	126	mW
Forward Current	$I_F$	30	mA
Reverse Voltage	$V_R$	5	V



### 3. LED Description

#### 3.1 D1 Description

This yellow green lamp is made with GaAs chip and water clear epoxy resin.



Notes:

1. All dimensions are in mm.
2. Tolerance is  $\pm 0.25$ mm unless otherwise noted..

#### Description

LED Chip		Lens Color
Material	Emitting Color	
GaAs	Yellow Green	Water clear





### 3.1.1 Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Rating	Unit
Power Dissipation	PD	72	mW
Reverse Voltage	VR	5	V
D.C. Forward Current	If	30	mA
Reverse (Leakage) Current	Ir	100	μA
Peak Current(1/10Duty Cycle,0.1ms Pulse Width.)	If(Peak)	100	mA
Operating Temperature Range	Topr.	-25 to +85	°C
Storage Temperature Range	Tstg.	-40 to +100	°C
Soldering Temperature(1.6mm from body)	Tsol	Dip Soldering : 260°C for 5 sec. Hand Soldering : 350°C for 3 sec.	

### 3.1.2 Electrical and Optical Characteristics:

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Luminous Intensity	IV	If=20mA	20.0	45.0		mcd
Forward Voltage	Vf	If=20mA		2.1	2.4	V
Peak Wavelength	λp	If=20mA	565	572	575	nm
Dominant Wavelength	λd	If=20mA		570		nm
Reverse (Leakage) Current	Ir	Vr=5V			100	μA
Viewing Angle	2θ1/2	If=20mA		35		deg
Spectrum Line Halfwidth	Δλ	If=20mA		30		nm

Notes: 1. Tolerance of Luminous Intensity is ±15%

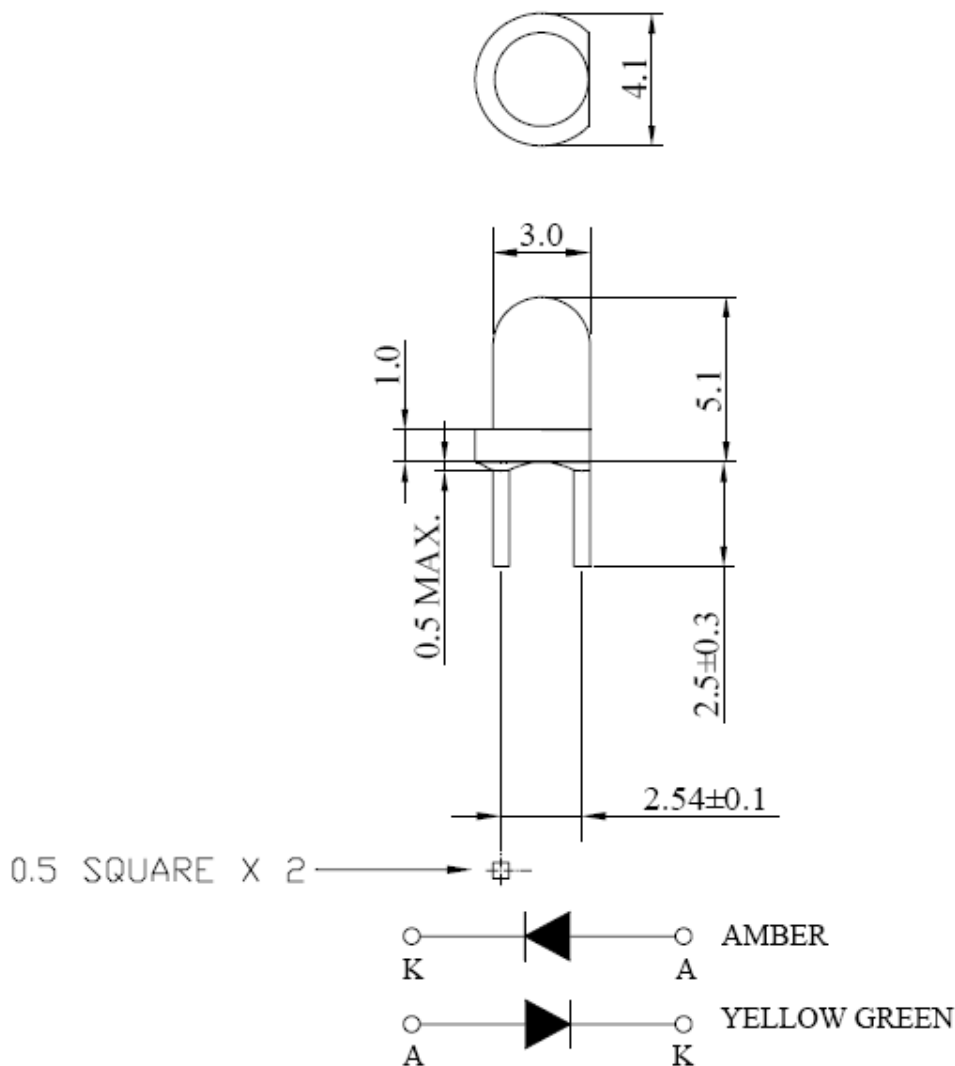
2. Tolerance of Forward Voltage is ±0.1V

3. Tolerance of Dominant Wavelength is ±1nm



### 3.2 D2 Description

This amber and yellow green bi-color lamp is made with GaAsP/GaP amber chip, GaAs yellow green chip and water clear epoxy resin.



#### Notes:

1. All dimensions are in mm.
2. The specifications, characteristics and technical data described in the datasheet are subject to change without notice.
3. Tolerance is ±0.25mm unless otherwise noted.

### Description

LED Chip		Lens Color
Material	Emitting Color	
GaAsP/GaP	Amber	Water clear
GaAs	Yellow Green	



### 3.2.1 Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Rating	Unit
Power Dissipation	PD	78	mW
Reverse Voltage	VR	5	V
D.C. Forward Current	If	30	mA
Reverse (Leakage) Current	Ir	100	μA
Peak Current(1/10Duty Cycle,0.1ms Pulse Width.)	If(Peak)	100	mA
Operating Temperature Range	Topr.	-25 to +85	°C
Storage Temperature Range	Tstg.	-40 to +100	°C
Soldering Temperature(1.6mm from body)	Tsol	Dip Soldering : 260°C for 5 sec. Hand Soldering : 350°C for 3 sec.	

### 3.2.2 Electrical and Optical Characteristics:

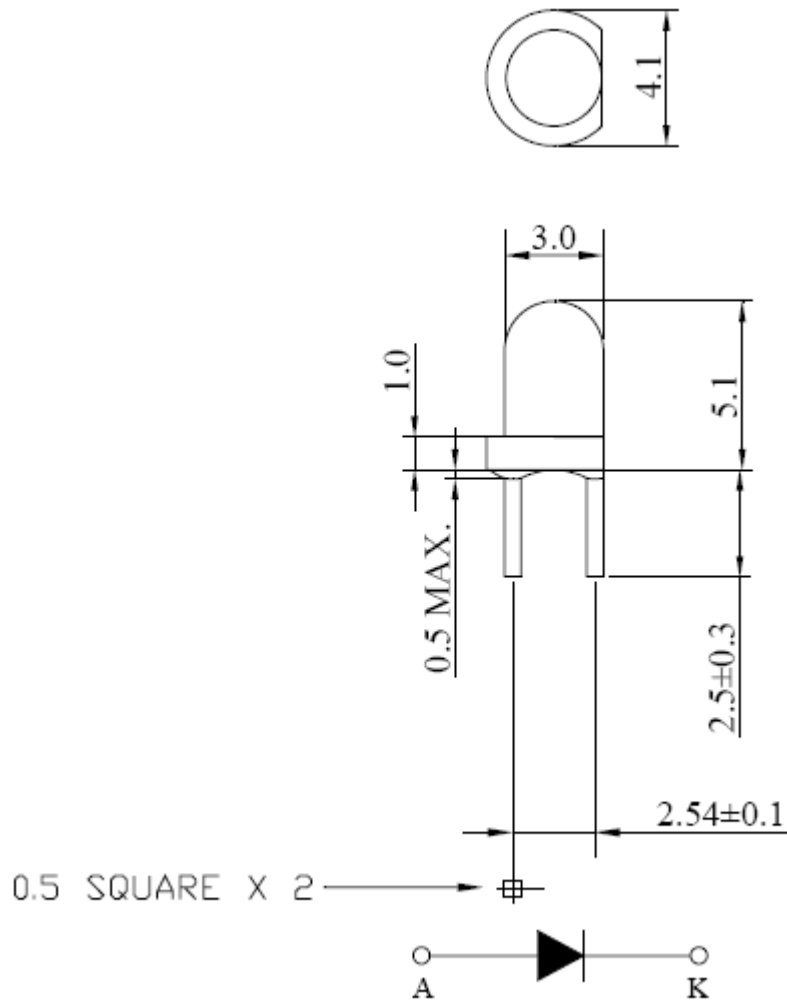
Parameter	Symbol	Condition	Color	Min.	Typ.	Max.	Unit
Luminous Intensity	Iv	If=20mA	A	4.2	10.0		mcd
			2	4.2	10.0		
Forward Voltage	Vf	If=20mA	A		2.1	2.6	V
			2		2.1	2.6	
Peak Wavelength	λp	If=20mA	A		610		nm
			2		572		
Dominant Wavelength	λd	If=20mA	A		605		nm
			2	565	570	575	
Reverse (Leakage) Current	Ir	Vr=5V				50	μA
Viewing Angle	2θ1/2	If=20mA			50		deg
Spectrum Line Halfwidth	Δλ	If=20mA	A		35		nm
			2		30		

NOTE: 1.Tolerance of Luminous Intensity is ±15%.  
2.Tolerance of Forward Voltage is ±0.1V.  
3.Tolerance of Dominant Wavelength is ±1nm.



### 3.3 D3 Description

This true green lamp is made with InGaN chip and water clear epoxy resin.



#### Notes:

1. All dimensions are in mm.
2. Tolerance is  $\pm 0.25$ mm unless otherwise noted.

### Description

LED Chip		Lens Color
Material	Emitting Color	
InGaN	True green	Water clear



### 3.3.1 Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Rating	Unit
Power Dissipation	PD	120	mW
Reverse Voltage	VR	5	V
D.C. Forward Current	If	30	mA
Reverse (Leakage) Current	Ir	50	μA
Peak Current(1/10Duty Cycle,0.1ms Pulse Width.)	If(Peak)	100	mA
Operating Temperature Range	Topr.	-25 to +85	°C
Storage Temperature Range	Tstg.	-40 to +100	°C
Soldering Temperature(1.6mm from body)	Tsol.	Dip Soldering : 260°C for 5 sec. Hand Soldering : 350°C for 3 sec.	
Electrostatic discharge	ESD	1000	V

### 3.3.2 Electrical and Optical Characteristics:

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Luminous Intensity	IV	If=20mA	2750	5000		mcd
Forward Voltage	Vf	If=20mA		3.2	4.0	V
Dominant Wavelength	λd	If=20mA		525		nm
Reverse (Leakage) Current	Ir	Vr=5V			50	μA
Viewing Angle	2θ1/2	If=20mA		30		deg
Spectrum Line Halfwidth	Δλ	If=20mA		35		nm

Notes:1. The datas tested by IS tester.

2. Customer's special requirements are also welcome.



## 4. RELIABILITY

NO.	ITEM	CONDITION		STANDARD	NOTE
1	High Temp. Storage	80°C	120 hrs	Appearance Without defect	
2	Low Temp. Storage	-30°C	120 hrs	Appearance Without defect	
3	High Temp. & High Humi. Storage	40°C 90% RH	120 hrs	Appearance Without defect	
4	High Temp. Operating Display	70°C	120 hrs	Appearance Without defect	
5	Low Temp. Operating Display	-20°C	120 hrs	Appearance Without defect	
6	Thermal Shock	-20°C, 30min. → 70°C, 30min. ↑ (1cycle)		Appearance Without defect	10 cycles

\*\* Dissipation current, contrast and display functions

\*\* Polarizing filter deterioration, other appearance defects

\*\* The function test shall be conducted after 4hours storage at the normal temperature and humidity after remove from the test chamber.



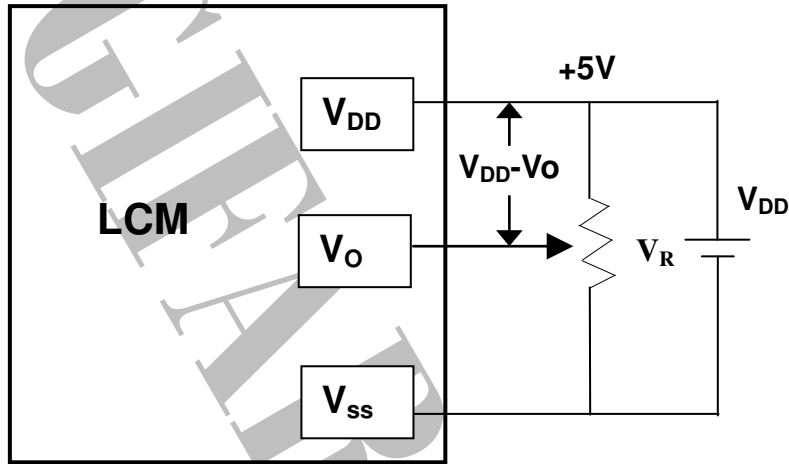
## 5. OPERATING INSTRUCTIONS

### 5.1 Input signal Function

NO.	Symbol	Function																																				
1	VSS	Ground (0V)																																				
2	VDD	Power supply for Logic circuit																																				
3	VO	Power Supply for Driving the LCD																																				
4	RSNC	Data / Instruction select;NC																																				
5	NC	NC																																				
6	NC	NC																																				
7-14	DB0-DB7	<table border="1"> <thead> <tr> <th>MODE</th> <th>DB7</th> <th>DB6</th> <th>DB5</th> <th>DB4</th> <th>DB3</th> <th>DB2</th> <th>DB1</th> <th>DB0</th> </tr> </thead> <tbody> <tr> <td>S8</td> <td>H</td> <td>L</td> <td>CS1</td> <td>SDA</td> <td>SCL</td> <td>CS0</td> <td>--</td> <td>--</td> </tr> <tr> <td>S9</td> <td>H</td> <td>H</td> <td>CS1</td> <td>SDA</td> <td>SCL</td> <td>CS0</td> <td>--</td> <td>--</td> </tr> <tr> <td>I<sup>2</sup>C</td> <td>L</td> <td>H</td> <td>ID1</td> <td>ID0</td> <td>--</td> <td>--</td> <td>SCL</td> <td>SDA</td> </tr> </tbody> </table>	MODE	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	S8	H	L	CS1	SDA	SCL	CS0	--	--	S9	H	H	CS1	SDA	SCL	CS0	--	--	I <sup>2</sup> C	L	H	ID1	ID0	--	--	SCL	SDA
		MODE	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0																												
		S8	H	L	CS1	SDA	SCL	CS0	--	--																												
		S9	H	H	CS1	SDA	SCL	CS0	--	--																												
I <sup>2</sup> C	L	H	ID1	ID0	--	--	SCL	SDA																														
<p>In SPI-S8/S9 mode, SPI-S8/S9 slave is selected when DB[5] (CS1)="1" and DB[2] (CS0)="0".            In I<sup>2</sup>C mode, I<sup>2</sup>C slave is selected when DB[5]=ID1 (head byte) and DB[4]=ID0 (head byte).</p>																																						
15	LED+	Power supply for LED backlight (+)																																				
16	LED-	Power supply for LED backlight (-)																																				
17	KAP1	S1 and S2 Key Scan																																				
18	KAP2	S2 and S3 Key Scan																																				
19	KAP3	S3 and S4 Key Scan																																				
20	KAP4	S1 and S4 Key Scan																																				
21	CT_GRN	LED D2 Yellow Green (-) OR Amber (+)																																				
22	NC																																					
23	HDD_LED	LED D1 Yellow Green (-)																																				
24	CTR_YELL	LED D2 Yellow Green (+) OR Amber (-)																																				



## 5.2 Voltage Generator Circuit



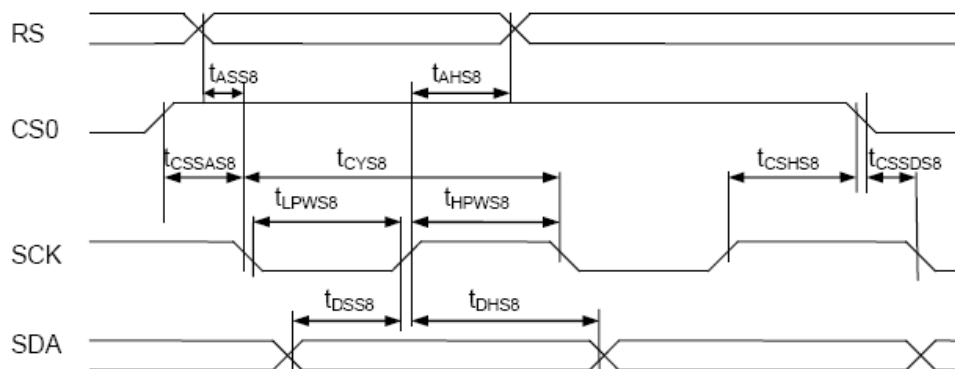
$V_{DD}-V_o$  : LCD Driving Voltage

$V_R$  : 10K~20K





### 5.3 Timing Diagram



Serial Bus Timing Characteristics (for S8)

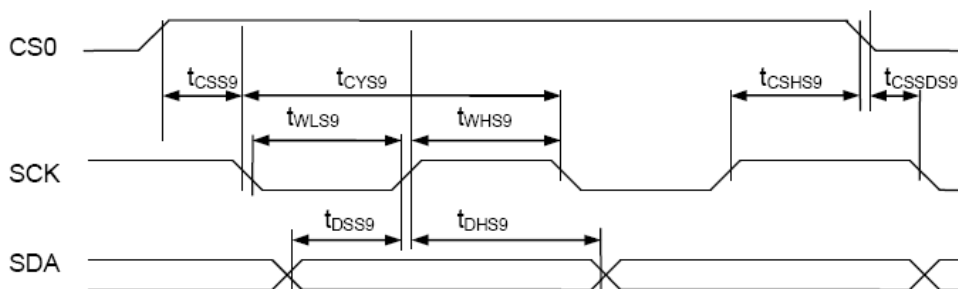
$T_A = 25^\circ\text{C}$ ,  $V_{CC} = 2.7\text{V} \sim 4.5\text{V}$

Symbol	Signal	Description	Min.	Typ.	Max.	Unit
$t_{ASS8}$	RS	Address setup time	0		–	nS
$t_{AHS8}$		Address hold time	20		–	nS
$t_{CYS8}$	SCK	System cycle time	140		–	nS
$t_{LPWS8}$		Low pulse width	65		–	nS
$t_{HPWS8}$		High pulse width	65		–	nS
$t_{DSS8}$	SDA	Data setup time	30		–	nS
$t_{DHS8}$		Data hold time	20		–	nS
$t_{CSSAS8}$	CS1, CS0	Chip select setup time	10			nS
$t_{CSSDS8}$			20			
$t_{CSHS8}$			10			

$T_A = 25^\circ\text{C}$ ,  $V_{CC} = 4.5\text{V} \sim 5\text{V}$

Symbol	Signal	Description	Min.	Typ.	Max.	Unit
$t_{ASS8}$	RS	Address setup time	0		–	nS
$t_{AHS8}$		Address hold time	20		–	nS
$t_{CYS8}$	SCK	System cycle time	140		–	nS
$t_{LPWS8}$		Low pulse width	65		–	nS
$t_{HPWS8}$		High pulse width	65		–	nS
$t_{DSS8}$	SDA	Data setup time	30		–	nS
$t_{DHS8}$		Data hold time	20		–	nS
$t_{CSSAS8}$	CS1, CS0	Chip select setup time	10			nS
$t_{CSSDS8}$			20			
$t_{CSHS8}$			10			





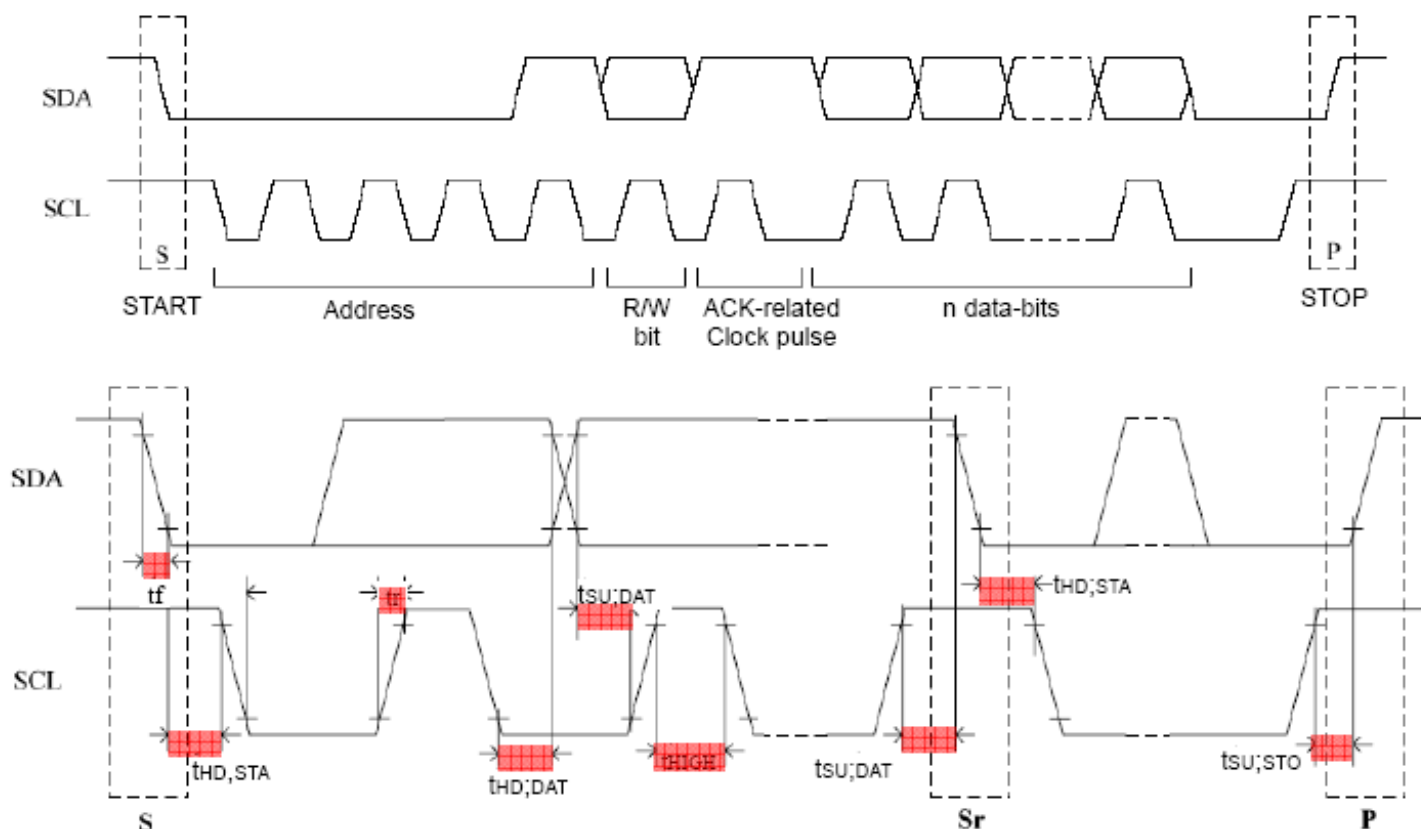
Serial Bus Timing Characteristics (for S9)

$T_A = 25^\circ\text{C}$ ,  $V_{CC} = 2.7\text{V} \sim 4.5\text{V}$

Symbol	Signal	Description	Min.	Typ.	Max.	Unit
$t_{ASS8}$	RS	Address setup time	0		-	nS
$t_{AHS8}$		Address hold time	20		-	nS
$t_{CYS8}$	SCK	System cycle time	140		-	nS
$t_{LPWS8}$		Low pulse width	65		-	nS
$t_{HPWS8}$		High pulse width	65		-	nS
$t_{DSS8}$	SDA	Data setup time	30		-	nS
$t_{DHS8}$		Data hold time	20		-	nS
$t_{CSSAS8}$ $t_{CSSDS8}$ $t_{CSHS8}$	CS1, CS0	Chip select setup time	10 20 10			nS

$T_A = 25^\circ\text{C}$ ,  $V_{CC} = 4.5\text{V} \sim 5\text{V}$

Symbol	Signal	Description	Min.	Typ.	Max.	Unit
$t_{ASS8}$	RS	Address setup time	0		-	nS
$t_{AHS8}$		Address hold time	20		-	nS
$t_{CYS8}$	SCK	System cycle time	140		-	nS
$t_{LPWS8}$		Low pulse width	65		-	nS
$t_{HPWS8}$		High pulse width	65		-	nS
$t_{DSS8}$	SDA	Data setup time	30		-	nS
$t_{DHS8}$		Data hold time	20		-	nS
$t_{CSSAS8}$ $t_{CSSDS8}$ $t_{CSHS8}$	CS1, CS0	Chip select setup time	10 20 10			nS



Serial Bus Timing Characteristics (for I<sup>2</sup>C MCU)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$T_A = 25^\circ\text{C}$ , $V_{CC} = 2.7\text{V} \sim 4.5\text{V}$						
fSCL	SCL Clock Frequency		0	–	400	kHz
tSU, tSTA	Set-up time for START condition		600	–	–	nS
tHD, tSTA	Hold time for START condition		600	–	–	nS
tLOW	Low period of the SCL clock		1300	–	–	nS
tHIGH	High period of the SCL clock		600	–	–	nS
tSU, tDAT	Data setup time		100	–	–	nS
tHD, tDAT	Data hold time		–	–	900	nS
tr	Rise time of both SDA and SCL signals		T.B.D	–	300	nS
tf	Fall time of both SDA and SCL signals		T.B.D	–	300	nS
tSU, tSTO	Setup time for STOP condition		600	–	–	nS
$T_A = 25^\circ\text{C}$ , $V_{CC} = 24.5\text{V} \sim 5.5\text{V}$						
fSCL	SCL Clock Frequency		0	–	400	kHz
tSU, tSTA	Set-up time for START condition		600	–	–	nS
tHD, tSTA	Hold time for START condition		600	–	–	nS
tLOW	Low period of the SCL clock		1300	–	–	nS
tHIGH	High period of the SCL clock		600	–	–	nS
tSU, tDAT	Data setup time		100	–	–	nS
tHD, tDAT	Data hold time		–	–	900	nS
tr	Rise time of both SDA and SCL signals		T.B.D	–	300	nS
tf	Fall time of both SDA and SCL signals		T.B.D	–	300	nS
tSU, tSTO	Setup time for STOP condition		600	–	–	nS



## 5.4.Display Command

### COMMAND TABLE

The following is a list of host commands supported by UCi7066

**R/S**: 0: Control, 1: Data      **W/R**: 0: Write Cycle, 1: Read Cycle      **D7-D0**: -: Don't Care

#	Command	RS	R/W	D7	D6	D5	D4	D3	D2	D1	D0	Action
1	Clear Display	0	0	0	0	0	0	0	0	0	1	Clear the screen
2	Return Home	0	0	0	0	0	0	0	0	1	-	Move cursor to HOME
3	Set Entry Mode	0	0	0	0	0	0	0	1	I/D	S	I/D: Left / Right S: Shift OFF/ON
4	Display ON/OFF	0	0	0	0	0	0	1	D	C	B	D: Display OFF / ON C: Cursor OFF / ON B: Blink OFF / ON
5	Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	-	-	S/C: Screen / Cursor R/L Right / Left
6	Set Function	0	0	0	0	1	DL	N	F	-	-	DL: 4-bit / 8-bit, N: 1-line / 2-line F: 5x8 / 5x11
7	Set CGRAM address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	
8	Set DDRAM address	0	0	1	AC12	AC11	AC10	AC9	AC8	AC7	AC6	
9	Read Busy Flag and address	0	1	BF	AC19	AC18	AC17	AC16	AC15	AC14	AC13	
10	Write data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data to RAM
11	Read data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from RAM
<b>For S8/S9 Mode</b>												
12	Status Read	1	1	0	0	0	0	0	0	0	0	Read status
		0	1	BF	AC19	AC18	AC17	AC16	AC15	AC14	AC13	

**Note:**

Ensure that UCi7066 is not in the BUSY state (BF = 0) before sending an instruction from the MPU to the UCi7066. If an instruction is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself.

NOT FOR SALE





## 6. Character Pattern

### APPENDIX

Correspondence between Character Codes and Character Patterns (ROM Code: 0A)

Upper 4 bits Lower 4 bits	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111	
0000				0	1	2	3	4	5	6	7	8	9	A	B	C	
0001		!	"	#	\$	%	&	'	(	)	*	+	,	-	.	/	
0010		1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	
0011		a	b	c	d	e	f	g	h	i	j	k	l	m	n	o	
0100		p	q	r	s	t	u	v	w	x	y	z	[	\	]	~	
0101		0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	
0110		F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T	
0111		U	V	W	X	Y	Z	[	\	]	~	0	1	2	3	4	
1000		5	6	7	8	9	A	B	C	D	E	F	G	H	I	J	
1001		K	L	M	N	O	P	Q	R	S	T	U	V	W	X	Y	
1010		Z	[	\	]	~	0	1	2	3	4	5	6	7	8	9	
1011		A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	
1100		P	Q	R	S	T	U	V	W	X	Y	Z	[	\	]	~	
1101		0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	
1110		F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T	
1111		U	V	W	X	Y	Z	[	\	]	~	0	1	2	3	4	5



## 7. NOTES

### ▪ Safety

- If the LCD panel breaks, be careful not to get the liquid crystal in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and plenty of water.

### Handling

- Avoid static electricity as this can damage the CMOS LSI.
- The LCD panel is plate glass; do not hit or crush it.
- Do not remove the panel or frame from the module.
- The polarizing plate of the display is very fragile; handle it very carefully

### Mounting and Design

- Mount the module by using the specified mounting part and holes.
- To protect the module from external pressure, leave a small gap by placing transparent plates (e.g. acrylic or glass ) on the display surface, frame, and polarizing plate
- Design the system so that no input signal is given unless the power-supply voltage is applied.
- Keep the module dry. Avoid condensation, otherwise the transparent electrodes may break.

### Storage

- Store the module in a dark place where the temperature is  $25^{\circ}\text{C} \pm 10^{\circ}\text{C}$  and the humidity below 65% RH.
- Do not store the module near organic solvents or corrosive gases.
- Do not crush, shake, or jolt the module (including accessories).

### Cleaning

- Do not wipe the polarizing plate with a dry cloth, as it may scratch the surface.
- Wipe the module gently with soft cloth soaked with a petroleum benzene.
- Do not use ketonic solvents (ketone and acetone) or aromatic solvents (toluene and xylene), as they may damage the polarizing plate.

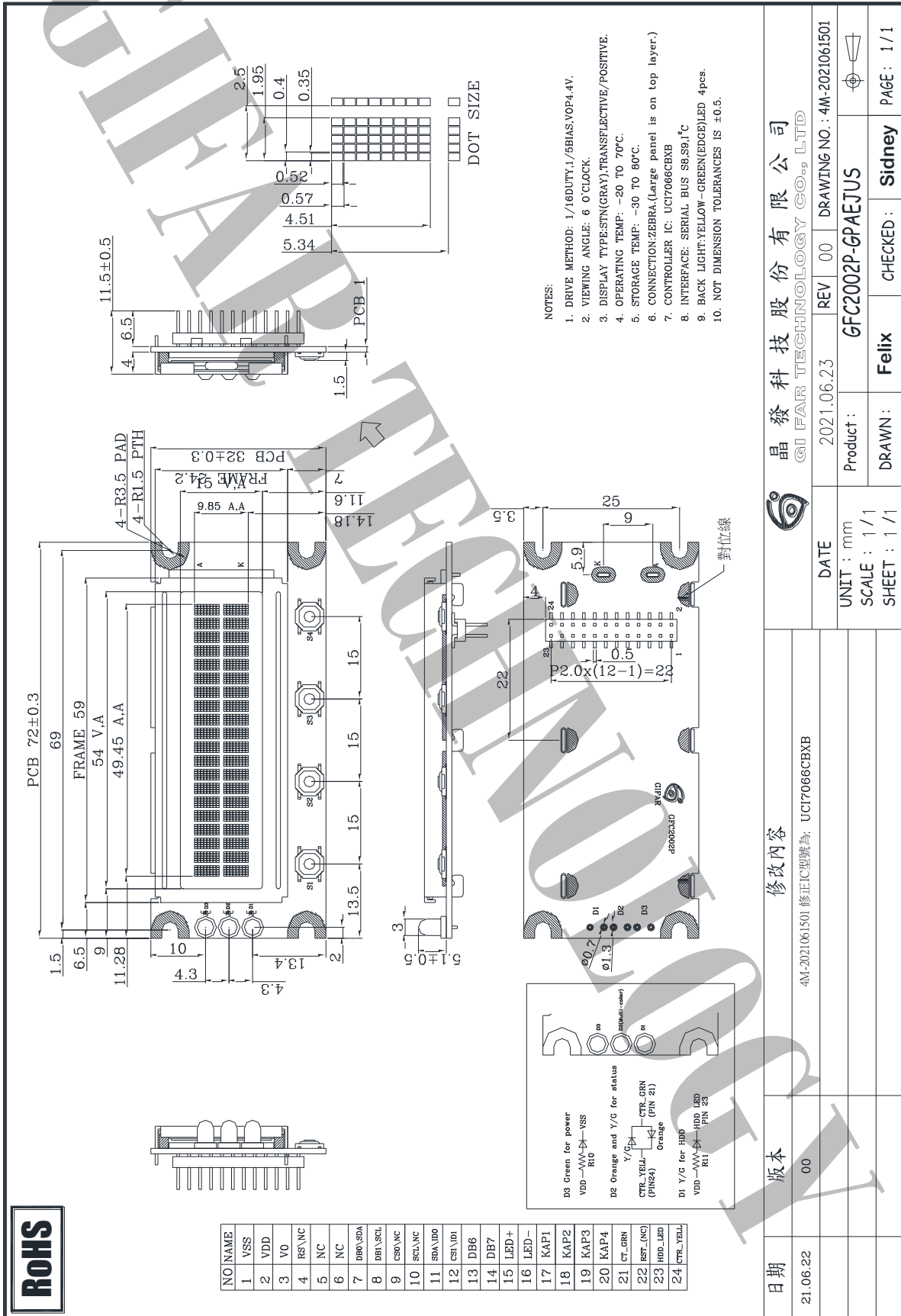
## 8. OPERATION PRECAUTIONS

Any changes that need to be made in this specification or any problems arising from it will be dealt with quickly by discussion between both companies.

Quality warranty period: Within one year after shipment date (excluding abnormal usage way and abnormal environments.)



## 9. LCM Dimension







## 10. PACKAGE INFORMATION

1	1 Tray	:	24 pcs (modules)
2	1 stack	:	6 tray +1 Cover tray
3	1 Carton	:	(1 Cover tray + 6 tray )x 3 stack
4	Total pcs	:	1 Carton (24pcs*6tray * 3 stack) = 432 pcs
5	Carton size = NO. 17	:	495*315*435mm
7	Net weight	:	7.6 KG
8	Gross weight	:	11.5 KG

\*\* Illustration

- 1 Tray= 24 pcs (modules)



- 1 stack=6 tray+1 Cover tray

\*\*Each layer of tray should be staggered stacked



- 1 Carton = 3 stack, Total pcs = 432 pcs





出貨檢驗標準書  
Shipping inspection standard

核准 Approved by	審核 Checked by	作成 Made by
ANDY	JACKY	RUBY

**1.目的 Purpose :**

規範出貨產品之檢驗項目及判斷標準，確保產品出貨能滿足客戶要求。

Standardize the inspection items and judgment standards to ensure the products that shipped out can meet customer's requirements.

**2.範圍 Area :**

適用於出廠之所有產品。

Applicable to all products shipped from the factory.

**3.名詞解釋 Explanation of terms :**

3-1 主要缺陷：亦會造成功能缺失或嚴重外觀缺陷。

Major Defects: It also causes loss of function or serious appearance defects.

3-2 次要缺陷：稍有缺陷但不影響客戶使用。

Minor defect: Slightly defective but does not affect customer use.

**4.檢驗體制 Inspection system :**

4-1 抽樣計劃：依 ANSI/ASQ Z1.4 一般檢驗水準 II 之 正常檢驗一次抽驗方案。

Sampling plan: According to ANSI/ASQ Z1.4 general inspection level II the normal inspection one-time sampling plan.

4-2 允收水準 Acceptable Level : (AQL)

主要缺陷 Major defect : 0.4 %

次要缺陷 Minor defect : 0.65 %

**5.檢驗條件 Inspection conditions :**

5-1 使用相關之檢測儀器及測試、量測工具。

Use relevant testing instrument, testing and measuring tools .

5-2 環境要求：其條件需控制在常溫下  $23^{\circ}\text{C}\pm 3^{\circ}\text{C}$  及溼度 70%RH 以下。

Environmental requirements: The conditions should be controlled at room temperature  $23^{\circ}\text{C}\pm 3^{\circ}\text{C}$  and humidity below 70%RH.

5-3 外觀檢驗：須在  $380\pm 20\%$  LUX 的白色日光燈下，其目視距離需於產品離  $30\pm 5$  cm 檢驗。

Appearance inspection: Under the white fluorescent lamp of  $380\pm 20\%$  LUX , the visual distance shall be checked above the product  $30 \pm 5$  cm.

5-4 電性測試 Electrical Testing :

5-4-1 有背光之產品需關燈並在  $5\sim 300\text{Lux}\pm 3\%$  下檢驗。

The products with backlight should be tested at  $5\sim 300\pm 3\%$  Lux.

5-4-2 無背光之產品需開燈並在  $60\sim 300\text{Lux}\pm 3\%$  白色日光燈下檢驗。

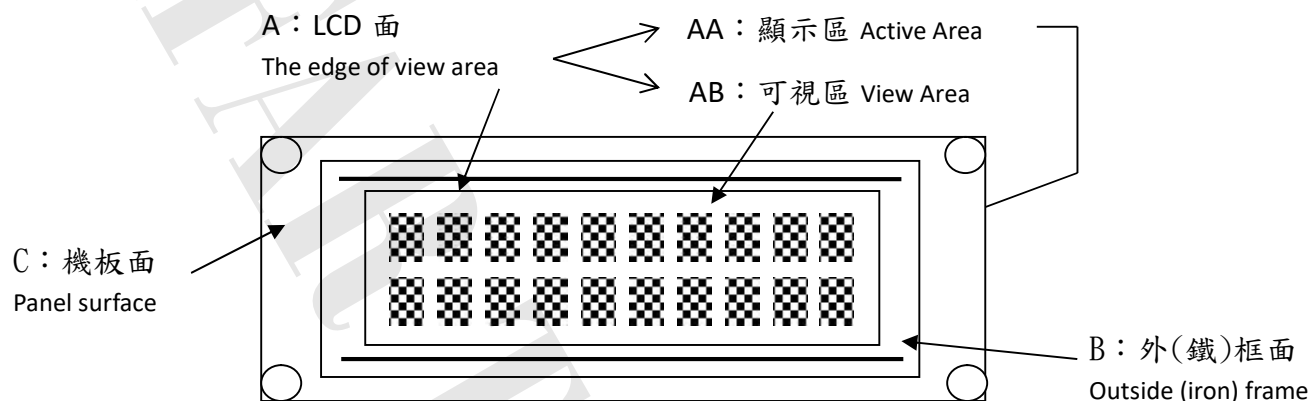
Products without backlight need to be turned on and tested under  $60\sim 300 \pm 3\%$  LUX white fluorescent lamps .

5-5 檢查視角依產品視角方向。

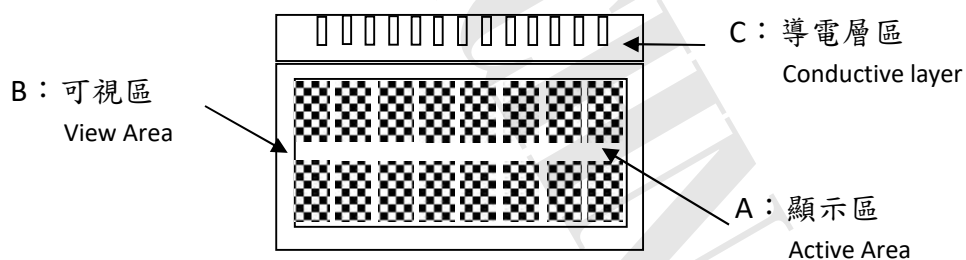
Check the viewing angle according to the product viewing angle.

5-6 其不良現象檢視區域 Bad phenomenon View area

5-6-1 適用種類 Applicable category : COB、TFT

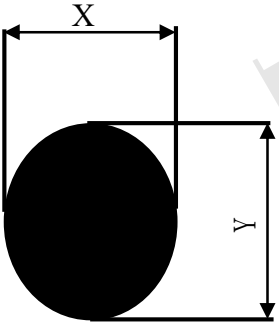
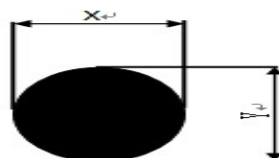
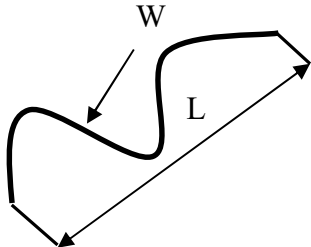


5-6-2 適用種類 Applicable category : COG、TAB、TN



6. 檢驗標準 Inspection standards

COB

種類 Category		COB																			
編號 No.	檢驗項目 Item	檢驗內容及判定標準 Inspection Content & Standard	區域 Zone	類別 Category	缺陷等級 Level																
1	點類(一) Dot (1)	黑點、刺傷...等圓狀 Black dot、Stab...and other round shape  $\varphi = \frac{(X + Y)}{2}$ 	兩點距離須超過 5 mm Two points have to be $\geq 5$ mm  <table border="1"> <thead> <tr> <th><math>\varphi</math> (mm)</th> <th>允收數 Acceptance Qty</th> </tr> </thead> <tbody> <tr> <td><math>\phi \leq 0.1</math></td> <td>無視 Ignore</td> </tr> <tr> <td><math>0.1 &lt; \phi \leq 0.25</math></td> <td>3</td> </tr> <tr> <td><math>0.25 &lt; \phi \leq 0.3</math></td> <td>1</td> </tr> <tr> <td><math>\phi &gt; 0.3</math></td> <td>0</td> </tr> </tbody> </table>	$\varphi$ (mm)	允收數 Acceptance Qty	$\phi \leq 0.1$	無視 Ignore	$0.1 < \phi \leq 0.25$	3	$0.25 < \phi \leq 0.3$	1	$\phi > 0.3$	0	A	外觀 Appearance	次要 Minor AQL0.65%					
		$\varphi$ (mm)	允收數 Acceptance Qty																		
$\phi \leq 0.1$	無視 Ignore																				
$0.1 < \phi \leq 0.25$	3																				
$0.25 < \phi \leq 0.3$	1																				
$\phi > 0.3$	0																				
其點位於側邊 無視 The dot is located on the side(B area)-OK  <table border="1"> <thead> <tr> <th><math>\varphi</math> (mm)</th> <th>允收數 Acceptance Qty</th> </tr> </thead> <tbody> <tr> <td><math>\phi \leq 0.3</math></td> <td>無視 Ignore</td> </tr> <tr> <td><math>0.3 &lt; \phi \leq 0.5</math></td> <td>3</td> </tr> <tr> <td><math>0.5 &lt; \phi \leq 1</math></td> <td>1</td> </tr> <tr> <td><math>\phi &gt; 1</math></td> <td>0</td> </tr> </tbody> </table>	$\varphi$ (mm)	允收數 Acceptance Qty	$\phi \leq 0.3$	無視 Ignore	$0.3 < \phi \leq 0.5$	3	$0.5 < \phi \leq 1$	1	$\phi > 1$	0	B	外觀 Appearance	次要 Minor AQL0.65%								
$\varphi$ (mm)	允收數 Acceptance Qty																				
$\phi \leq 0.3$	無視 Ignore																				
$0.3 < \phi \leq 0.5$	3																				
$0.5 < \phi \leq 1$	1																				
$\phi > 1$	0																				
2	點類(二) Dot (2)	氣泡、凹凸點 Bubble、Uneven dots  $\varphi = \frac{(X + Y)}{2}$ 	兩點距離須超過 5 mm Two points to be $\geq 5$ mm  <table border="1"> <thead> <tr> <th><math>\varphi</math> (mm)</th> <th>允收數 Acceptance Qty</th> </tr> </thead> <tbody> <tr> <td><math>\phi \leq 0.2</math></td> <td>無視 Ignore</td> </tr> <tr> <td><math>0.2 &lt; \phi \leq 0.5</math></td> <td>2</td> </tr> <tr> <td><math>\phi &gt; 0.5</math></td> <td>0</td> </tr> </tbody> </table>	$\varphi$ (mm)	允收數 Acceptance Qty	$\phi \leq 0.2$	無視 Ignore	$0.2 < \phi \leq 0.5$	2	$\phi > 0.5$	0	A	外觀 Appearance	次要 Minor AQL0.65%							
$\varphi$ (mm)	允收數 Acceptance Qty																				
$\phi \leq 0.2$	無視 Ignore																				
$0.2 < \phi \leq 0.5$	2																				
$\phi > 0.5$	0																				
3	線類 Line	刮傷、毛屑...等線狀 Scratch、Fiber.. and other linear shape.  	<table border="1"> <thead> <tr> <th>L (mm)</th> <th>W (mm)</th> <th>允收數 Acceptance Qty</th> </tr> </thead> <tbody> <tr> <td>--</td> <td><math>W \leq 0.02</math></td> <td>無視 Ignore</td> </tr> <tr> <td><math>L \leq 5</math></td> <td><math>W \leq 0.03</math></td> <td>3</td> </tr> <tr> <td><math>L \leq 3</math></td> <td><math>W \leq 0.05</math></td> <td>2</td> </tr> <tr> <td><math>L &gt; 5</math></td> <td><math>W &gt; 0.05</math></td> <td>0</td> </tr> </tbody> </table>	L (mm)	W (mm)	允收數 Acceptance Qty	--	$W \leq 0.02$	無視 Ignore	$L \leq 5$	$W \leq 0.03$	3	$L \leq 3$	$W \leq 0.05$	2	$L > 5$	$W > 0.05$	0	A	外觀 Appearance	次要 Minor AQL0.65%
L (mm)	W (mm)	允收數 Acceptance Qty																			
--	$W \leq 0.02$	無視 Ignore																			
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$L \leq 3$	$W \leq 0.05$	2																			
$L > 5$	$W > 0.05$	0																			
4	底色 Background color	同批供貨不能有明顯色差 No obvious color difference allowed in same shipment. (必要時與客端制定限度樣) (According to the gold samples if necessary)	A	外觀 Appearance	次要 Minor AQL0.65%																

5	零件位置 Parts position	與工程 BOM 上標示不符 拒收 Different with the BOM marked Reject	C	外觀 Appearance	主要 Major AQL 0.4%
6	板面潔淨 FPC/PCB's surface is clean	※ 焊接面上附著錫渣、珠 拒收 Solder side has tin slag, beads or particle Reject ※ 焊接面上附著於過多錫油 拒收 Solder side has too much tin oil Reject	C	外觀 Appearance	次要 Minor AQL0.65%
7	點、線類 (三) Dot、Line (3)	※ 於全黑、白畫面下看見之區塊狀或線狀不良 拒收 There is a block or linear in the view area under the screen is whole black or white. Reject ※ 但依 2% ND Filter 遮蓋無視 允收 But after inspecting by 2% ND Filter without seeing block or linear, it is confirmed Acceptance	AA	電訊 Electronics	次要 Minor AQL0.65%
8	點、線類 (四) Dot、Line (4)	畫面中顯示出現黑、白、亮、異色點或線狀 There is a black, white, bright or other dot or lines showing in the view area. ※ 依編號 1、3 之判定標準 According to the inspection standard: No. 1 and 3.	AA	電訊 Electronics	次要 Minor AQL0.65%
9	缺字 Lack of characters	顯示時畫面缺少部份字元 拒收 Lacking part of characters in the view area. Reject	AA	電訊 Electronics	主要 Major AQL 0.4%
10	無動作 No reaction	顯示畫面一直處於起始畫面而無法進行切換 拒收 The display (view area) always shows in the initial screen and can't be switched to others. Reject	AA	電訊 Electronics	主要 Major AQL 0.4%
11	無畫面 No display	通電後，完全無任何畫面顯示 拒收 After connecting to the power, there is no display. Reject	AA	電訊 Electronics	主要 Major AQL 0.4%
12	斷線 Broken line	顯示畫面中少直、橫線 拒收 There is a lack of vertical or horizontal lines in the view area. Reject	AA	電訊 Electronics	主要 Major AQL 0.4%
13	CROSS TALK	顯示畫面時有局部之條紋或拖影 There are some stripes or shadow/smear showing in the view area. 拒收或與客端簽訂限度樣 Reject or inspect according to the golden sample	AA	電訊 Electronics	次要 Minor AQL0.65%

14	I CON	顯示畫面缺少部份顯示圖案 拒收 Lack of partial ICON in the view area. Reject	AA	電訊 Electronics	主要 Major AQL 0.4%
15	深淺不一 Color difference	顯示畫面的對比，比其他顯示深或淺並依電氣規格(VOP) 值判定 The contrast of display is obviously lighter or darker than others and according to the VOP value in the electronics specification. 拒收或與客端簽訂限度樣 Reject or inspect according to the golden sample	AA	電訊 Electronics	次要 Minor AQL0.65%
16	畫面異常 Abnormal screen	通電後畫面出現未定義之電訊不良現象 拒收 After connecting to the power, there is an undefined electronics appearance showing in the view area. Reject	AA	電訊 Electronics	主要 Major AQL 0.4%
17	背光色不均 Uneven color of backlight	※ 點亮後 LED 有明暗不均現象依其均勻度判定 拒收 After lighting LEDs have brightness and darkness uneven the determined according to its uniformity. Reject ※ 點亮後 LED 色澤不一致 拒收 LED color is inconsistent after lighting Reject	A	電訊 Electronics	次要 Minor AQL0.65%
18	亮度不足 Lack of brightness	波長、色座標、輝度與圖面標示定義不符 拒收 Wave length, chromatic coordinates, brightness don't correspond to the definition of the drawing. Reject	A	電訊 Electronics	次要 Minor AQL0.65%
19	尺寸量測 Size Measurement	未依圖面上標示 拒收 No correspond to the indication on the drawing. Reject	ALL	外觀 Appearance	主要 Major AQL 0.4%
20	其他 Other	如發現有上述未定義之不良則與客端簽訂限度樣 If there is another undefined defective situation. It will be listed as others. The inspection standard is according to the golden sample.	ALL	電訊 Electronics 外觀 Appearance	次要 Minor AQL0.65%