

ROHS

HALOGEN FREE

Hyperfast Rectifier, 30 A FRED Pt® G5



LINKS TO ADDITIONAL RESOURCES





PRIMARY CHARACTERISTICS						
I _{F(AV)}	30 A					
V_{R}	1200 V					
V _F at I _F at 125 °C	2.1 V					
t _{rr}	26 ns					
T _J max.	175 °C					
Package	TO-220AC 2L					
Circuit configuration	Single					

FEATURES

- Hyperfast and optimized Q_{rr}
- Best in class forward voltage drop and switching losses trade off



- 175 °C maximum operating junction temperature
- Polyimide passivation
- AEC-Q101 qualified, meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

Featuring a unique combination of low conduction and switching losses, this rectifier is the right choice for high frequency converters, both soft switched / resonant.

Specifically designed to improve efficiency of PFC and output rectification stages of EV / HEV battery charging stations, booster stage of solar inverters and UPS applications, these devices are perfectly matched to operate with MOSFETs or high speed IGBTs.

MECHANICAL DATA

Case: TO-220AC 2L

Molding compound meets UL 94 V-0 flammability rating **Terminals:** matte tin plated leads, solderable per

J-STD-002

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Repetitive peak reverse voltage	V_{RRM}		1200	V			
Average rectified forward current	I _{F(AV)}	T _C = 83 °C, D = 0.50	30				
Non-repetitive peak surge current	I _{FSM}	T_C = 83 °C, t_p = 10 ms, sine wave	190	Α			
Repetitive peak forward current	I _{FRM}	T _C = 45 °C, D = 0.50, f = 20 kHz	60				
Operating junction and storage temperature	T _J , T _{Stq}		-55 to +175	°C			

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	1200	-	-	.,		
Forward voltage	V _F	I _F = 30 A	-	2.6	3.3	V		
		I _F = 30 A, T _J = 125 °C	-	2.1	-			
Reverse leakage current	I _R	$V_R = V_R$ rated	-	-	50			
neverse leakage current		$T_J = 125 ^{\circ}\text{C}, V_R = V_R \text{rated}$		500	μΑ			
Junction capacitance	C _T	V _R = 200 V	-	17	-	pF		
Series inductance	L _S	Measured to lead 5 mm from package body	-	8	-	nH		



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS			MAX.	UNITS	
		I _F = 1.0 A, dI _F /dt =	100 A/ μ s, V _R = 30 V	-	26	-		
Reverse recovery time	t _{rr}	T _J = 25 °C		=	100	-	ns	
		T _J = 125 °C		=	150	-		
Dook recovery ourrent	1	T _J = 25 °C	$I_F = 20 \text{ A}$	=	12	-	А	
Peak recovery current	I _{RRM}	T _J = 125 °C	dI _F /dt = 600 A/μs V _R = 400 V	=	22	-		
Develope vegevent shows	0	T _J = 25 °C		=	530	-	nC	
Reverse recovery charge	Q _{rr}	T _J = 125 °C		-	1650	-		
Dayaraa raaayany tima	+	T _J = 25 °C		=	80	-	ns	
Reverse recovery time	t _{rr}	T _J = 125 °C		=	120	-		
Dools week your assument		T _J = 25 °C	$I_F = 30 \text{ A}$ $dI_F/dt = 1000 \text{ A/}\mu\text{s}$ $V_R = 800 \text{ V}$	-	22	-	A	
Peak recovery current	I _{RRM}	T _J = 125 °C		=	37	-		
Reverse recovery charge	0	T _J = 25 °C		-	900	-	nC	
	Q _{rr}	T _J = 125 °C		-	2400	-		

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Thermal resistance, junction-to-case	R _{thJC}		-	-	1.1	°C/W			
Weight			-	2.0	-	g			
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)			
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C			
Marking device		Case style: TO-220AC 2L	E5TX3012TH						

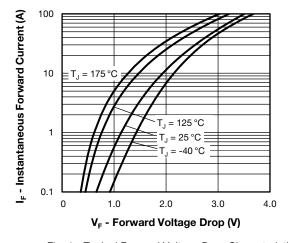


Fig. 1 - Typical Forward Voltage Drop Characteristics

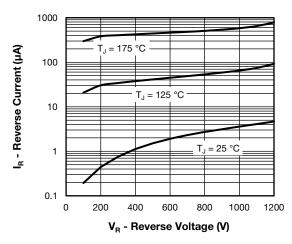


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

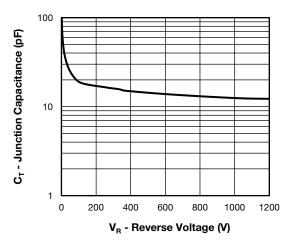


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

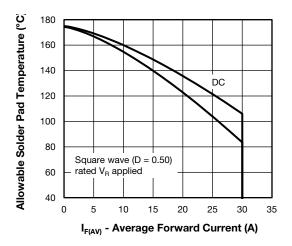


Fig. 4 - Maximum Allowable Case Temperature vs.
Average Forward Current

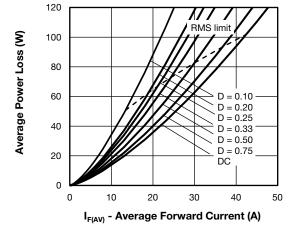


Fig. 5 - Typical Recovery Current vs. dI_F/dt

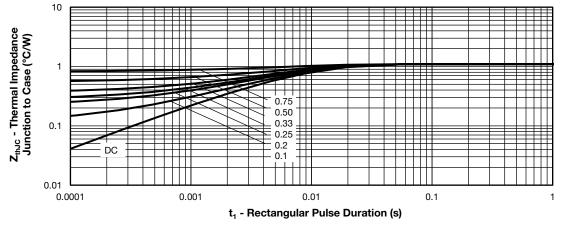
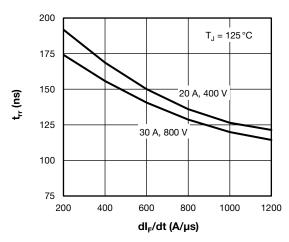


Fig. 6 - Thermal Impedance Z_{thJC} Characteristics







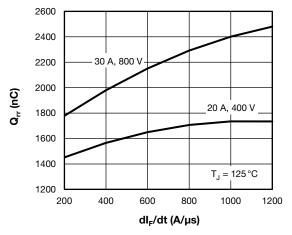


Fig. 8 - Typical Stored Charge vs. dl_F/dt

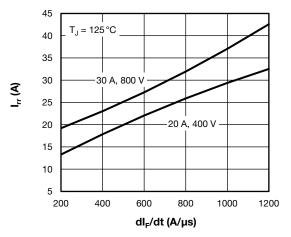


Fig. 9 - Typical Recovery Current vs. dl_F/dt

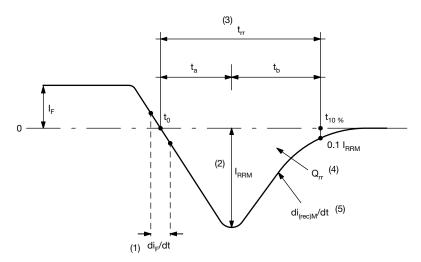


Fig. 10 - Reverse Recovery Waveform and Definitions

Notes

- (1) di_F/dt rate of change of current through zero crossing
- $^{(2)}$ I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from t_0 , crossing point of negative going I_F , to point $t_{10\%}$, 0.1 I_{RRM} (4) Q_{rr} area under curve defined by t_0 and $t_{10\%}$

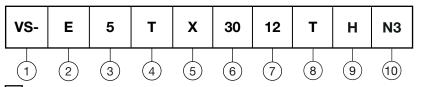
$$Q_{rr} = \int_{t_0}^{t_{10}\%} I(t)dt$$

 $^{(5)}$ di_(rec)M/dt - peak rate of change of current during t_{b} portion of t_{rr}



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - E = single diode

3 - 5 = FRED generation 5

- Package: T = TO-220AC 2L

5 - X = hyperfast recovery

6 - Current rating (30 = 30 A)

- Voltage rating (12 = 1200 V)

8 - T = true 2 pin TO-220

9 - H = AEC-Q101 qualified

- Environmental digit:

N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

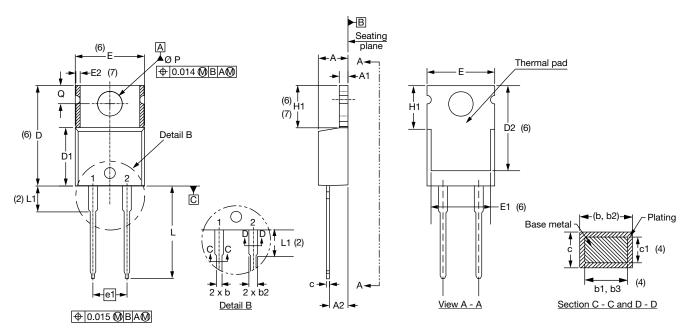
ORDERING INFORMATION (Example)							
PREFERRED P/N QUANTITY PER TUBE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION							
VS-E5TX3012THN3	50	1000	Antistatic plastic tube				

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?96069				
Part marking information	www.vishay.com/doc?95391				
SPICE model	www.vishay.com/doc?97017				



TO-220AC 2L

DIMENSIONS in millimeters and inches



SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6
E	10.11	10.51	0.398	0.414	3, 6

SYMBOL	MILLIN	MILLIMETERS		INCHES	
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
E1	6.86	8.89	0.270	0.350	6
E2	-	0.76	-	0.030	7
e1	4.88	5.28	0.192	0.208	
H1	5.84	6.86	0.230	0.270	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- (7) Dimension E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC® TO-220, except D2, where JEDEC® minimum is 0.480"



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Vishay

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