

# **Addendum to MC68HC912DT128A, MC68HC912DG128A, MC68HC912DT128C, MC68HC912DG128C, MC68HC912DT128P, MC68HC912DG128P Technical Data**

Refer to note 2 under the EEPROM Module Configuration (EEMCR) register on page 128 of the above referenced Technical Data. Correct this note by replacing the information as follows:

**From:**

2. The FPOPEN bit is available only on the 0L05H and later mask sets. For previous masks, this bit is reserved.

**To:**

2. The FPOPEN bit is available only on the 1L05H and later mask sets. For previous masks, this bit is reserved.

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