

PMSTA55; PMSTA56

500 mA PNP general-purpose transistors

Rev. 05 — 1 February 2010

Product data sheet

1. Product profile

1.1 General description

PNP transistors in a SOT323 (SC-70) very small Surface-Mounted Device (SMD) plastic package.

Table 1. Product overview

Type number	Package		NPN complement	
	Nexperia	JEITA		
PMSTA55	SOT323	SC-70	PMSTA05	
PMSTA56			PMSTA06	

1.2 Features

- High current (max. 500 mA)
- Collector-emitter voltage:
 - ◆ 60 V (PMSTA55)
 - ◆ 80 V (PMSTA56)

1.3 Applications

Intended for telephony and professional communication equipment.

2. Pinning information

Table 2. Pinning

Table 2.	i iiiiiiig		
Pin	Description	Simplified outline	Graphic symbol
1	base		
2	emitter	3	3
3	collector		1—
			2
		1 🗆 🗆 2	_
			006aab25



3. Ordering information

Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
PMSTA55	SC-70	plastic surface-mounted package; 3 leads	SOT323			
PMSTA56						

4. Marking

Table 4. Marking codes

Type number	Marking code ^[1]
PMSTA55	*2H
PMSTA56	*2G

^{[1] * = -:} made in Hong Kong

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Parameter	Conditions	Min	Max	Unit
collector-base voltage	open emitter			
PMSTA55		-	-60	V
PMSTA56		-	-80	V
collector-emitter voltage	open base			
PMSTA55		-	-60	V
PMSTA56		-	-80	V
emitter-base voltage	open collector	-	-4	V
collector current		-	-500	mA
peak collector current		-	-500	mA
peak base current		-	-500	mA
total power dissipation	$T_{amb} \le 25 ^{\circ}C$	[1] -	200	mW
junction temperature		-	150	°C
ambient temperature		–65	+150	°C
storage temperature		–65	+150	°C
	collector-base voltage PMSTA55 PMSTA56 collector-emitter voltage PMSTA55 PMSTA56 emitter-base voltage collector current peak collector current peak base current total power dissipation junction temperature ambient temperature	collector-base voltage open emitter PMSTA55 PMSTA56 collector-emitter voltage open base PMSTA55 PMSTA56 emitter-base voltage open collector collector current peak collector current peak base current total power dissipation $T_{amb} \le 25 ^{\circ}\text{C}$ junction temperature ambient temperature	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	collector-base voltage open emitter PMSTA5560 PMSTA5680 collector-emitter voltage open base $\begin{array}{c ccccccccccccccccccccccccccccccccccc$

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

^{* =} p: made in Hong Kong

^{* =} t: made in Malaysia

^{* =} W: made in China

6. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	<u>[1]</u> -	-	625	K/W

^[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

7. Characteristics

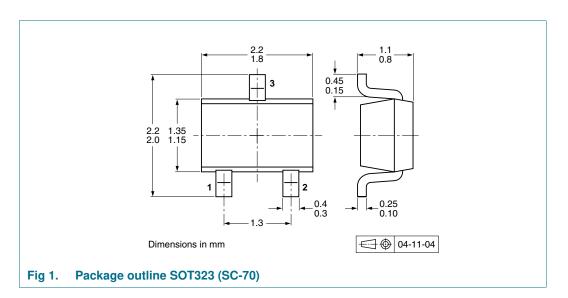
Table 7. Characteristics

T_{amb} = 25 °C unless otherwise specified.

amb - 20	o arriodo otriorwido op	comea.				
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off current					
	PMSTA55	$V_{CB} = -60 \text{ V}; I_E = 0 \text{ A}$	-	-	-100	nA
	PMSTA56	$V_{CB} = -80 \text{ V}; I_E = 0 \text{ A}$	-	-	-100	nA
I _{EBO}	emitter-base cut-off current	$V_{EB} = -4 \text{ V}; I_C = 0 \text{ A}$	-	-	-500	nA
h _{FE}	DC current gain	$V_{CE} = -1 V;$ $I_{C} = -10 \text{ mA}$	100	-	-	
		$V_{CE} = -1 \text{ V};$ $I_{C} = -100 \text{ mA}$	100	-	-	
V _{CEsat}	collector-emitter saturation voltage	$I_C = -100 \text{ mA};$ $I_B = -10 \text{ mA}$	-	-	-250	mV
V_{BE}	base-emitter voltage	$I_{C} = -100 \text{ mA};$ $V_{CE} = -1 \text{ V}$	-	-	-1.2	mV
f _T	transition frequency	$V_{CE} = -1 \text{ V};$ $I_{C} = -100 \text{ mA};$ $f = 100 \text{ MHz}$	50	-	-	MHz

^[1] Pulse test: $t_p \leq 300~\mu s;~\delta \leq 0.02.$

8. Package outline



9. Packing information

Table 8. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Type number	Package	e Description Packing q		quantity	
				3000	10000
PMSTA55	SOT323	4 mm pitch, 8 mm tape and reel		-115	-135
PMSTA56					

[1] For further information and the availability of packing methods, see Section 12.

10. Revision history

Table 9. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes		
PMSTA55_56_5	20100201	Product data sheet	-	PMSTA55_56_N_4		
Modifications:	 The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors. 					
	 Legal texts have been adapted to the new company name where appropriate. 					
	Section 1 "Product profile": amended					
	• Table 2 "Pir	nning": amended				
	Section 3 "Ordering information": added					
	Section 4 "Marking": amended					
	• Figure 1: superseded by minimized package outline drawing					
	Section 9 "Packing information": added					
	Section 11	<u>"Legal information"</u> : updated	d			
PMSTA55_56_N_4	20080117	Product data sheet	-	PMSTA55_56_3		
PMSTA55_56_3	19990422	Product specification	-	PMSTA55_56_2		
PMSTA55_56_2	19980721	Product specification	-	PMSTA55_56_1		
PMSTA55_56_1	19970602	Product specification	-	-		

11. Legal information

11.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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For more information, please visit: http://www.nexperia.com

For sales office addresses, please send an email to: salesaddresses@nexperia.com

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Nexperia

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