

40V N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI3333-8 (Type UX)

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _C = +25°C
40V	$7.9 \text{m}\Omega$ @ $V_{GS} = 10V$	54.8A
	$12m\Omega @ V_{GS} = 4.5V$	44.5A

Features

- 100% Unclamped Inductive Switching-Ensures More Reliable and Robust End Application
- Low On-Resistance
- Low Input Capacitance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

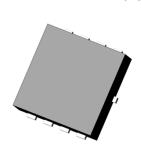
Description and Applications

This new generation MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, which makes it ideal for high-efficiency power management applications.

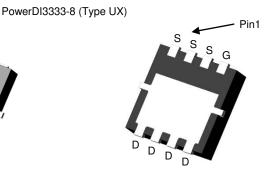
- Power Management Functions
- **DC-DC Converters**

Mechanical Data

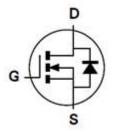
- Case: PowerDI[®]3333-8 (Type UX)
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish—Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.072 grams (Approximate)







Bottom View



Equivalent Circuit

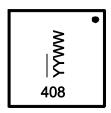
Ordering Information (Note 4)

Part Number	Case	Packaging
DMT4008LFV-7	PowerDI3333-8 (Type UX)	2000/Tape & Reel
DMT4008LFV-13	PowerDI3333-8 (Type UX)	3000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain < 900ppm bromine, < 900ppm chlorine (< 1500ppm total Br + CI) and <1000ppm antimony compounds.
- 4. For packaging details, see https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



408 = Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 18 = 2018) WW = Week Code (01 to 53)



Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V_{DSS}	40	V	
Gate-Source Voltage		V _{GSS}	±20	V
Continuous Drain Current, V _{GS} = 10V (Note 6)	$T_C = +25$ °C $T_C = +70$ °C	I _D	54.8 43.9	А
Continuous Drain Current, V _{GS} = 10V (Note 5)	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	12.1 9.7	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	70	Α	
Maximum Continuous Body Diode Forward Current (Note 6)	Is	29.8	Α	
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%	I _{SM}	70	Α	
Avalanche Current, L = 0.3mH	I _{AS}	11.3	Α	
Avalanche Energy, L = 0.3mH	E _{AS}	19.2	mJ	

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	P_{D}	1.9	W
Thermal Resistance, Junction to Ambient (Note 5)		R _{OJA}	64	°C/W
Total Power Dissipation (Note 6)	$T_C = +25^{\circ}C$	P_{D}	35.7	W
Thermal Resistance, Junction to Case (Note 6)		Rejc	3.5	°C/W
Operating and Storage Temperature Range		$T_{J_i} T_{STG}$	-55 to +150	°C

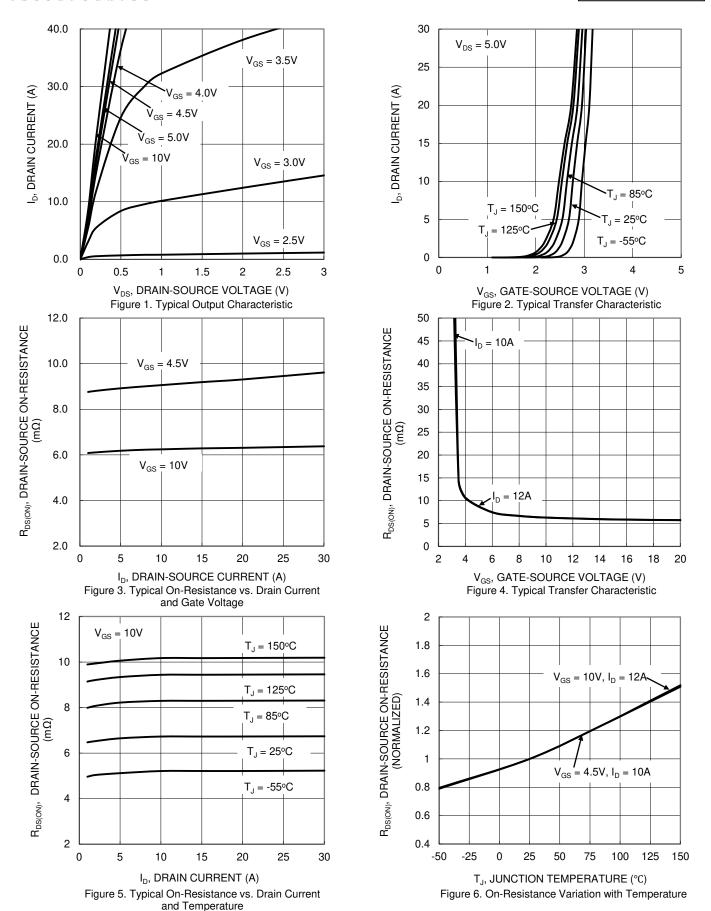
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	40	_	_	٧	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}			1	μΑ	$V_{DS} = 32V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}		_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)						_	
Gate Threshold Voltage	V _{GS(TH)}	1	_	3	٧	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	Dag (av)	_	6.5	7.9	mΩ	$V_{GS} = 10V, I_D = 12A$	
Static Brain-Source On-Hesistance	R _{DS(ON)}		9.4	12		$V_{GS} = 4.5V, I_D = 10A$	
Diode Forward Voltage	V_{SD}		_	1.2	٧	$V_{GS} = 0V, I_{S} = 10A$	
DYNAMIC CHARACTERISTICS (Note 8)						_	
Input Capacitance	C _{iss}		1179	_	pF	.,	
Output Capacitance	Coss		384	_	pF	V _{DS} = 20V, V _{GS} = 0V, -f = 1MHz	
Reverse Transfer Capacitance	C _{rss}		42	_	pF	1 – 1101112	
Gate Resistance	R_{g}	l	1.7		Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 10V)	Q_g		17.1	_	nC		
Total Gate Charge (V _{GS} = 4.5V)	Q_g	l	8.3		nC	V _{DS} = 20V. I _D = 10A	
Gate-Source Charge	Qgs		2.4	_	nC	V _{DS} = 20V, I _D = 10A	
Gate-Drain Charge	Q_{gd}	_	3.4	_	nC	1	
Turn-On Delay Time	t _{D(ON)}	_	3.5	_	ns	$V_{DD} = 20V, V_{GS} = 10V,$ $R_{G} = 6\Omega, I_{D} = 10A$	
Turn-On Rise Time	t _R	_	3.7	_	ns		
Turn-Off Delay Time	t _{D(OFF)}		17.1	_	ns		
Turn-Off Fall Time	t _F		6.4	_	ns		
Body Diode Reverse Recovery Time	t _{RR}	1	19.8	_	ns	I _F = 10A, di/dt = 400A/µs	
Body Diode Reverse Recovery Charge	Q _{RR}	_	8.8	_	nC		

Notes:

- 5. Device mounted on FR-4 substrate PCB, 2oz copper, with thermal bias to bottom layer and 1inch square copper plate.
- 6. Thermal resistance from junction to soldering point (on the exposed drain pad).7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to product testing.







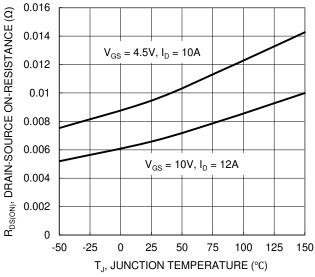


Figure 7. On-Resistance Variation with Temperature

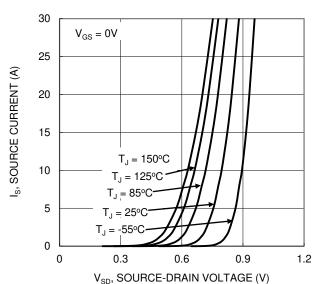


Figure 9. Diode Forward Voltage vs. Current 10 8 6 $V_{GS}(V)$ 4 $V_{DS} = 20V, I_{D} = 10A$ 2 0 2 0 4 6 8 10 12 14 16 18

 Q_g (nC) Figure 11. Gate Charge

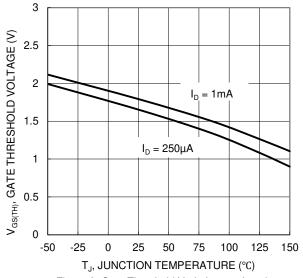
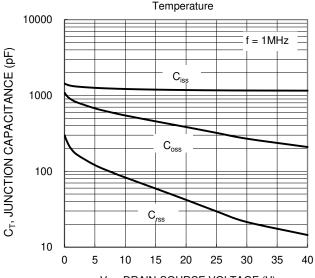


Figure 8. Gate Threshold Variation vs. Junction Temperature



V_{DS}, DRAIN-SOURCE VOLTAGE (V) Figure 10. Typical Junction Capacitance

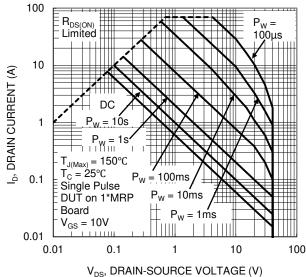


Figure 12. SOA, Safe Operation Area



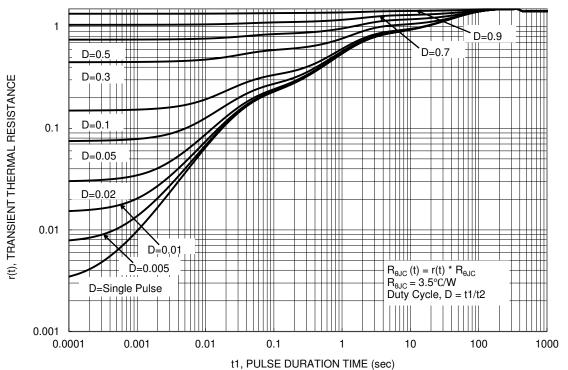


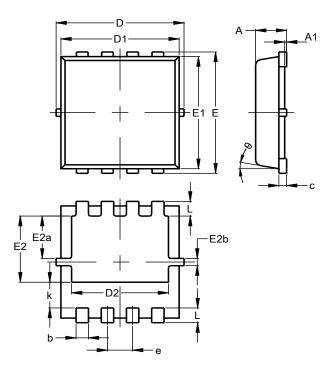
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

 $Please see \ http://www.diodes.com/package-outlines.html \ for \ the \ latest \ version.$

PowerDI3333-8 (Type UX)

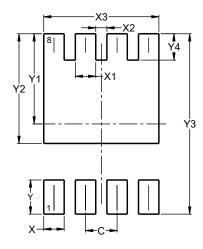


PowerDl3333-8 (Type UX)					
Dim	Min	Max	Тур		
Α	0.75	0.85	0.80		
A 1	0.00	0.05	_		
b	0.25	0.40	0.32		
С	0.10	0.25	0.15		
D	3.20	3.40	3.30		
D1	2.95	3.15	3.05		
D2	2.30	2.70	2.50		
Е	3.20	3.40	3.30		
E1	2.95	3.15	3.05		
E2	1.60	2.00	1.80		
E2a	0.95	1.35	1.15		
E2b	0.10	0.30	0.20		
е	0.65 BSC				
k	0.50	0.90	0.70		
L	0.30	0.50	0.40		
θ	0°	12°	10°		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8 (Type UX)



Dimensions	Value (in mm)		
С	0.650		
X	0.420		
X1	0.420		
X2	0.230		
Х3	2.370		
Υ	0.700		
Y1	1.850		
Y2	2.250		
Y3	3.700		
Y4	0.540		



IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

- A. Life support devices or systems are devices or systems which:
 - 1. are intended to implant into the body, or
 - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2018, Diodes Incorporated

www.diodes.com