ANALOG DEVICES

EV-ADuM7703-8FMCZ User Guide
UG-1545

One Technology Way • P.O. Box 9106 • Norwood, MA 02062-9106, U.S.A. • Tel: 781.329.4700 • Fax: 781.461.3113 • www.analog.com

Evaluating the ADuM7703 16-Bit, Isolated, Sigma-Delta ADC

FEATURES

Fully featured evaluation board for the ADuM7703
On-board power supplies
Standalone capability
SDP-H1 controller board compatible
PC-based software for control and data analysis

EVALUATION KIT CONTENTS

EV-ADuM7703-8FMCZ evaluation board
ACE software download instructions

EQUIPMENT NEEDED

SDP-H1 controller board
USB cable (provided in SDP-H1 kit)
12 V wall wart signal source (provided in SDP-H1 kit)
PC running Windows Vista SP2 (32-bit or 64-bit), Windows 7
   SP1 (32-bit or 64-bit), Windows 8.1 (32-bit or 64-bit), or
   Windows 10 (32-bit or 64-bit) with a USB 2.0 port

DOCUMENTS NEEDED

ADuM7703 data sheet

SOFTWARE NEEDED

ACE software
ADuM7703 ACE plugin (provided in ACE software)

GENERAL DESCRIPTION

The EV-ADuM7703-8FMCZ is a fully featured evaluation board that allows the user to evaluate all features of the ADuM7703, 16-bit, isolated, Σ-Δ analog-to-digital converter (ADC). The system demonstration platform high speed EVAL-SDP-CH1Z (SDP-H1) controller board controls the EV-ADuM7703-8FMCZ evaluation board via the J9 connector and through the USB port of a PC using the Analysis Control Evaluation (ACE) software. The ACE software is available for download on the ACE software page.

On-board components include an isolated, 5 kV, *iso*Power® dc-to-dc converter (the ADuM6000), a 50 mA, 60 V, high efficiency buck regulator (the ADP2360ACPZ-5.0), and a 5 V, low noise, low dropout (LDO) regulator (the ADP7104ARDZ-5.0).

This user guide explains how to use the EV-ADuM7703-8FMCZ to evaluate the ADuM7703. The ADuM7703 is a high performance, second-order, Σ-Δ ADC that converts an analog input signal into a high speed, single-bit data stream with on-chip digital isolation based on Analog Devices, Inc., *i*Coupler technology.

Complete specifications for the ADuM7703 are provided in the ADuM7703 data sheet and must be consulted in conjunction with this user guide when using the evaluation board. Full details on the SDP-H1 board are available on the SDP-H1 product page.

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REVISION HISTORY

6/2019—Revision 0: Initial Version
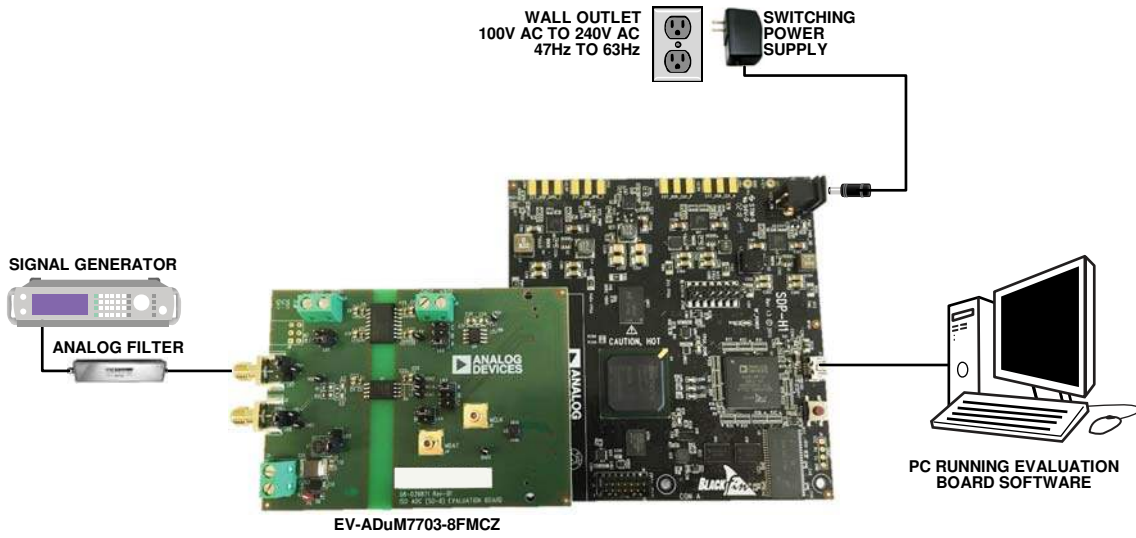
EVALUATION BOARD CONNECTION DIAGRAM

Figure 1.

EVALUATION BOARD HARDWARE

POWER SUPPLIES

Ensure that all link positions are set according to the required operating mode before applying power and signals to the EV-ADuM7703-8FMCZ. See Table 2 for the complete list of link options.

The EV-ADuM7703-8FMCZ is powered via the SDP-H1 board. The SDP-H1 board generates 12 V and 3.3 V supply rails. The 12 V supply is connected to the on-board ADP7104ARDZ-5.0 that powers the ADuM6000. The ADuM6000 generates an isolated 5 V supply to power the V_{DD1} rail of the ADuM7703. The 3.3 V supply rail generated by the SDP-H1 board powers the V_{DD2} rail of the ADuM7703.

To power V_{DD1} externally, connect an external power supply in the 23 V to 25 V range to the HIGH_V J7 connector, or connect an external power supply in the 4.5 V to 20 V range to the J5 connector.

Power V_{DD2} externally with an external power supply in the 3 V to 5.5 V range via the J6 connector.

The ADuM7703 has two main ground planes, GND_1 and GND_2. These planes are isolated with a creepage and clearance of 8.1 mm.

When an external voltage is supplied to the J6 input connector and the EV-ADuM7703-8FMCZ is connected to the SDP-H1 board, ensure that the voltage does not exceed 3.3 V. Exceeding 3.3 V causes permanent damage to the SDP-H1 board.

INPUT SIGNALS

Do not connect an analog input exceeding ±250 mV (±320 mV at full scale) to the ADuM7703. Connect an input signal of up to 500 mV p-p to the EV-ADuM7703-8FMCZ via the J1 analog input connector.

The EV-ADuM7703-8FMCZ has analog and digital ground planes that are physically isolated from each other. Power to the analog supply rail is supplied through the on-board ADuM6000. V_{DD1} can be supplied from the J5 external connector or from the high voltage (HIGH_V) J7 external connector. The on-board ADP2360ACPZ-5.0 steps the 24 V supply connected to the J7 connector down to 5 V. See Table 1 and Table 2 for more information about supplying V_{DD1} externally.

Table 1. External Power Supplies (Optional)

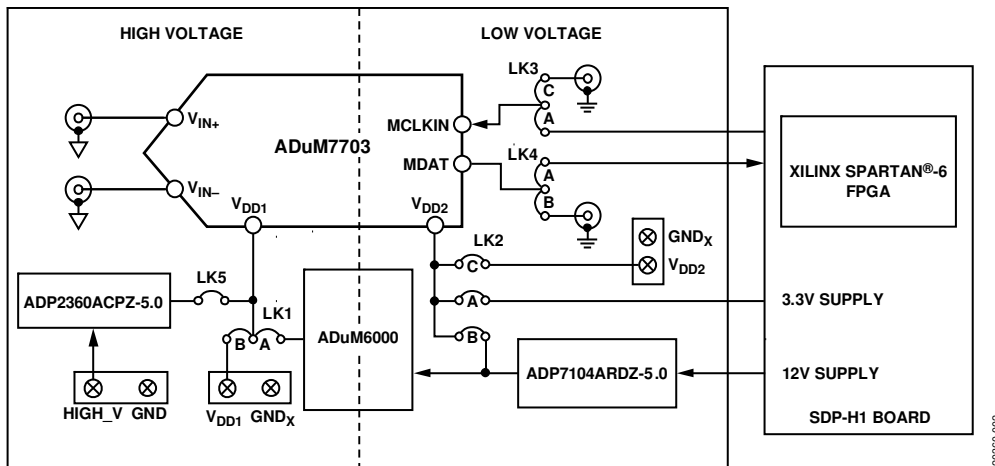| Power Supply	Connector	Voltage Range	Purpose
V_{DD1}	J5	4.5 V to 20 V	Analog supply rail
V_{DD2}	J6	3 V to 5.5 V	Digital supply rail without the SDP-H1 board connected
		3.15 V to 3.45 V	Digital supply rail with the SDP-H1 board connected
HIGH_V	J7	23 V to 25 V	Analog supply rail (high voltage alternative to J5)

Figure 2. EV-ADuM7703-8FMCZ Block Diagram

LINK CONFIGURATION OPTIONS

Multiple link options must be set correctly to select the appropriate operating setup before using the EV-ADuM7703-8FMCZ. The functions of these options are outlined in Table 2.

Setup Conditions

Ensure that all link positions are set as required by the operating mode before applying power and signals to the EV-ADuM7703-8FMCZ. The EV-ADuM7703-8FMCZ can be operated in standalone mode or in SDP-H1 controlled mode and used with the SDP-H1 board.

Table 2 shows the default positions the links are set to when the EV-ADuM7703-8FMCZ is packaged. When the EV-ADuM7703-8FMCZ is shipped, it is set up to operate in SDP-H1 controlled mode with power supplied from the SDP-H1 board, and the on-board ADuM6000 powers V_{DD1}.

Table 2. Link Options

Category	Link	Default Position	Function
Power Supplies	LK1	A	LK1 selects the ADuM7703 V_{DD1} supply source. Remove LK5 if using Position A or Position B.
			In Position A, the on-board ADuM6000 supplies V_{DD1}.
			In Position B, the J5 connector supplies V_{DD1} externally.
	LK5	Removed	Insert LK5 to power V_{DD1} with the on-board ADP2360ACPZ-5.0 via the J7 connector. Remove LK5 if V_{DD1} is powered via LK1.
	LK2	A	LK2 selects the ADuM7703 V_{DD2} supply source.
			In Position A, the SDP-H1 board supplies V_{DD2}.
			In Position B, the on-board ADP7104ARDZ-5.0 supplies V_{DD2}.
			In Position C, the J6 connector supplies V_{DD2} externally.
Analog Input	LK7	Removed	Insert LK7 to short V_{IN+} to ground. Remove LK7 if a signal is applied to V_{IN+}.
	LK8	Inserted	Insert LK8 to short V_{IN-} to ground. Remove LK8 if a signal is applied to V_{IN-}.
Serial Interface	LK3	A	LK3 selects the MCLK source for the serial interface.
			In Position A, the SDP-H1 board sources MCLK.
			In Position B, do not use.
			In Position C, the J3 Subminiature Version B (SMB) jack sources MCLK (standalone mode).
	LK4	A	LK4 routes the MDAT output for the serial interface.
			In Position A, MDAT is sent to the SDP-H1 board.
			In Position B, MDAT is sent to the J4 SMB jack (standalone mode).

SOCKETS AND CONNECTORS

Table 3 describes the connectors and sockets on the EV-ADuM7703-8FMCZ.

Table 3. On-Board Connectors

Connector	Function
J1	Analog input V_{IN+}
J2	Analog input V_{IN-}
J3	MCLK input (standalone mode)
J4	MDAT output (standalone mode)
J5	V_{DD1} external source
J6	V_{DD2} external source
J7	V_{DD1} external source (high voltage)

The default interface to the EV-ADuM7703-8FMCZ is achieved via the 160-way connector. The connector attaches the EV-ADuM7703-8FMCZ to the SDP-H1 board. When the EV-ADuM7703-8FMCZ is used in standalone mode, communication is achieved via the J3 SMB jack and the J4 SMB jack. See Table 2 for more information on configuring the EV-ADuM7703-8FMCZ for standalone mode.

TEST POINTS

The EV-ADuM7703-8FMCZ has several test points. These test points provide easy access to the EV-ADuM7703-8FMCZ signals for probing, evaluation, and debugging.

EVALUATION BOARD SOFTWARE

SOFTWARE INSTALLATION PROCEDURES

Download the ACE software from the ADuM7703 product page. Install the ACE software on the PC before using the EV-ADuM7703-8FMCZ.

The installation process in the Installing the ACE Software section includes the ACE software installation and the SDP-H1 driver installation.

Note that the ACE software and SDP-H1 drivers must be installed before connecting the EV-ADuM7703-8FMCZ and the SDP-H1 board to the USB port of the PC to ensure that the evaluation system is properly recognized when it is connected to the PC.

Installing the ACE Software

To install the ACE software, take the following steps:

1. Download the ACE software to a Windows®-based PC.
2. Double click the **ACEInstall.exe** file to begin the installation. By default, the software is saved to the following location: **C:\Program Files (x86)\Analog Devices\ACE.**
3. A dialog box opens asking for permission to allow the program to make changes to the PC. Click **Yes** to start the installation process.
4. Click **Next >** to continue the installation (see Figure 3).
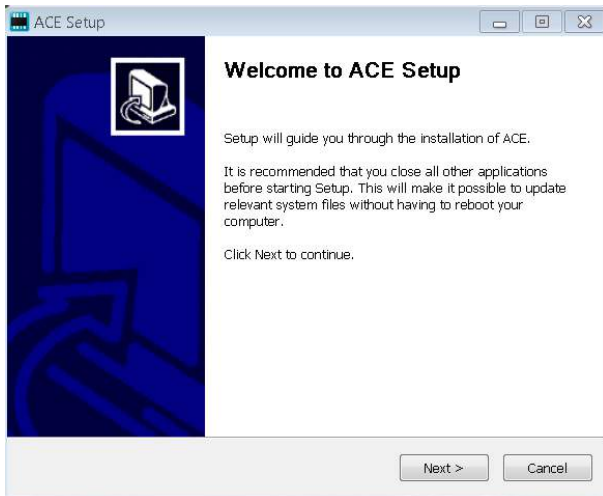
Figure 3. ACE Software Installation Confirmation

5. Read the software license agreement and click **I Agree** (see Figure 4).
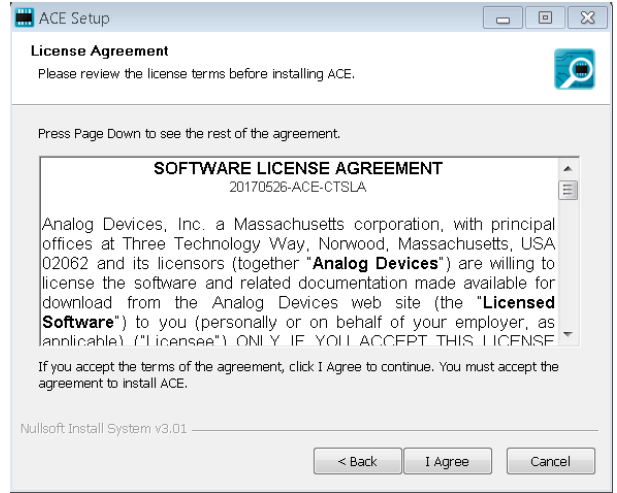
Figure 4. License Agreement

6. Click **Browse** to choose the installation location and then click **Next >** (see Figure 5).
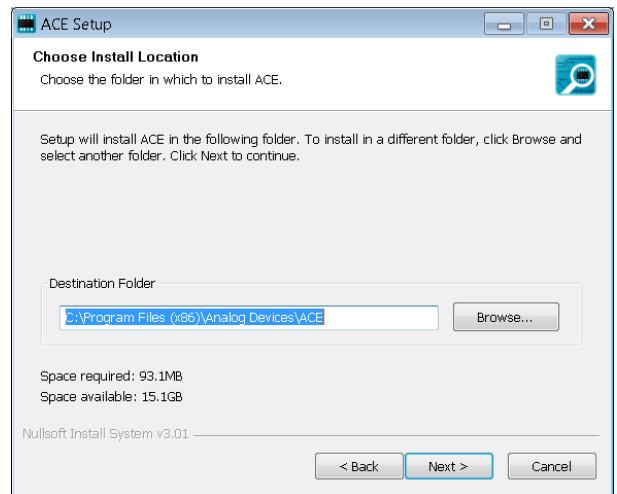
Figure 5. Choose Installation Location

7. The components to install are preselected (see Figure 6). Click **Install.**
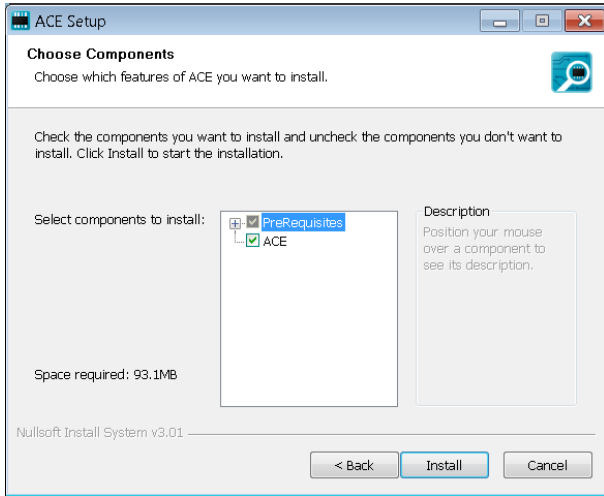
Figure 6. Choose Components

8. The **Windows Security** window appears (see Figure 7). Click **Install**. Figure 8 shows the installation in progress. No action is required.
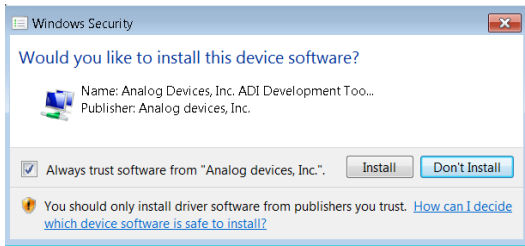
Figure 7. Windows Security Window
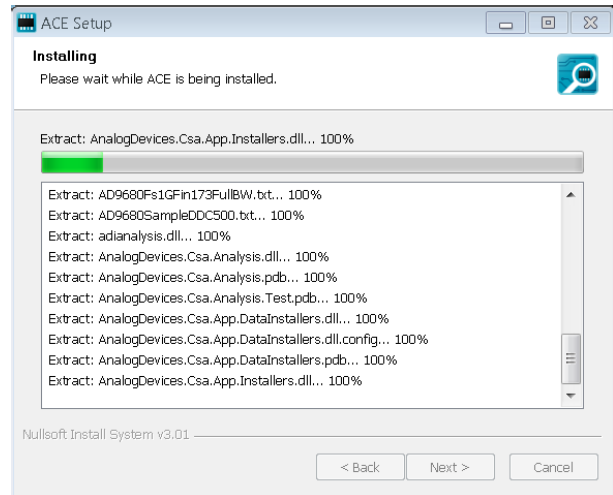
Figure 8. Installation in Progress

9. When the installation is complete, click **Next >**, and then click **Finish** to complete the installation process (see Figure 9).
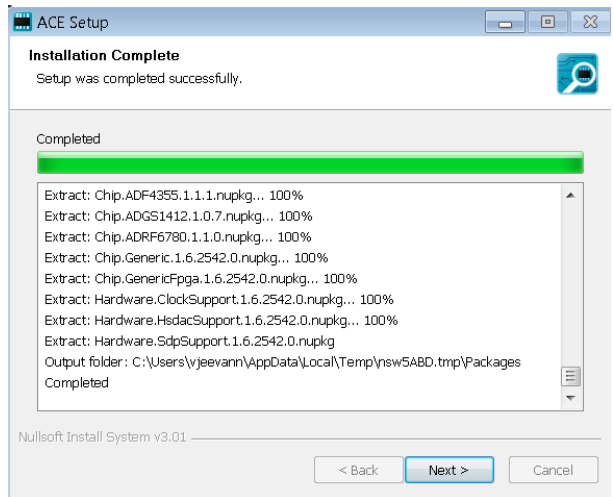
Figure 9. Installation Complete

EVALUATION BOARD SETUP PROCEDURES

The EV-ADuM7703-8FMCZ connects to the SDP-H1 board. The SDP-H1 board is the communication link between the PC and the EV-ADuM7703-8FMCZ. Figure 11 shows a diagram of the connections between the EV-ADuM7703-8FMCZ and the SDP-H1 board.

Connecting the EV-ADuM7703-8FMCZ and the SDP-H1 Board to the PC

When the ACE software is installed, use the following steps to set up the EV-ADuM7703-8FMCZ and the SDP-H1 board:

1. Ensure that all configuration links are in the appropriate positions, as described in Table 2.
2. Connect the EV-ADuM7703-8FMCZ securely to the 160-way connector on the SDP-H1.
3. To power up the SDP-H1, insert the 12 V, dc barrel jack (included in the SDP-H1 kit) into the barrel connector labeled +12V_VIN on the SDP-H1 board.
   Note that the EV-ADuM7703-8FMCZ does not require an external power supply adapter.
4. Connect the SDP-H1 board to the PC via the USB cable included in the SDP-H1 kit.

Verifying the Board Connection

To verify the connections to the board, take the following steps:

1. When the SDP-H1 board is plugged into the PC, allow the **Found New Hardware Wizard** to run. If prompted by the operating system, choose to automatically search for the drivers for the SDP-H1 board.

2. Navigate to the **Device Manager** window on the PC (see Figure 10).
3. A dialog box opens asking for permission to allow the program to make changes to the computer. Click **Yes**.
4. The **Computer Management** window opens. In the list labeled **System Tools**, click **Device Manager**. If the SDP-H1 driver is installed and the board is properly connected to the PC, **Analog Devices SDP-H1** is shown in the **ADI Development Tools** list in the **Device Manager** window, as shown in Figure 10.
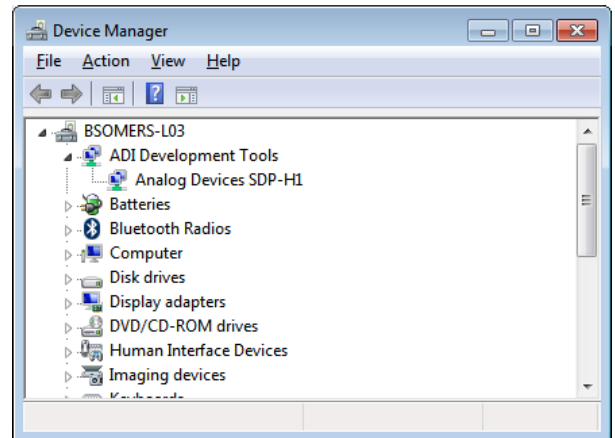
*Figure 10. **Device Manager** Window*

Disconnecting the EV-ADuM7703-8FMCZ

Disconnect power from the SDP-H1 board or press the reset tact switch located alongside the mini USB port before disconnecting the EV-ADuM7703-8FMCZ from the SDP-H1 board.
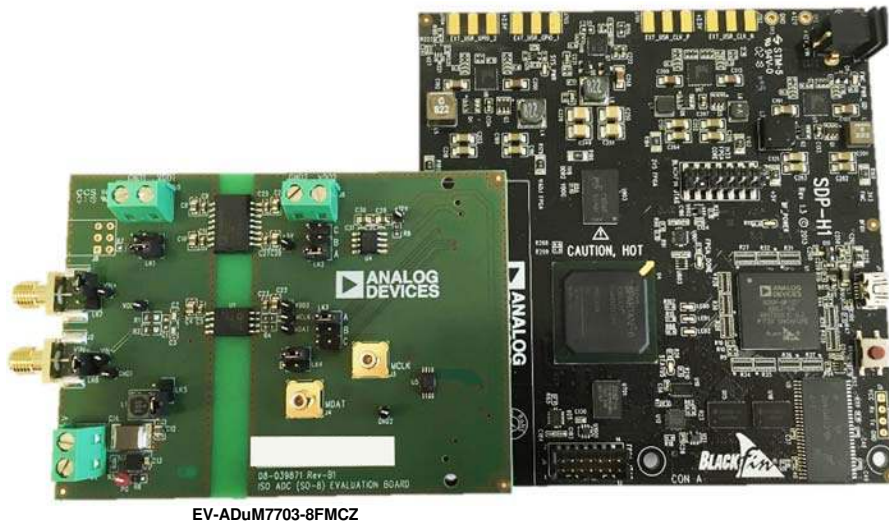
EV-ADuM7703-8FMCZ

Figure 11. EV-ADuM7703-8FMCZ (Left) and SDP-H1 Board (Right) Connection

ACE SOFTWARE OPERATION

LAUNCHING THE SOFTWARE

When the EV-ADuM7703-8FMCZ and the SDP-H1 board are properly connected to the PC, launch the ACE software. To launch the ACE software, take the following steps:

1. In the **Start** menu of the PC, click **All Programs** > **Analog Devices** > **ACE** > **ACE.exe** to open the ACE window shown in Figure 12.
2. Connect an input signal via the V_{IN+} J1 connector.

If the EV-ADuM7703-8FMCZ is not connected to the USB port via the SDP-H1 board when the ACE software is launched, the **ADuM7703 Eval Board** icon does not show up in the **Attached Hardware** section. To make the icon appear, connect the EV-ADuM7703-8FMCZ and the SDP-H1 to the USB port of the PC, wait a few seconds, and then follow the instructions in the dialogue box that opens.

3. Double click the **ADuM7703 Eval Board** icon to open the chip view window shown in Figure 13.

Figure 12. ACE Software Main Window

CHIP VIEW WINDOW FEATURES
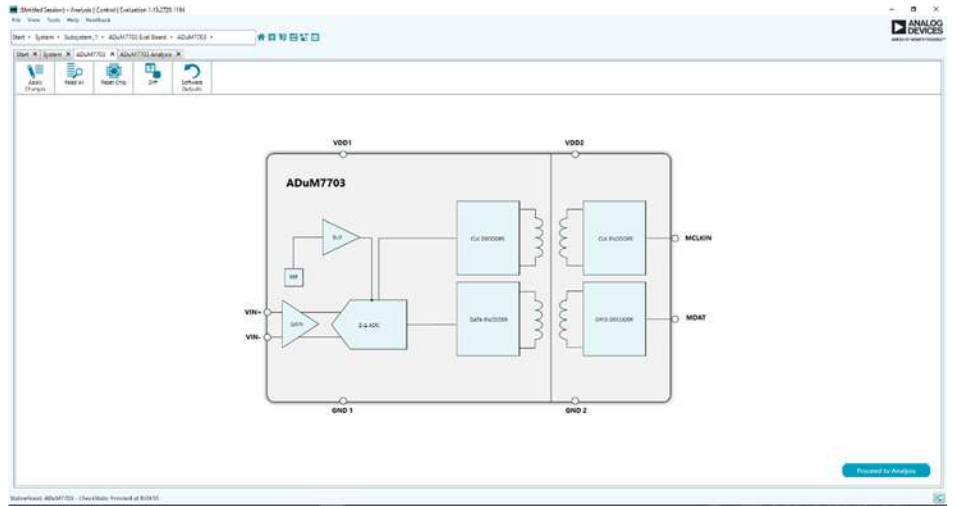
Figure 13. Chip View Window

Figure 14. Analysis View Window

Click **Proceed to Analysis** in the chip view window to open the analysis view window shown in Figure 14. The analysis view contains the **Waveform** tab, **Histogram** tab, and **FFT** tab on the left side of the window.

WAVEFORM TAB

The **Waveform** tab displays data in form of time vs. discrete data values with the results (see Figure 15).

CAPTURE

The **CAPTURE** pane contains the capture settings. These settings reflect onto the registers automatically before data capture.

The **Sample Count** list in the **General Capture Settings** section allows the user to select the number of samples per channel per capture.

The **FPGA Settings** section contains the field programmable gate array (FPGA) settings that specify the **Sample Frequency** (MCLK frequency) used for the serial interface, and the decimation ratio used by the FPGA to filter the data.

Click **Run Once** at the bottom of the pane to start a data capture of the samples at the sample rate specified in the **Sample Count** field. These samples are stored on the SDP-H1 board and are only transferred to the PC when the sample frame is complete.

Click **Run Continuously** to start a data capture that gathers samples continuously with one batch of data at a time.

RESULTS

Display Channels allows the user to select which channels to capture. Data for a specific channel is only shown if that channel is selected before the capture.

Waveform displays amplitude and sample frequency for the selected channel.

Click **Export** to export captured data. The waveform, histogram, and FFT data is stored in **.xml** files along with the values of the parameters at capture.

The data waveform graph shows each successive sample of the ADC output. Zoom in on and pan over the waveform graph using the embedded waveform tools above the graph. Select the channels to display in **Display Channels.**

Click the display units dropdown list labeled **Codes** in Figure 15 to select whether the data graph displays in units of **Codes, Hex**, or **Volts**. The axis controls are dynamic.

Note that the corresponding axis width automatically adjusts to show the entire range of the ADC results after each batch of samples when either xy scale dynamic or x scale dynamic is selected. Select the dynamic using the **XYDirection** tool in Figure 15.

HISTOGRAM TAB

The **Histogram** tab contains the **Histogram** graph and the **RESULTS** pane (see Figure 16).

Click the **Results** drop down menu in the **RESULTS** pane to display the information related to the dc performance.

The **Histogram** graph displays the number of hits per code within the sampled data. This graph displays dc analysis and indicates the noise performance of the device.

FFT TAB

Figure 17 shows the **FFT** tab. This tab displays fast Fourier transform (FFT) information for the last batch of samples gathered.

The **General Settings** section in the **Analysis** pane allows the user to set up the preferred configuration of the FFT analysis. The fundamental is set manually.

The **Windowing** section allows the user to select the windowing type used in the FFT analysis, the number of harmonic bins, and the number of fundamental bins that must be included.

The **Single Tone Analysis** and **Two Tone Analysis** sections allow the user to select the fundamental frequency included in the FFT analysis. Use the **Two Tone Analysis** settings when there are two frequencies being analyzed.

Click **Next** in the **RESULTS** pane to view the **Signal** section. This section displays the sample frequency, sample count, fundamental frequency, and fundamental power.

The **Noise** section displays the SNR and other noise performance results.

The **Distortion** section displays the harmonic content of the sampled signal and dc power when viewing the FFT analysis.

EXITING THE SOFTWARE

To exit the software, click **File** and then click **Exit.**

Figure 15. **Waveform** Tab
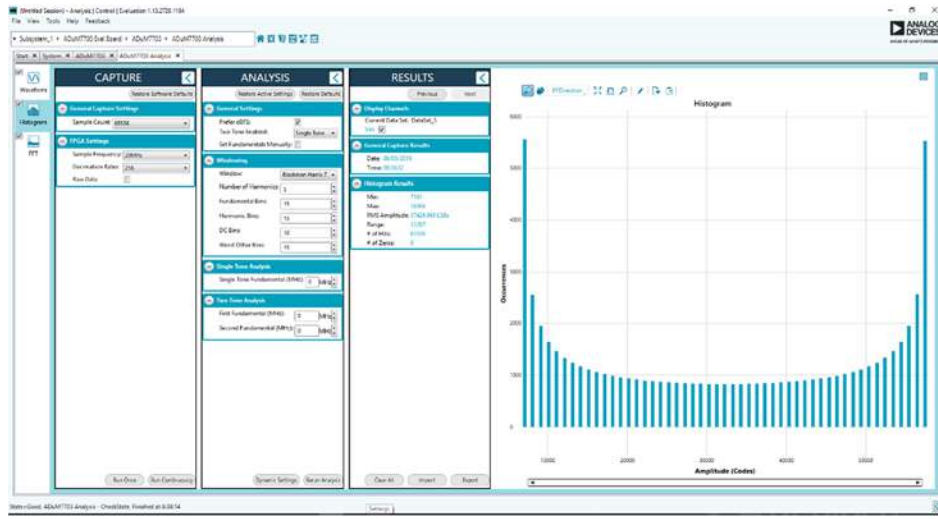
*Figure 16. **Histogram** Tab*

*Figure 17. **FFT** Tab*

NOTES

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