

Pin	Symbol		Function
1; 2	OUT	O	Output; output to the load; pin 1, 2, 6 and 7 must be externally shorted* .
3	IN	I	Input; activates the power switch if shorted to ground.
4; Tab	V _{bb}	+	Supply Voltage; positive power supply voltage; tab and pin4 are internally shorted.
5	IS	S	Sense Output; Diagnostic feedback; provides at normal operation a sense current proportional to the load current; in case of overload, overtemperature and/or short circuit a defined current is provided (see Truth Table on page 8)
6; 7	OUT	O	Output; output to the load; pin 1, 2, 6 and 7 must be externally shorted* .

*) Not shorting all outputs will considerably increase the on-state resistance, reduce the peak current capability and decrease the current sense accuracy

Maximum Ratings at $T_j = 25\text{ °C}$ unless otherwise specified

Parameter	Symbol	Values	Unit
Supply voltage (overvoltage protection see page 4)	V_{bb}	38	V
Supply voltage for full short circuit protection ¹⁾	V_{bb}	30	V
Load dump protection $V_{LoadDump} = U_A + V_S$, $U_A = 13.5\text{ V}$ $R_I = 2\ \Omega$, $R_L = 1\ \Omega$, $t_d = 400\text{ ms}$, IN= low or high	$V_{Load\ dump}^{2)}$	45	V
Load current (Short-circuit current, see page 5)	I_L	self-limited	A
Operating temperature range	T_j	-40 ...+150	°C
Storage temperature range	T_{stg}	-55 ...+150	
Power dissipation (DC)	P_{tot}	81	W
Inductive load switch-off energy dissipation $m^3)$ single pulse, $I_L = 20\text{ A}$, $V_{bb} = 12\text{ V}$ $T_j = 150\text{ °C}$:	E_{AS}	0.4	J
Electrostatic discharge capability (ESD) (Human Body Model) acc. ESD assn. std. S5.1-1993; $R = 1.5\text{ k}\Omega$; $C = 100\text{ pF}$	V_{ESD}	3.0	kV
Current through input pin (DC)	I_{IN}	+15, -120	mA
Current through current sense pin (DC) see internal circuit diagrams page 9	I_{IS}	+15, -120	
Input voltage slew rate	dV_{bIN} / dt	self-limited 20	V/ ∞ s
	$V_{bb} \leq 16\text{ V}$: $V_{bb} > 16\text{ V}$ ⁴⁾ :		

1) Short circuit is defined as a combination of remaining resistances and inductances. See schematic on page 11.

2) $V_{Load\ dump}$ is setup without the DUT connected to the generator per ISO 7637-1 and DIN 40839

3) See also diagram on page 11.

4) See also on page 8. Slew rate limitation can be achieved by means of using a series resistor R_{IN} in the input path. This resistor is also required for reverse operation. See also page 10.

Thermal Characteristics

Parameter and Conditions	Symbol	Values			Unit
		min	typ	max	
Thermal resistance chip - case: junction - ambient (free air): SMD version, device on PCB ⁶⁾ :	R_{thJC} ⁵⁾	--	0.7	0.8	K/W
	R_{thJA}	--	60	--	
		--	33	40	

Electrical Characteristics

Parameter and Conditions at $T_j = 25$, $V_{bb} = 12$ V unless otherwise specified	Symbol	Values			Unit
		min	typ	max	

Load Switching Capabilities and Characteristics

On-state resistance (pin 3 to pin 1,2,6,7) $V_{IN} = 0$, $V_{bb} = 5.5$ V, $I_L = 10$ A	R_{ON}	--	$T_j = 25$ °C:	9.5	13	mΩ
			$T_j = 150$ °C:	17	22	
$V_{IN} = 0$, $V_{bb} = 12$ V, $I_L = 10$ A		--	$T_j = 25$ °C:	7	9	
			$T_j = 150$ °C:	13	16	
Output voltage drop limitation at small load currents (Tab to pin 1,2,6,7) $T_j = -40 \dots 150$ °C:	$V_{ON(NL)}$	--	30	60	mV	
Nominal load current (Tab to pin 1,5) ISO Proposal: $V_{ON} \leq 0.5$ V, $T_C = 85$ °C, $T_j \leq 150$ °C SMD ⁶⁾ , $V_{ON} \leq 0.5$ V, $T_A = 85$ °C, $T_j \leq 150$ °C	$I_{L(ISO)}$	37.5	48	--	A	
	$I_{L(nom)}$	9.5	12	--		
Turn-on time to 90% V_{OUT} :	t_{on}	--	300	550	∞s	
Turn-off time to 10% V_{OUT} : $R_L = 2.2$ Ω, $T_j = -40 \dots 150$ °C	t_{off}	--	300	600		
Slew rate on 25 to 50% V_{OUT} , $R_L = 2.2$ Ω, $T_j = -40 \dots 150$ °C	dV/dt_{on}	--	0.2	0.35	V/∞s	
Slew rate off 50 to 25% V_{OUT} , $R_L = 2.2$ Ω, $T_j = -40 \dots 150$ °C	$-dV/dt_{off}$	--	0.2	0.45	V/∞s	

⁵⁾ Thermal resistance R_{thCH} case to heatsink (about 0.5 ... 0.9 K/W with silicone paste) not included!

⁶⁾ Device on 50mm*50mm*1.5mm epoxy PCB FR4 with 6cm² (one layer, 70µm thick) copper area for V_{bb} connection. PCB is vertical without blown air.

Parameter and Conditions at $T_j = 25$, $V_{bb} = 12$ V unless otherwise specified	Symbol	Values			Unit
		min	typ	max	

Operating Parameters

Operating voltage ($V_{IN}=0$) $T_j = -40 \dots 150$ °C:	$V_{bb(on)}$	5.5	--	38	V
Undervoltage shutdown ^{7) 8)}	$V_{bIN(u)}$	--	2.5	3.5	V
Undervoltage restart of charge pump	$V_{bb(ucp)}$	--	4	5.5	V
Overvoltage protection ⁹⁾ $I_{bb} = 15$ mA $T_j = -40 \dots +150$ °C:	$V_{Z,IN}$	63	67	--	V
Standby current $I_{IN} = 0$ $T_j = -40 \dots +120$ °C:	$I_{bb(off)}$	--	3	6	∞A
$T_j = 150$ °C:		--	6	14	

Reverse Battery

Reverse battery voltage ¹⁰⁾	$-V_{bb}$	--	--	16	V
On-state resistance (pin 4, Tab to pin 1,2,6,7) $V_{bb} = -8$ V, $V_{IN} = 0$, $I_L = -10$ A, $R_{IS} = 1$ kΩ, ⁸⁾ $T_j = 25$ °C: $T_j = 150$ °C:	$R_{ON(rev)}$	--	8.5	12	mΩ
$V_{bb} = -12$ V, $V_{IN} = 0$, $I_L = -10$ A, $R_{IS} = 1$ kΩ, $T_j = 25$ °C: $T_j = 150$ °C:		--	13	18	
		--	8	11	
		--	13	19	
Integrated resistor in V_{bb} line	R_{bb}	--	100	150	Ω

Inverse Operation ¹¹⁾

Output voltage drop (pin 4, Tab to pin 1,2,6,7) ⁸⁾ $I_L = -10$ A, $R_{IS} = 1$ kΩ, $T_j = 25$ °C: $I_L = -10$ A, $R_{IS} = 1$ kΩ, $T_j = 150$ °C:	$-V_{ON(inv)}$	--	700	--	mV
		--	300	--	
Turn-on delay after inverse operation; $I_L > 0$ A ⁸⁾ $V_{IN(inv)} = V_{IN(fwd)} = 0$ V	$t_{d(inv)}$	--	1	--	ms

7) $V_{bIN} = V_{bb} - V_{IN}$ see diagram page 14.

8) not subject to production test, specified by design

9) See also $V_{ON(CL)}$ in circuit diagram page 9.

10) For operation at voltages higher than $|16$ V please see required schematic on page 10.

11) Permanent Inverse operation results eventually in a current flow via the intrinsic diode of the power DMOS.
In this case the device switches on with a time delay $t_{d(inv)}$ after the transition from inverse to forward mode.

Parameter and Conditions at $T_j = 25$, $V_{bb} = 12$ V unless otherwise specified	Symbol	Values			Unit	
		min	typ	max		
Protection Functions ¹²⁾						
Short circuit current limit (pin 4, Tab to pin 1,2,6,7) ¹³⁾						
Short circuit current limit at $V_{ON} = 6$ V ¹⁴⁾	$T_j = -40^\circ\text{C}$: $T_j = 25^\circ\text{C}$: $T_j = +150^\circ\text{C}$:	$I_{L6(SC)}$	-- -- 90	140 130 120	170 -- --	A
Short circuit current limit at $V_{ON} = 12$ V $t_m = 170 \mu\text{s}$	$T_j = -40^\circ\text{C}$: $T_j = 25^\circ\text{C}$: $T_j = +150^\circ\text{C}$:	$I_{L12(SC)}$	-- -- 55	105 95 85	130 -- --	A
Short circuit current limit at $V_{ON} = 18$ V ¹⁴⁾	$T_j = -40^\circ\text{C}$: $T_j = 25^\circ\text{C}$: $T_j = +150^\circ\text{C}$:	$I_{L18(SC)}$	-- -- 45	75 70 65	100 -- --	A
Short circuit current limit at $V_{ON} = 24$ V $t_m = 170 \mu\text{s}$	$T_j = -40^\circ\text{C}$: $T_j = 25^\circ\text{C}$: $T_j = +150^\circ\text{C}$:	$I_{L24(SC)}$	-- -- 28	47 46 45	70 -- --	A
Short circuit current limit at $V_{ON} = 36$ V ¹⁴⁾	$T_j = -40^\circ\text{C}$: $T_j = 25^\circ\text{C}$: $T_j = +150^\circ\text{C}$:	$I_{L36(SC)}$	-- -- 15	27 27 27	40 -- --	A
Short circuit shutdown detection voltage		$V_{ON(SC)}$	2.5	3.5	4.5	V
Short circuit shutdown delay after input current positive slope, $V_{ON} > V_{ON(SC)}$, $T_j = -40 \dots +150^\circ\text{C}$ min. value valid only if input "off-signal" time exceeds 30 ∞s		$t_d(SC1)$	350	650	1200	∞s
Short circuit shutdown delay during on condition ¹⁴⁾ $V_{ON} > V_{ON(SC)}$		$t_d(SC2)$	--	2	--	∞s
Output clamp (inductive load switch off) ¹⁵⁾ at $V_{OUT} = V_{bb} - V_{ON(CL)}$ (e.g. overvoltage) $I_L = 40$ mA		$V_{ON(CL)}$	39	42	--	V
Thermal overload trip temperature		T_{jt}	150	175	--	$^\circ\text{C}$
Thermal hysteresis		ΔT_{jt}	--	10	--	K

¹²⁾ Integrated protection functions are designed to prevent IC destruction under fault conditions described in the data sheet. Fault conditions are considered as "outside" normal operating range. Protection functions are not designed for continuous repetitive operation.

¹³⁾ Short circuit current limit for max. duration of $t_d(SC1)$, prior to shutdown, see also figures 3.x on page 13.

¹⁴⁾ not subject to production test, specified by design

¹⁵⁾ See also figure 2b on page 12.

Parameter and Conditions at $T_j = 25$, $V_{bb} = 12$ V unless otherwise specified	Symbol	Values			Unit
		min	typ	max	
Diagnostic Characteristics					
Current sense ratio, static on-condition $k_{ILIS} = I_L : I_{IS}$, $I_{IS} < I_{IS,lim}$ ¹⁶⁾ , $V_{IS} < V_{OUT} - 5$ V, $V_{bIN} > 4.5$ V	k_{ILIS}	--	12500	--	
IL = 35A, $T_j = -40^\circ\text{C}$:		11200	12700	14000	
T _j = +25°C:		11000	12600	13500	
T _j = +150°C:		11000	12200	12800	
IL = 10A, $T_j = -40^\circ\text{C}$:		10500	12700	14300	
T _j = +25°C:		10500	12600	14000	
T _j = +150°C:		10800	12200	13300	
IL = 2.5A, $T_j = -40^\circ\text{C}$:		10000	12300	17000	
T _j = +25°C:		10000	12500	16500	
T _j = +150°C:		10000	13000	15000	
IL = 0.5A, $T_j = -40^\circ\text{C}$:		7000	14000	26000	
T _j = +25°C:		8000	14500	24500	
T _j = +150°C:		9000	15000	23000	
$I_{IN} = 0$ (e.g. during deenergizing of inductive loads):		--	0	--	
Sense current under fault conditions ¹⁷⁾ $V_{ON} > 1$ V, typ $T_j = -40\dots+150^\circ\text{C}$:	$I_{IS,fault}$	4.0	5.2	7.5	mA
Sense saturation current $V_{ON} < 1$ V, typ $T_j = -40\dots+150^\circ\text{C}$:	$I_{IS,lim}$	4.0	6.0	7.5	mA
Fault-Sense signal delay after input current positive slope, $V_{ON} > 1$ V, $T_j = -40\dots+150^\circ\text{C}$	$t_{delay(fault)}$	350	650	1200	∞s
Current sense leakage current, $I_{IN} = 0$	$I_{IS(LL)}$	--	0.1	0.5	∞A
Current sense offset current, $V_{IN} = 0$, $I_L \leq 0$	$I_{IS(LH)}$	--	0.1	1	∞A
Minimum load current for sense functionality, $V_{IN} = 0$, $T_j = -40\dots+150^\circ\text{C}$	$I_{L(MIN)}$	50	--	--	mA
Current sense settling time to I_{IS} static after input current positive slope, ¹⁸⁾ $I_L = 0$ \rightarrow 20 A, $T_j = -40\dots+150^\circ\text{C}$	$t_{son(IS)}$	--	250	500	∞s
Current sense settling time during on condition, ¹⁸⁾ $I_L = 10$ \rightarrow 20 A, $T_j = -40\dots+150^\circ\text{C}$	$t_{slc(IS)}$	--	50	100	∞s
Overvoltage protection $I_{bb} = 15$ mA $T_j = -40\dots+150^\circ\text{C}$:	$V_{Z,IS}$	63	67	--	V

¹⁶⁾ See also figures 4.x and 6.x on page 13 and 14.

¹⁷⁾ Fault conditions are overload during on (i.e. $V_{ON} > 1$ V typ.), overtemperature and short circuit; see also truth table on page 8.

¹⁸⁾ not subject to production test, specified by design

Parameter and Conditions at $T_j = 25$, $V_{bb} = 12$ V unless otherwise specified	Symbol	Values			Unit
		min	typ	max	
Input					
Required current capability of input switch $T_j = -40..+150^\circ\text{C}$:	$I_{IN(on)}$	--	1.4	2.2	mA
Input current for turn-off $T_j = -40..+150^\circ\text{C}$:	$I_{IN(off)}$	--	--	30	μA

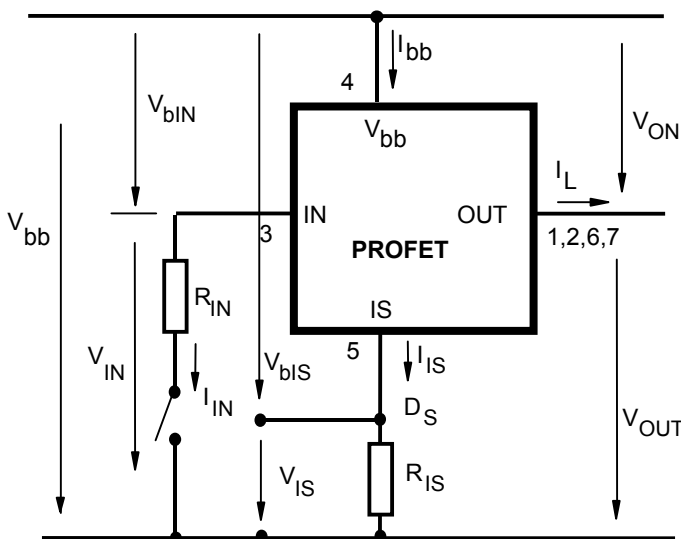
Truth Table

	Input Current level	Output level	Current Sense I_{IS}
Normal operation	L H	L H	≈ 0 ($I_{IS(LL)}$) nominal
Overload ¹⁹⁾	L H	L H	≈ 0 ($I_{IS(LL)}$) $I_{IS,fault}$
Short circuit to GND ²⁰⁾	L H	L L	≈ 0 ($I_{IS(LL)}$) $I_{IS,fault}$
Overtemperature	L H	L L	≈ 0 ($I_{IS(LL)}$) $I_{IS,fault}$
Short circuit to V_{bb}	L H	H H	≈ 0 ($I_{IS(LL)}$) <nominal ²¹⁾
Open load	L H	Z H	≈ 0 ($I_{IS(LL)}$) ≈ 0 ($I_{IS(LH)}$)

L = "Low" Level
H = "High" Level

Z = high impedance, potential depends on external circuit

Terms



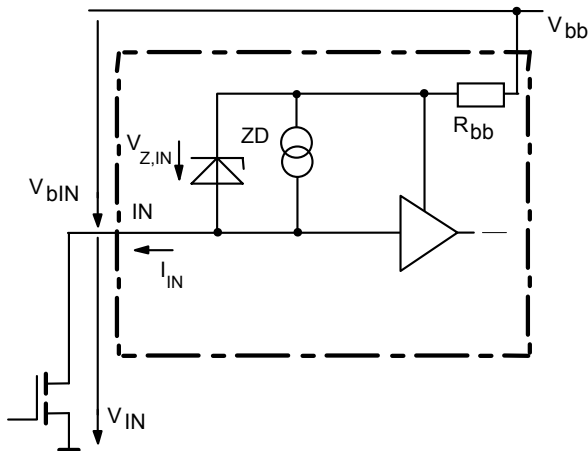
Two or more devices can easily be connected in parallel to increase load current capability.

¹⁹⁾ Overload is detected at the following condition: $1V$ (typ.) $< V_{ON} < 3.5V$ (typ.) . See also page 11.

²⁰⁾ Short Circuit is detected at the following condition: $V_{ON} > 3.5V$ (typ.) . See also page 11.

²¹⁾ Low ohmic short to V_{bb} may reduce the output current I_L and therefore also the sense current I_{IS} .

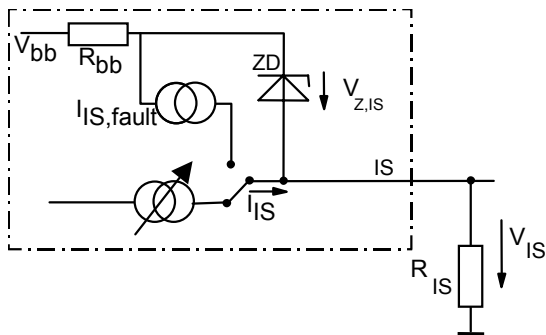
Input circuit (ESD protection)



ESD-Zener diode: 67 V typ., max 15 mA;

Current sense output

Normal operation

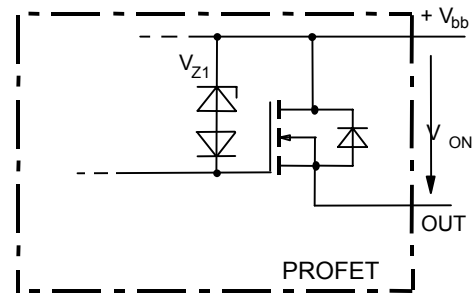


$V_{Z,IS} = 67V$ (typ.), $R_{IS} = 1k\Omega$ nominal (or $1k\Omega/n$, if n devices are connected in parallel). $I_S = I_L/k_{filis}$ can be only driven by the internal circuit as long as $V_{out} - V_{IS} > 5V$. Therefore R_{IS} should be less than

$$\frac{V_{bb} - 5V}{7.5mA}$$

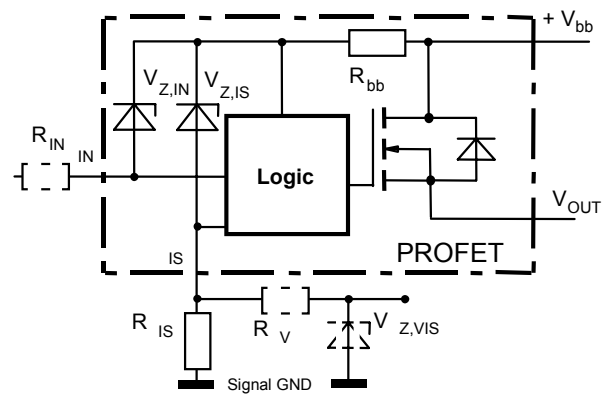
Note: For large values of R_{IS} the voltage V_{IS} can reach almost V_{bb} . See also overvoltage protection. If you don't use the current sense output in your application, you can leave it open.

Inductive and overvoltage output clamp



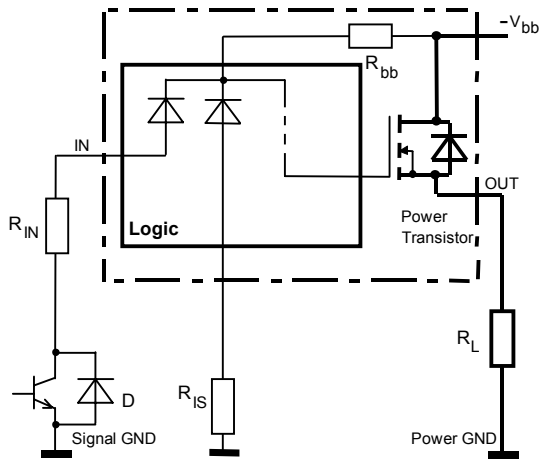
V_{ON} is clamped to $V_{ON(CI)} = 42V$ typ

Overvoltage protection of logic part



$R_{bb} = 100\Omega$ typ., $V_{Z,IN} = V_{Z,IS} = 67V$ typ., $R_{IS} = 1k\Omega$ nominal. Note that when overvoltage exceeds 67V typ. a voltage above 5V can occur between IS and GND, if $R_V, V_{Z,VIS}$ are not used.

Reversave™ (Reverse battery protection)

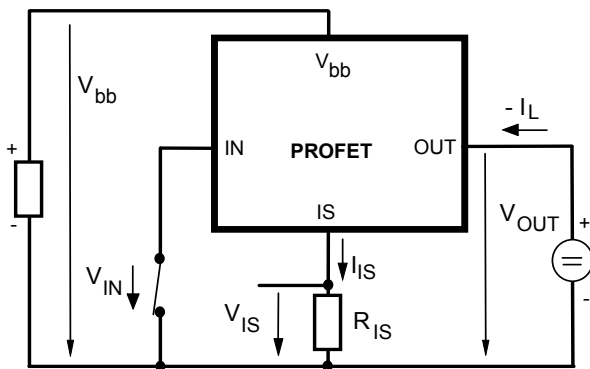


R_{IS} typ. 1 k Ω . Add R_{IN} for reverse battery protection in applications with V_{bb} above 16V;

recommended value: $\frac{1}{R_{IN}} + \frac{1}{R_{IS}} = \frac{0.08A}{|V_{bb}|-12V}$

To minimise power dissipation at reverse battery operation, the overall current into the IN and IS pin should be about 80mA. The current can be provided by using a small signal diode D in parallel to the input switch, by using a MOSFET input switch or by proper adjusting the current through R_{IS} . Since the current via R_{bb} generates additional heat in the device, this has to be taken into account in the overall thermal consideration.

Inverse load current operation



The device can be operated in inverse load current mode ($V_{OUT} > V_{bb} > 0V$). The current sense feature is not available during this kind of operation ($I_{IS} = 0$). In case of inverse operation the intrinsic drain source diode is eventually conducting resulting in considerably increased power dissipation.

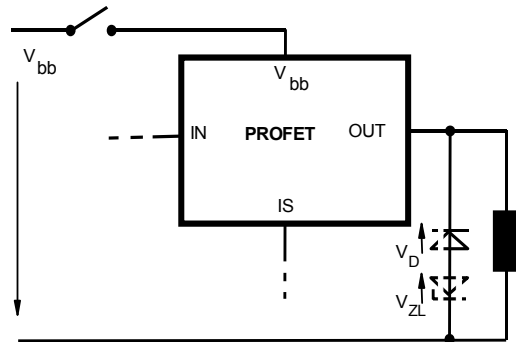
The transition from inverse to forward mode can result in a delayed switch on.

Note: *Temperature protection during inverse load current operation is not possible!*

V_{bb} disconnect with energised inductive load

Provide a current path with load current capability by using a diode, a Z-diode, or a varistor. ($V_{ZL} + V_D < 39V$ if $R_{IN} = 0$). For higher clamp voltages currents at IN and IS have to be limited to 120 mA.

Version a:

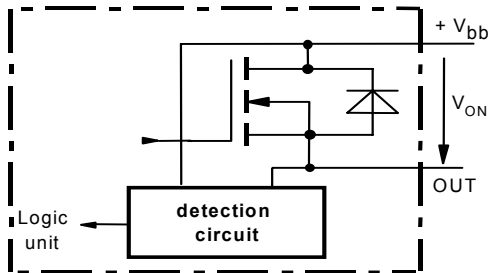


Short circuit detection

Fault Condition: $V_{ON} > V_{ON(SC)}$ (3.5V typ.) and $t > t_{d(SC)}$ (typ.650 μ s).

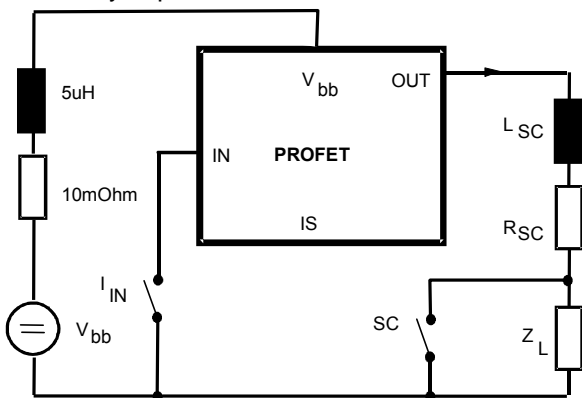
Overload detection

Fault Condition: $V_{ON} > 1V$ typ.

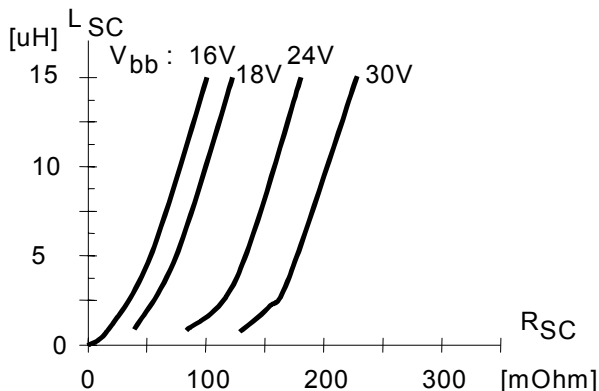


Short circuit

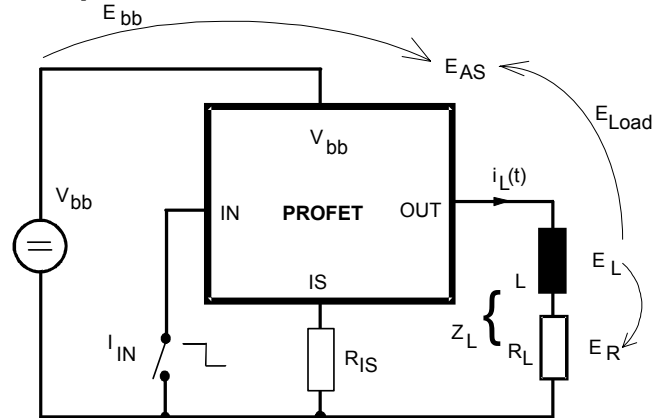
Short circuit is a combination of primary and secondary impedance's and a resistance's.



Allowable combinations of minimum, secondary resistance for full protection at given secondary inductance and supply voltage for single short circuit event:



Inductive load switch-off energy dissipation



Energy stored in load inductance:

$$E_L = 1/2 \cdot L \cdot I_L^2$$

While demagnetizing load inductance, the energy dissipated in PROFET is

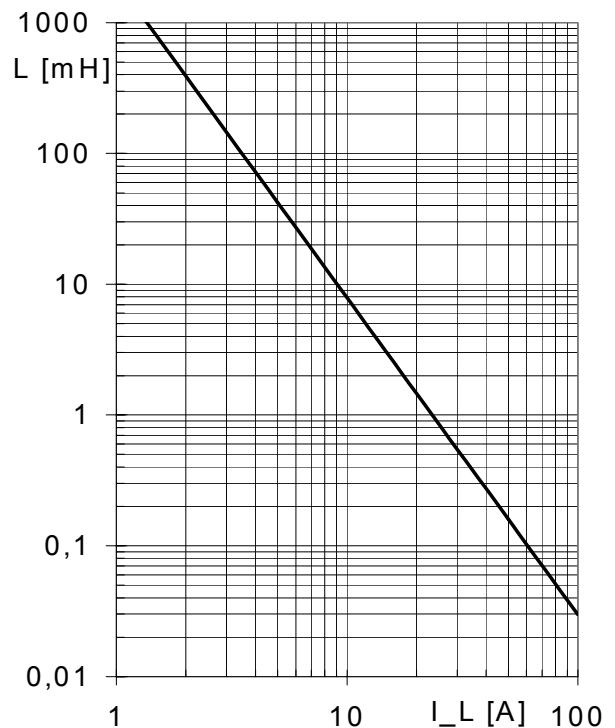
$$E_{AS} = E_{bb} + E_L - E_R = V_{ON(CL)} \cdot i_L(t) dt,$$

with an approximate solution for $R_L > 0\Omega$:

$$E_{AS} = \frac{I_L \cdot L}{2 \cdot R_L} (V_{bb} + |V_{OUT(CL)}|) \ln \left(1 + \frac{I_L \cdot R_L}{|V_{OUT(CL)}|} \right)$$

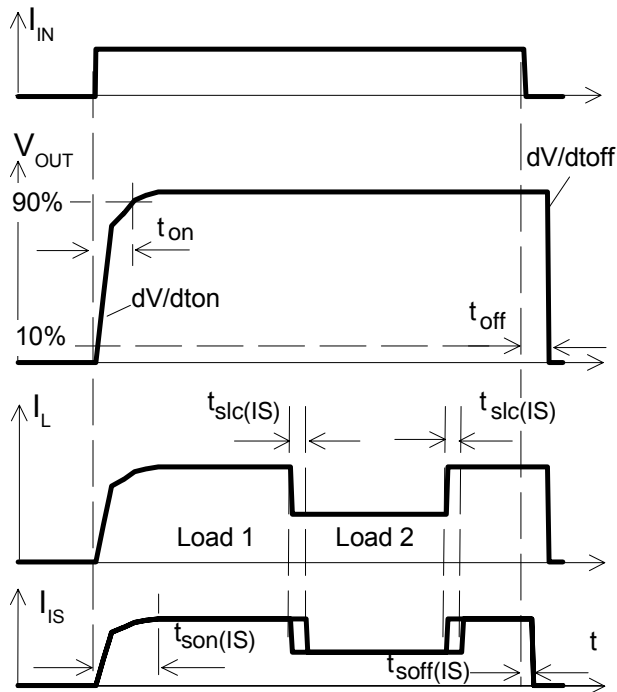
Maximum allowable load inductance for a single switch off

$L = f(I_L)$; $T_{j,start} = 150^\circ C$, $V_{bb} = 12V$, $R_L = 0\Omega$



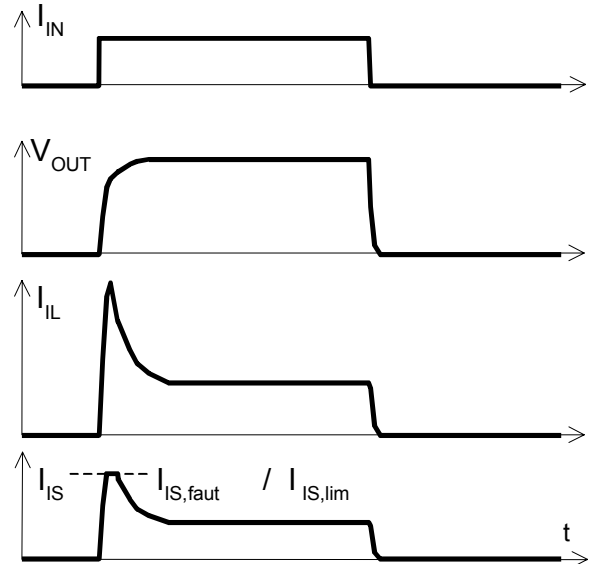
Timing diagrams

Figure 1a: Switching a resistive load, change of load current in on-condition:



The sense signal is not valid during a settling time after turn-on/off and after change of load current.

Figure 2a: Switching motors and lamps:



As long as $V_{bIS} < V_{Z,IS}$ the sense current will never exceed $I_{IS,fault}$ and/or $I_{IS,lim}$.

Figure 2b: Switching an inductive load:

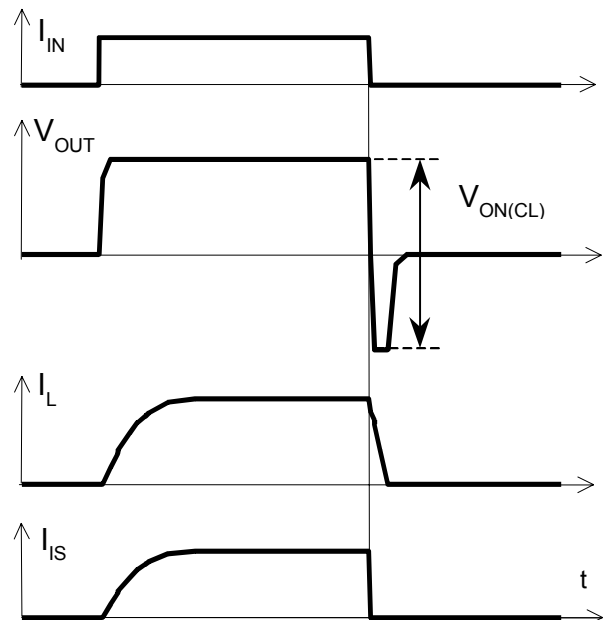
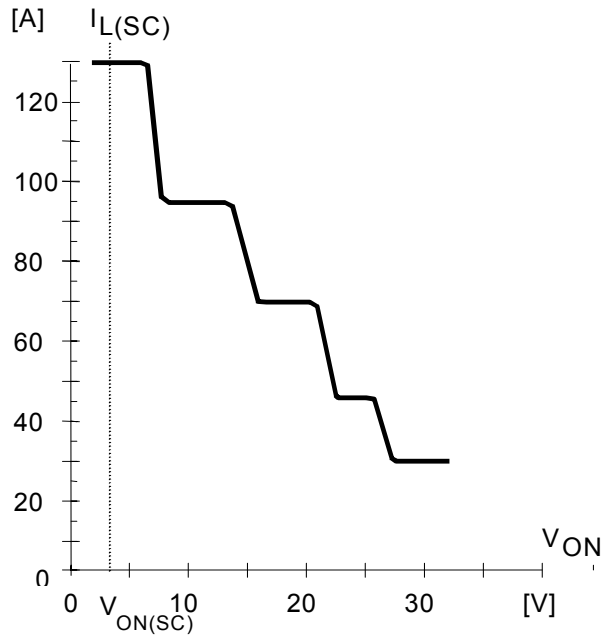
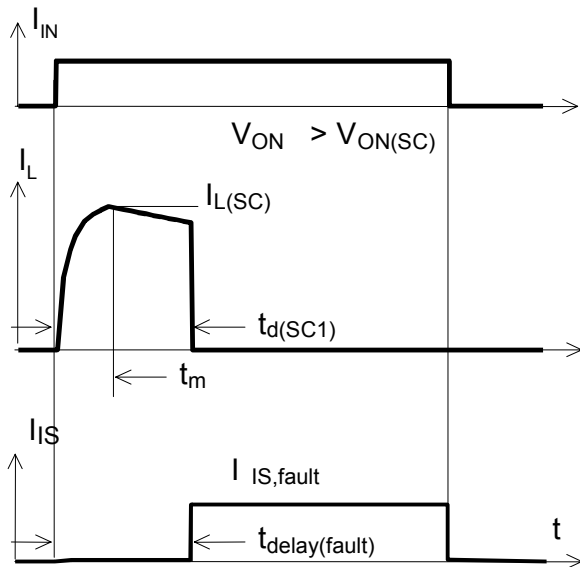


Figure 3a: Typ. current limitation characteristic



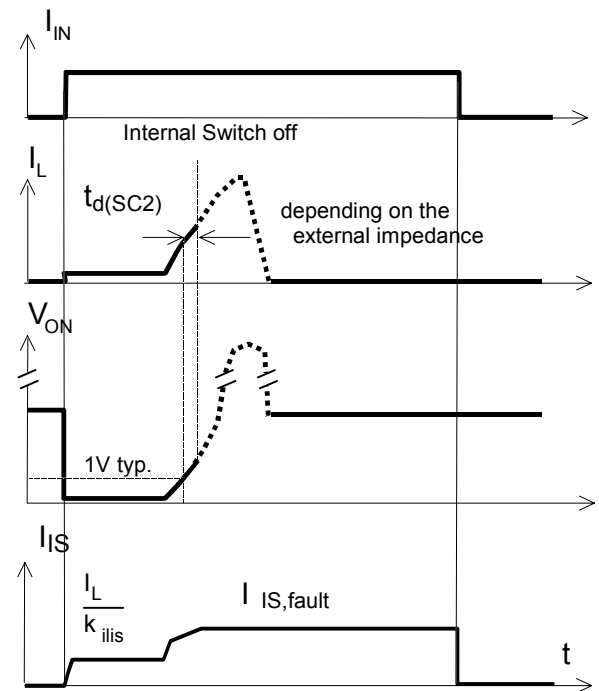
In case of $V_{ON} > V_{ON(SC)}$ (typ. 3.5 V) the device will be switched off by internal short circuit detection.

Figure 3b: Short circuit type one: shut down by short circuit detection, reset by $I_{IN} = 0$.



Shut down remains latched until next reset via input.

Figure 3c: Short circuit type two: shut down by short circuit detection, reset by $I_{IN} = 0$.



Shut down remains latched until next reset via input.

Figure 4a: Overtemperature Reset if $T_j < T_{jt}$

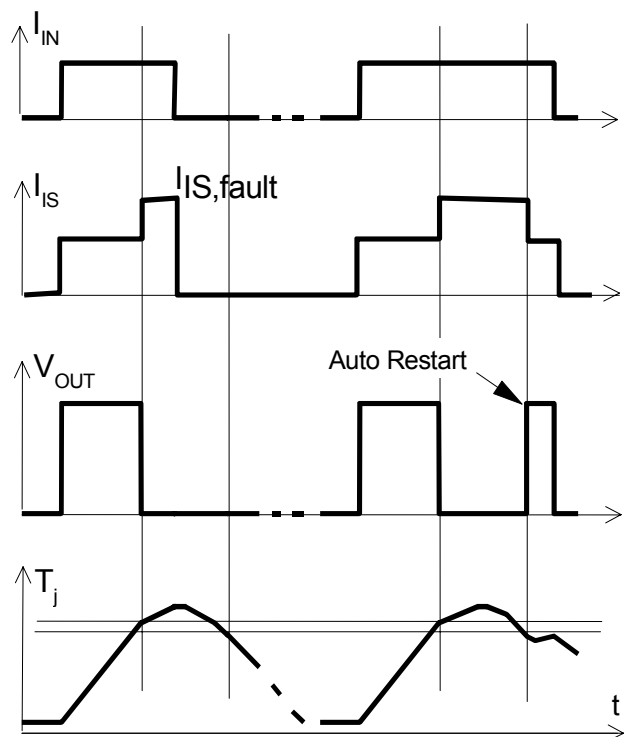


Figure 4b: Overload
 $T_j < T_{jt}$

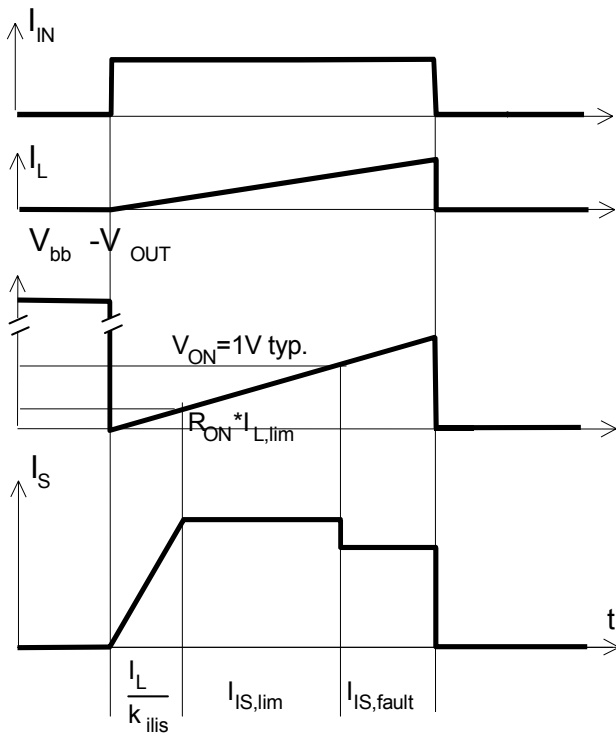


Figure 6a: Current sense versus load current:

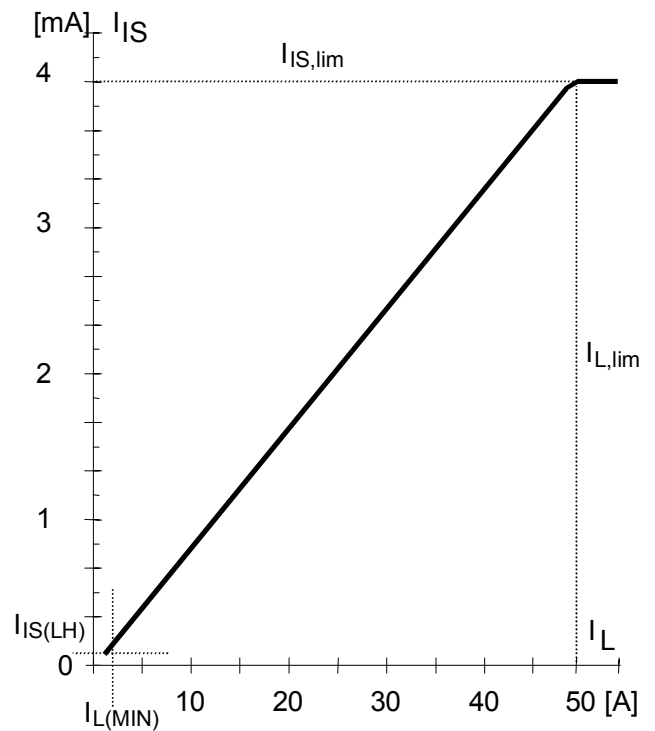


Figure 5a: Undervoltage restart of charge pump, overvoltage clamp

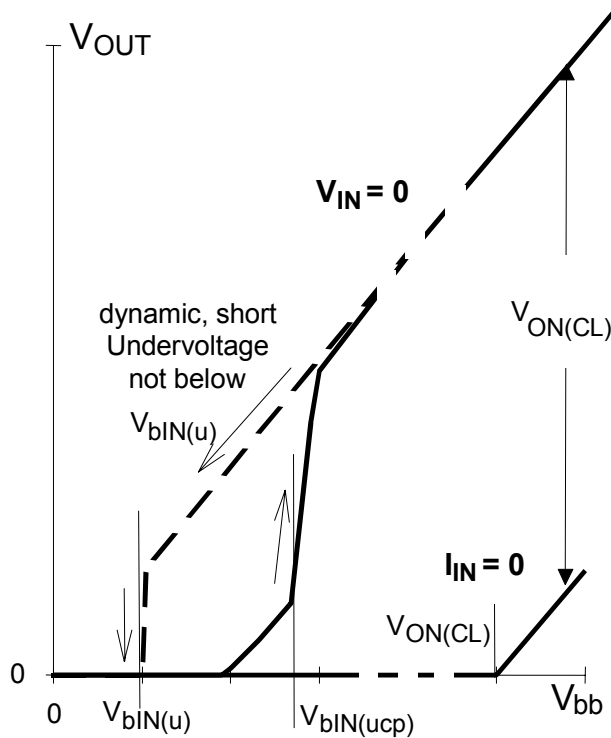
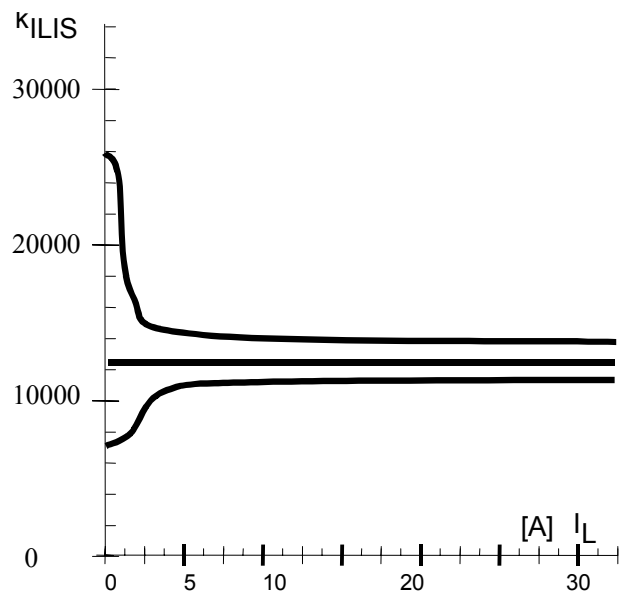


Figure 6b: Current sense ratio²²:



²² This range for the current sense ratio refers to all devices. The accuracy of the k_{ILIS} can be raised by means of calibration the value of k_{ILIS} for every single device.

Figure 7a: Output voltage drop versus load current:

