74AHCV245A

Octal bus transceiver; 3-state Rev. 2 — 8 November 2016

Product data sheet

General description 1.

The 74AHCV245A is an 8-bit transceiver with 3-state outputs and Schmitt trigger inputs. The device features an output enable (OE) and send/receive (DIR) for direction control. A HIGH on OE causes the outputs to assume a high-impedance OFF-state.

Inputs are overvoltage tolerant. This feature allows the use of these devices as translators in mixed voltage environments.

The data (An, Bn) and control (OE and DIR) inputs include Schmitt trigger inputs. These inputs can of transform slowly changing input signals into sharply defined, jitter-free output signals.

This device is ideal for driving bus lines or buffer memory address registers. It features inputs and outputs on opposite sides of the package to facilitate printed circuit board layout.

This device is fully specified for partial Power-down applications using I_{OFF}. The I_{OFF} circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

2. Features and benefits

- Wide supply voltage range from 1.8 V to 5.5 V
- Typical t_{pd} of 3.2 ns at 5 V
- Typical $V_{OL(p)}$ < 0.8 V at V_{CC} = 3.3 V, T_{amb} = 25 °C
- Typical $V_{OH(v)} > 2.3 \text{ V}$ at $V_{CC} = 3.3 \text{ V}$, $T_{amb} = 25 ^{\circ}\text{C}$
- Supports mixed-mode voltage operation on all ports
- I_{OFF} circuitry provides partial Power-down mode operation
- Latch-up performance exceeds 250 mA per JESD 78 Class II
- ESD protection:
 - ◆ HBM ANSI/ESDA/JEDEC JS-001 Class 2 exceeds 3 kV
 - MM JESD22-A115-A exceeds 200 V
 - CDM JESD22-C101E exceeds 2 kV
- Specified from −40 °C to +85 °C and from −40 °C to +125 °C

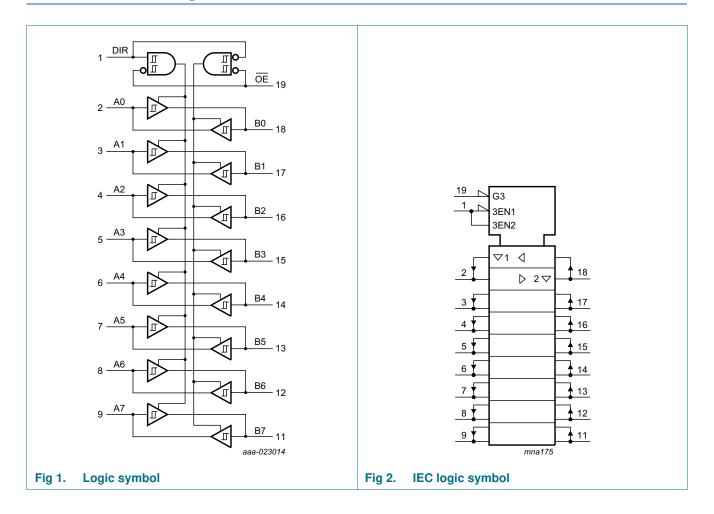


3. Ordering information

Table 1. Ordering information

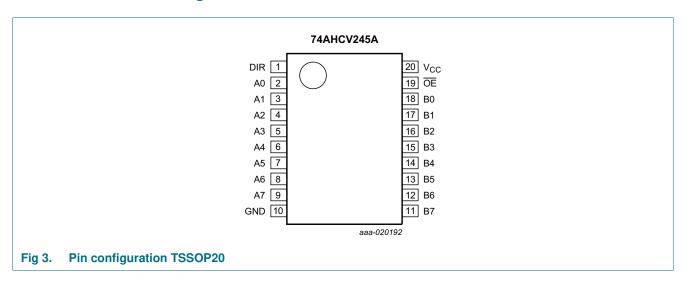
Type number	Package								
	Temperature range	Name	Description	Version					
74AHCV245APW	–40 °C to +125 °C	TSSOP20	plastic thin shrink small outline package; 20 leads; body width 4.4 mm	SOT360-1					

4. Functional diagram



5. Pinning information

5.1 Pinning



5.2 Pin description

Table 2. Pin description

Symbol	Pin	Description
DIR	1	direction control
A0 to A7	2, 3, 4, 5, 6, 7, 8, 9	data input/output
GND	10	ground (0 V)
B0 to B7	18, 17, 16, 15, 14, 13, 12, 11	data input/output
ŌĒ	19	output enable input (active LOW)
V _{CC}	20	supply voltage

6. Functional description

Table 3. Function table[1]

Input OE DIR		Input/output		
OE	DIR	An	Bn	
L	L	A = B	input	
L	Н	input	B = A	
Н	X	Z	Z	

^[1] H = HIGH voltage level; L = LOW voltage level; X = don't care; Z = high-impedance OFF-state.

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CC}	supply voltage			-0.5	+7.0	V
V _I	input voltage		[1]	-0.5	+7.0	V
V _O	output voltage	active mode	[2][3]	-0.5	V _{CC} + 0.5	V
		power-down or 3-state mode	[2]	-0.5	+7.0	V
I _{IK}	input clamping current	V _I < 0 V		-50	-	mA
I _{OK}	output clamping current	V _O < 0 V		-50	-	mA
Io	output current	$V_O = 0 \text{ V to } V_{CC}$		-	±50	mA
I _{CC}	supply current			-	100	mA
I _{GND}	ground current			-100	-	mA
T _{stg}	storage temperature			-65	+150	°C
P _{tot}	total power dissipation	$T_{amb} = -40 ^{\circ}\text{C} \text{ to } +125 ^{\circ}\text{C}$	[4]	-	500	mW

^[1] If the input current ratings are observed, the minimum input voltage ratings may be exceeded.

^[2] If the output current ratings are observed, the output voltage ratings may be exceeded.

^[3] This value is limited to 7.0 V maximum.

^[4] For TSSOP20 package: above 100 $^{\circ}$ C, the value of P_{tot} derates linearly with 10 mW/K.

8. Recommended operating conditions

Table 5. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		1.8	5.5	V
VI	input voltage		0	5.5	V
Vo	output voltage	active mode	0	V _{CC}	V
		power-down or 3-state mode	0	5.5	V
T _{amb}	ambient temperature		-40	+125	°C
Δt/ΔV	input transition rise and fall rate	V _{CC} = 2.3 V to 2.7 V	-	50	ms/V
		V _{CC} = 3.0 V to 3.6 V	-	20	ms/V
		V _{CC} = 4.5 V to 5.5 V	-	1	ms/V

9. Static characteristics

Table 6. Static characteristics

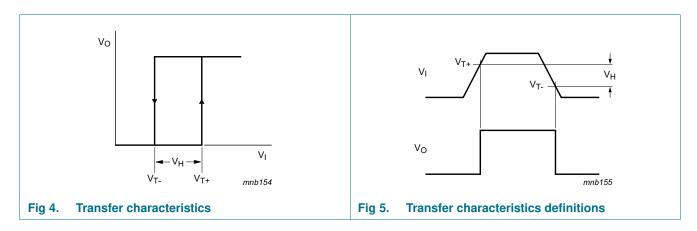
Voltages are referenced to GND (ground = 0 V).

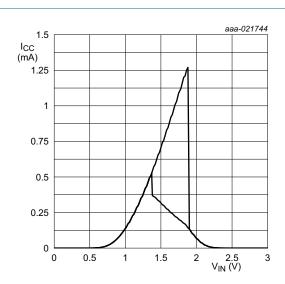
Symbol	Parameter	Conditions		25 °C		-40 °C to	+85 °C	-40 °C to	+125 °C	Unit
			Min	Тур	Max	Min	Max	Min	Max	
V_{T+}	positive-going	V _{CC} = 1.8 V	-	-	1.65	-	1.65	-	1.65	٧
	threshold	V _{CC} = 2.3 V	-	-	1.85	-	1.85	-	1.85	٧
	voltage	V _{CC} = 3.0 V	-	-	2.2	-	2.2	-	2.2	٧
		V _{CC} = 4.5 V	-	-	3.15	-	3.15	-	3.15	٧
		V _{CC} = 5.5 V	-	-	3.85	-	3.85	-	3.85	٧
V_{T-}	negative-going	V _{CC} = 1.8 V	0.15	-	-	0.15	-	0.15	-	٧
	threshold	V _{CC} = 2.3 V	0.45	-	-	0.45	-	0.45	-	٧
	voltage	V _{CC} = 3.0 V	0.9	-	-	0.9	-	0.9	-	٧
		V _{CC} = 4.5 V	1.35	-	-	1.35	-	1.35	-	٧
		V _{CC} = 5.5 V	1.65	-	-	1.65	-	1.65	-	٧
V_{H}	hysteresis	V _{CC} = 1.8 V	0.15	-	1.05	0.15	1.05	0.15	1.05	٧
	voltage	V _{CC} = 2.3 V	0.2	-	1.1	0.2	1.1	0.2	1.1	٧
		V _{CC} = 3.0 V	0.3	-	1.2	0.3	1.2	0.3	1.2	V
		V _{CC} = 4.5 V	0.4	-	1.4	0.4	1.4	0.4	1.4	٧
		V _{CC} = 5.5 V	0.5	-	1.6	0.5	1.6	0.5	1.6	٧
V _{OH}	HIGH-level	$V_I = V_{T+}$ or V_{T-}								V
	output voltage	$I_O = -50 \mu A$; $V_{CC} = 1.8 \text{ V}$	1.7	1.8	-	1.7	-	1.7	-	V
		$I_O = -50 \mu A; V_{CC} = 3.0 \text{ V}$	2.9	3.0	-	2.9	-	2.9	-	٧
		$I_O = -50 \mu A$; $V_{CC} = 4.5 \text{ V}$	4.4	4.5	-	4.4	-	4.4	-	٧
		$I_{O} = -8 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.58	-	-	2.48	-	2.48	-	V
		$I_{O} = -16 \text{ mA}; V_{CC} = 4.5 \text{ V}$	3.94	-	-	3.80	-	3.80	-	

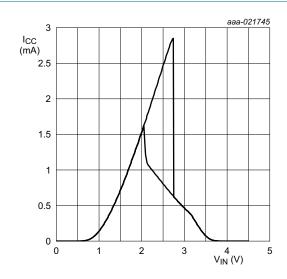
Table 6. Static characteristics ...continued Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		25 °C		-40 °C t	o +85 °C	–40 °C to	+125 °C	Unit
			Min	Тур	Max	Min	Max	Min	Max	
V_{OL}	LOW-level	$V_I = V_{T+}$ or V_{T-}								
	output voltage	$I_O = 50 \mu A; V_{CC} = 1.8 V$	-	0	0.1	-	0.1	-	0.1	V
		$I_O = 50 \mu A; V_{CC} = 3.0 V$	-	0	0.1	-	0.1	-	0.1	V
		$I_O = 50 \mu A; V_{CC} = 4.5 V$	-	0	0.1	-	0.1	-	0.1	V
		$I_O = 8 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.36	-	0.44	-	0.44	V
		$I_O = 16 \text{ mA}; V_{CC} = 4.5 \text{ V}$	-	-	0.44	-	0.55	-	0.55	٧
I _{OZ}	OFF-state output current	$V_{CC} = 5.5 \text{ V}; V_I = V_{IH} \text{ or } V_{IL};$ $V_O = \text{GND to } 5.5 \text{ V}$	-	-	±0.25	-	±2.5	-	±2.5	μА
I _{OFF}	power-off leakage current	V_I or V_O = GND to 5.5 V; V_{CC} = 0 V	-	-	0.5	-	5	-	5	μΑ
I _I	input leakage current	$V_I = V_{CC}$ or GND; $V_{CC} = 0$ V to 5.5 V	-	-	±0.1	-	±1	-	±1	μΑ
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5$ V	-	-	2	-	20	-	20	μΑ

9.1 Transfer characteristics waveforms

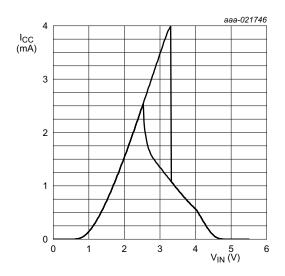






a. $V_{CC} = 3.0 \text{ V}$





c. $V_{CC} = 5.5 \text{ V}$

Fig 6. Typical transfer characteristics

10. Dynamic characteristics

Table 7. Dynamic characteristics

GND = 0 V. For test circuit, see Figure 9.

Symbol	Parameter	Conditions		25 °C		-40 °C	to +85 °C	-40 °C to +125 °C		Unit
			Min	Typ[1]	Max	Min	Max	Min	Max	
t _{pd}	propagation delay	An to Bn or Bn to An; see [2] Figure 7								
		V _{CC} = 2.3 V to 2.7 V								
		C _L = 15 pF	-	5.3	13	1	15	1	16.3	ns
		C _L = 50 pF	-	7.3	15.9	1	18	1	19.4	ns
		V _{CC} = 3.0 V to 3.6 V								
		C _L = 15 pF	-	4.1	8.4	1	10	1	10.9	ns
		C _L = 50 pF	-	5.7	11.9	1	13.5	1	14.6	ns
		V _{CC} = 4.5 V to 5.5 V								
		C _L = 15 pF	-	3.2	5.5	1	6.5	1	7	ns
		C _L = 50 pF	-	4.5	7.5	1	8.5	1	9.3	ns
t _{en}	enable time	OE to An or OE to Bn; see [2] Figure 8								
		V _{CC} = 2.3 V to 2.7 V								
		C _L = 15 pF	-	6.5	19.9	1	22	1	23	ns
		C _L = 50 pF	-	8.6	22.7	1	26	1	27	ns
		V _{CC} = 3.0 V to 3.6 V								
		C _L = 15 pF	-	4.9	13.2	1	15.5	1	16	ns
		C _L = 50 pF	-	6.6	16.7	1	19	1	20	ns
		V _{CC} = 4.5 V to 5.5 V								
		C _L = 15 pF	-	3.7	8.5	1	10	1	10.5	ns
		C _L = 50 pF	-	5.1	10.6	1	12	1	12.5	ns
t _{dis}	disable time	OE to An or OE to Bn; see [2] Figure 8								
		V _{CC} = 2.3 V to 2.7 V								
		C _L = 15 pF	-	7	15.1	1	16.7	1	17.8	ns
		C _L = 50 pF	-	11.6	23.1	1	25	1	26	ns
		V _{CC} = 3.0 V to 3.6 V								
		C _L = 15 pF	-	5.7	10.5	1	12.3	1	13.2	ns
		C _L = 50 pF	-	9.1	15.8	1	18	1	19	ns
		V _{CC} = 4.5 V to 5.5 V								
		C _L = 15 pF	-	4.5	6.9	1	8	1	8.5	ns
		C _L = 50 pF	-	6.7	9.7	1	11	1	11.5	ns
t _{sk(o)}	output skew	C _L = 50 pF								
	time	V _{CC} = 2.3 V to 2.7 V	-	-	2	-	2	-	2	ns
		V _{CC} = 3.0 V to 3.6 V	-	-	1.5	-	1.5	-	1.5	ns
		V _{CC} = 4.5 V to 5.5 V	-	-	1	-	1	-	1	ns

 Table 7.
 Dynamic characteristics ...continued

GND = 0 V. For test circuit, see Figure 9.

Symbol	Parameter	Conditions	25 °C		-40 °C to +85 °C		-40 °C to +125 °C		Unit	
			Min	Typ[1]	Max	Min	Max	Min	Max	
Cı	input capacitance	$V_{I} = V_{CC}$ or GND; $V_{CC} = 3.3 \text{ V}$	-	2	6	-	6	-	6	pF
C _{I/O}	input/output capacitance	$V_O = V_{CC}$ or GND; $V_{CC} = 3.3 \text{ V}$	-	5.5	-	-	-	-	-	pF
C _{PD}	power dissipation capacitance	per buffer; [3] $C_L = 0$ pF; $f = 10$ MHz; $V_{CC} = 5$ V; $V_I = GND$ to V_{CC}	-	13.8	-	-	-	-	-	pF

^[1] Typical values are measured at $T_{amb} = 25$ °C and $V_{CC} = 2.5$ V, 3.3 V, and 5 V respectively, unless otherwise specified.

[2] t_{pd} is the same as t_{PLH} and t_{PHL} .

 t_{en} is the same as t_{PZL} and t_{PZH} .

 t_{dis} is the same as t_{PLZ} and t_{PHZ} .

[3] C_{PD} is used to determine the dynamic power dissipation P_D (μW).

 $P_D = C_{PD} \times V_{CC}^2 \times f_i + \sum (C_L \times V_{CC}^2 \times f_o)$ where:

f_i = input frequency in MHz;

f_o = output frequency in MHz;

 C_L = output load capacitance in pF;

V_{CC} = supply voltage in Volts.

Table 8. Noise characteristics

GND = 0 V. For test circuit, see Figure 9.

Symbol	Parameter	Conditions	T	T _{amb} = 25 °C			
			Min	Тур	Max		
$V_{CC} = 3.3$	V; C _L = 50 pF		1				
$V_{OL(p)}$	LOW-level output voltage (peak)		-	0.3	0.8	٧	
$V_{OL(v)}$	LOW-level output voltage (valley)		-0.8	-0.2	-	٧	
$V_{OH(v)}$	HIGH-level output voltage (valley)		-	2.9	-	٧	
V _{IH(AC)}	AC HIGH-level input voltage	dynamic	2.31	-	-	٧	
V _{IL(AC)}	AC LOW-level input voltage	dynamic	-	-	0.99	٧	
$V_{CC} = 5.0$	V; C _L = 50 pF		1				
$V_{OL(p)}$	LOW-level output voltage (peak)		-	0.6	1.5	V	
$V_{OL(v)}$	LOW-level output voltage (valley)		-1.5	-0.6	-	٧	
$V_{OH(v)}$	HIGH-level output voltage (valley)		-	4.0	-	٧	
V _{IH(AC)}	AC HIGH-level input voltage	dynamic	3.5	-	-	٧	
V _{IL(AC)}	AC LOW-level input voltage	dynamic	-	-	1.5	V	

11. Waveforms

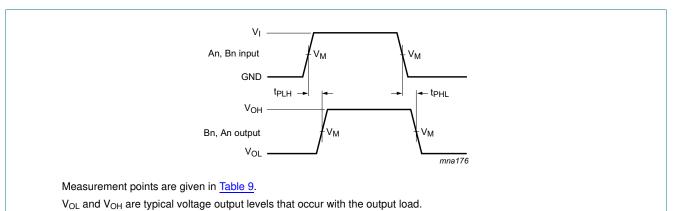


Fig 7. Propagation delay input (An, Bn) to output (Bn, An)

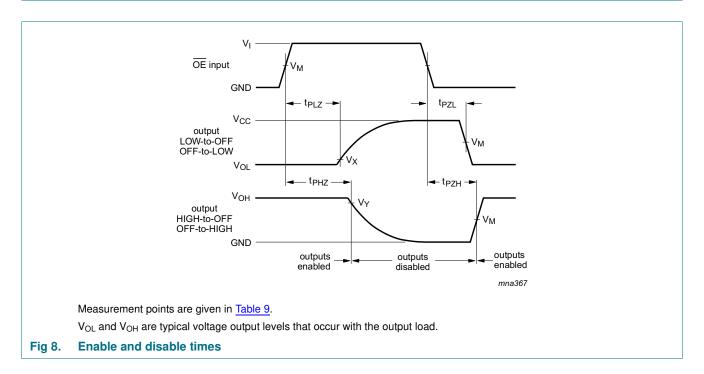
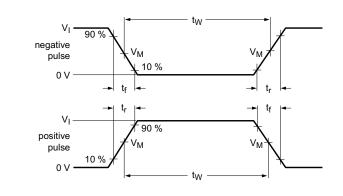
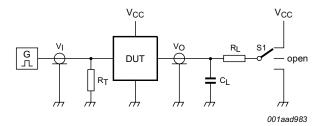


Table 9. Measurement points

Input	Output		
V_{M}	V _M	V _X	V _Y
0.5V _{CC}	0.5V _{CC}	V _{OL} + 0.3 V	V _{OH} – 0.3 V





Test data is given in Table 10.

Definitions test circuit:

 R_T = Termination resistance should be equal to output impedance Z_0 of the pulse generator

 C_L = Load capacitance including jig and probe capacitance

R_L = Load resistor

S1 = Test selection switch

Fig 9. Test circuit for measuring switching times

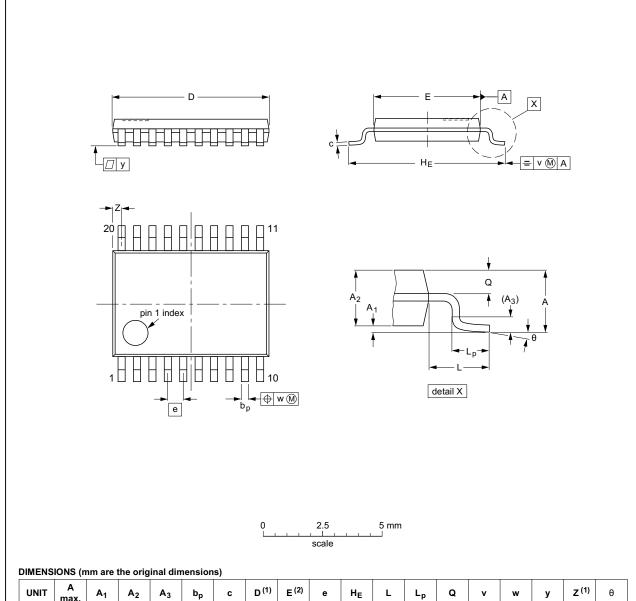
Table 10. Test data

Input		Load		S1 position			
VI	t _r , t _f	CL	R _L	t _{PHL} , t _{PLH}	t _{PZH} , t _{PHZ}	t _{PZL} , t _{PLZ}	
GND to V_{CC}	3.0 ns	15 pF, 50 pF	1 kΩ	open	GND	V _{CC}	

12. Package outline

TSSOP20: plastic thin shrink small outline package; 20 leads; body width 4.4 mm

SOT360-1



UNIT	A max.	A ₁	A ₂	A ₃	bp	С	D ⁽¹⁾	E (2)	е	HE	L	Lp	Q	٧	w	у	Z ⁽¹⁾	θ
mm	1.1	0.15 0.05	0.95 0.80	0.25	0.30 0.19	0.2 0.1	6.6 6.4	4.5 4.3	0.65	6.6 6.2	1	0.75 0.50	0.4 0.3	0.2	0.13	0.1	0.5 0.2	8° 0°

Notes

- 1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.
- 2. Plastic interlead protrusions of 0.25 mm maximum per side are not included.

OUTLINE		REFER	EUROPEAN	ISSUE DATE			
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE	
SOT360-1		MO-153				99-12-27 03-02-19	
	VERSION	VERSION IEC	VERSION IEC JEDEC	VERSION IEC JEDEC JEITA	VERSION IEC JEDEC JEITA	VERSION IEC JEDEC JEITA PROJECTION	

Fig 10. Package outline SOT360-1 (TSSOP20)

74AHCV245A

All information provided in this document is subject to legal disclaimers.

© Nexperia B.V. 2017. All rights reserve

13. Abbreviations

Table 11. Abbreviations

Acronym	Description
CDM	Charge Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MM	Machine Model
TTL	Transistor-Transistor Logic

14. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes		
74AHCV245A v.2	20161108	Product data sheet	-	74AHCV245A v.1		
Modifications:	Type number 74AHCV245ABQ removed.					
74AHCV245A v.1	20160610	Product data sheet	-	-		

15. Legal information

15.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nexperia.com.

15.2 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

15.3 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia.

In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of Nexperia.

Right to make changes — Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — Nexperia products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of a Nexperia product can reasonably be expected to result in personal injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — Nexperia products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nexperia.com/profile/terms, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

74AHCV245A

All information provided in this document is subject to legal disclaimers.

© Nexperia B.V. 2017. All rights reserved

14 of 16

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Non-automotive qualified products — Unless this data sheet expressly states that this specific Nexperia product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. Nexperia accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without Nexperia's warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond

Nexperia's specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies Nexperia for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond Nexperia's standard warranty and Nexperia's product specifications.

Translations — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

15.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

16. Contact information

For more information, please visit: http://www.nexperia.com

For sales office addresses, please send an email to: salesaddresses@nexperia.com

Nexperia

74AHCV245A

Octal bus transceiver; 3-state

17. Contents

1	General description
2	Features and benefits
3	Ordering information
4	Functional diagram
5	Pinning information 3
5.1	Pinning
5.2	Pin description
6	Functional description4
7	Limiting values
8	Recommended operating conditions
9	Static characteristics
9.1	Transfer characteristics waveforms 6
10	Dynamic characteristics
11	Waveforms
12	Package outline
13	Abbreviations13
14	Revision history
15	Legal information14
15.1	Data sheet status
15.2	Definitions14
15.3	Disclaimers
15.4	Trademarks
16	Contact information
17	Contents