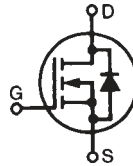


# Polar™ Power MOSFET

## IXTY1N100P IXTA1N100P IXTP1N100P

$V_{DSS} = 1000V$   
 $I_{D25} = 1A$   
 $R_{DS(on)} \leq 15\Omega$

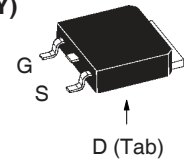
N-Channel Enhancement Mode  
Avalanche Rated



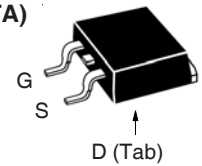
Symbol	Test Conditions	Maximum Ratings	
$V_{DSS}$	$T_J = 25^\circ C$ to $150^\circ C$	1000	V
$V_{DGR}$	$T_J = 25^\circ C$ to $150^\circ C$ , $R_{GS} = 1M\Omega$	1000	V
$V_{GSS}$	Continuous	$\pm 20$	V
$V_{GSM}$	Transient	$\pm 30$	V
$I_{D25}$	$T_C = 25^\circ C$	1.0	A
$I_{DM}$	$T_C = 25^\circ C$ , Pulse Width Limited by $T_{JM}$	1.8	A
$I_A$	$T_C = 25^\circ C$	1.0	A
$E_{AS}$	$T_C = 25^\circ C$	100	mJ
$dv/dt$	$I_S \leq I_{DM}$ , $V_{DD} \leq V_{DSS}$ , $T_J \leq 150^\circ C$	10	V/ns
$P_D$	$T_C = 25^\circ C$	50	W
$T_J$		-55 ... +150	$^\circ C$
$T_{JM}$		150	$^\circ C$
$T_{stg}$		-55 ... +150	$^\circ C$
$T_L$	Maximum Lead Temperature for Soldering	300	$^\circ C$
$T_{SOLD}$	1.6 mm (0.062in.) from Case for 10s	260	$^\circ C$
$F_C$	Mounting Force (TO-263)	10..65 / 2.2..14.6	N/lb
$M_d$	Mounting Torque (TO-220)	1.13 / 10	Nm/lb.in
<b>Weight</b>	TO-252	0.35	g
	TO-263	2.50	g
	TO-220	3.00	g

Symbol	Test Conditions ( $T_J = 25^\circ C$ , Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
$BV_{DSS}$	$V_{GS} = 0V$ , $I_D = 250\mu A$	1000		V
$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = 50\mu A$	2.5		4.5 V
$I_{GSS}$	$V_{GS} = \pm 20V$ , $V_{DS} = 0V$			$\pm 50$ nA
$I_{DSS}$	$V_{DS} = V_{DSS}$ , $V_{GS} = 0V$ $T_J = 125^\circ C$			5 $\mu A$ 100 $\mu A$
$R_{DS(on)}$	$V_{GS} = 10V$ , $I_D = 0.5 \cdot I_{D25}$ , Note 1		12.2	15.0 $\Omega$

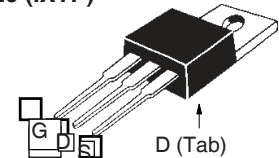
TO-252 (IXTY)



TO-263 (IXTA)



TO-220 (IXTP)



G = Gate      D = Drain  
S = Source    Tab = Drain

### Features

- International Standard Packages
- Low  $Q_G$
- Avalanche Rated
- Low Package Inductance
- Fast Intrinsic Rectifier

### Advantages

- High Power Density
- Easy to Mount
- Space Savings

### Applications

- DC-DC Converters
- Switch-Mode and Resonant-Mode Power Supplies
- AC and DC Motor Drives
- Lasers Drivers
- Robotics and Servo Controls

Symbol	Test Conditions ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max
$g_{fs}$	$V_{DS} = 30\text{V}$ , $I_D = 0.5 \cdot I_{D25}$ , Note 1	0.45	0.78	S
$C_{iss}$	$V_{GS} = 0\text{V}$ , $V_{DS} = 25\text{V}$ , $f = 1\text{MHz}$		331	pF
$C_{oss}$			24	pF
$C_{rss}$			5.5	pF
$Q_{g(on)}$	$V_{GS} = 10\text{V}$ , $V_{DS} = 0.5 \cdot V_{DSS}$ , $I_D = 0.5 \cdot I_{D25}$		15.5	nC
$Q_{gs}$			4.1	nC
$Q_{gd}$			8.0	nC
$t_{d(on)}$	<b>Resistive Switching Times</b> $V_{GS} = 10\text{V}$ , $V_{DS} = 0.5 \cdot V_{DSS}$ , $I_D = 0.5 \cdot I_{D25}$ $R_G = 50\Omega$ (External)		20	ns
$t_r$			26	ns
$t_{d(off)}$			55	ns
$t_f$			24	ns
$R_{thJC}$				2.5 °C/W
$R_{thCS}$	TO-220	0.50		°C/W

#### Source-Drain Diode

Symbol	Test Conditions ( $T_J = 25^\circ\text{C}$ , Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max
$I_S$	$V_{GS} = 0\text{V}$			1.0 A
$I_{SM}$	Repetitive, Pulse Width Limited by $T_{JM}$			3.0 A
$V_{SD}$	$I_F = I_S$ , $V_{GS} = 0\text{V}$ , Note 1			1.5 V
$t_{rr}$	$I_F = 1\text{A}$ , $-di/dt = 100\text{A}/\mu\text{s}$ , $V_R = 100\text{V}$		750	ns

Note 1. Pulse test,  $t \leq 300\mu\text{s}$ , duty cycle,  $d \leq 2\%$ .

IXYS Reserves the Right to Change Limits, Test Conditions, and Dimensions.

IXYS MOSFETs and IGBTs are covered by one or more of the following U.S. patents:	4,835,592	4,931,844	5,049,961	5,237,481	6,162,665	6,404,065B1	6,683,344	6,727,585	7,005,734B2	7,157,338B2
	4,860,072	5,017,508	5,063,307	5,381,025	6,259,123B1	6,534,343	6,710,405B2	6,759,692	7,063,975B2	
	4,881,106	5,034,796	5,187,117	5,486,715	6,306,728B1	6,583,505	6,710,463	6,771,478B2	7,071,537	

Fig. 1. Output Characteristics @  $T_J = 25^\circ\text{C}$

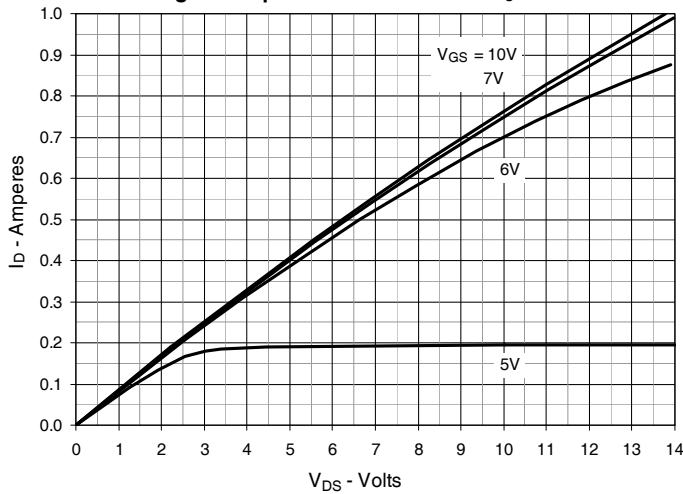


Fig. 2. Extended Output Characteristics @  $T_J = 25^\circ\text{C}$

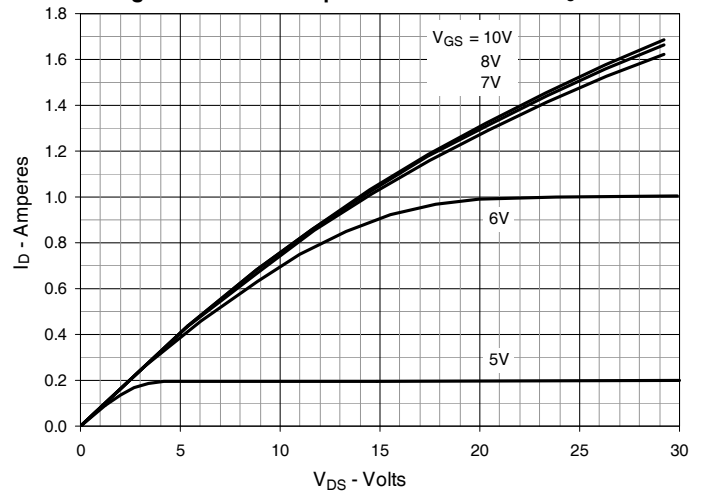


Fig. 3. Output Characteristics @  $T_J = 125^\circ\text{C}$

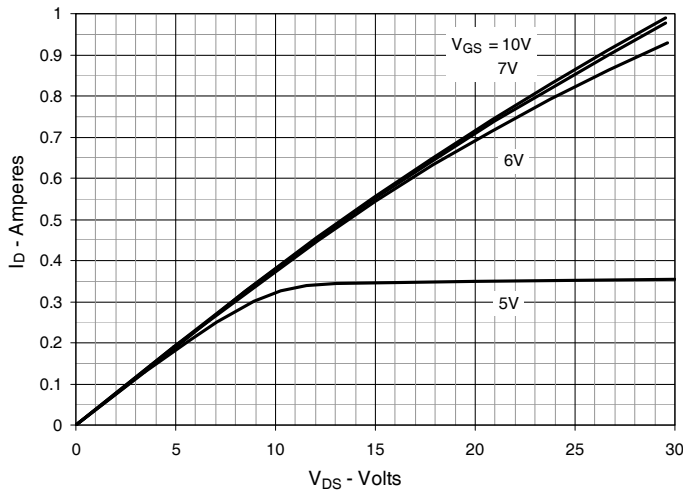


Fig. 4.  $R_{DS(on)}$  Normalized to  $I_D = 0.5\text{A}$  Value vs. Junction Temperature

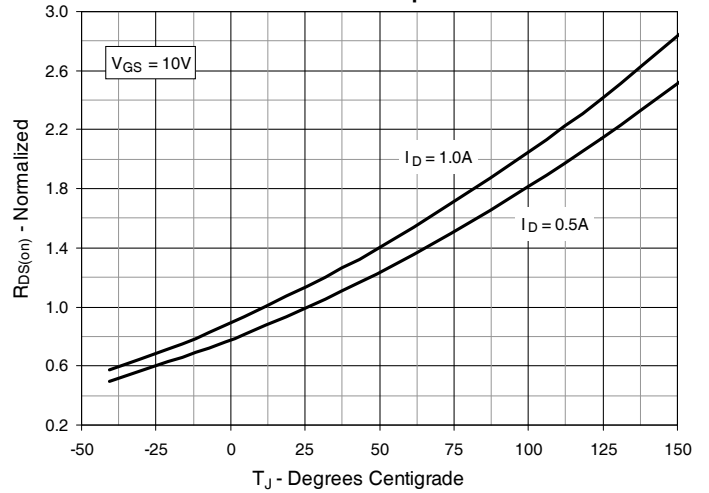


Fig. 5.  $R_{DS(on)}$  Normalized to  $I_D = 0.5\text{A}$  Value vs. Drain Current

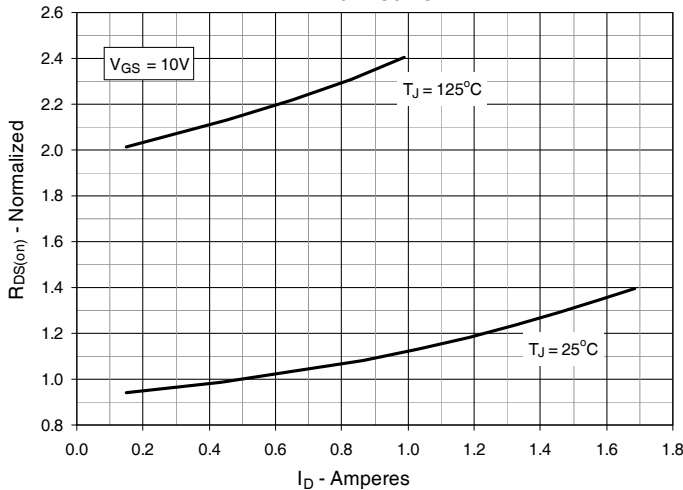


Fig. 6. Maximum Drain Current vs. Case Temperature

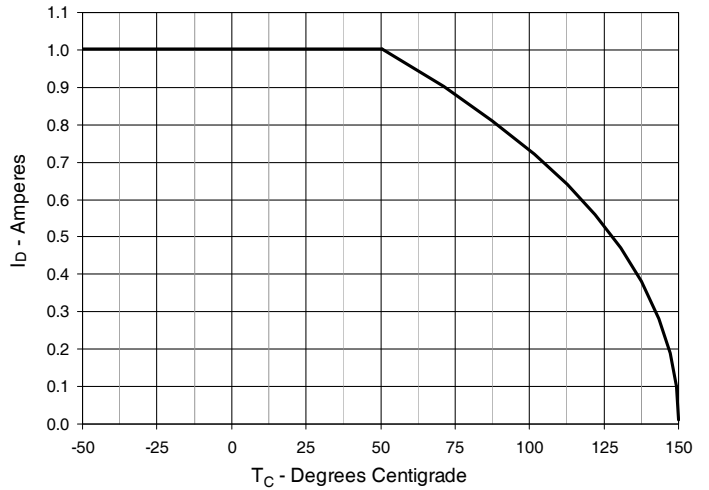


Fig. 7. Input Admittance

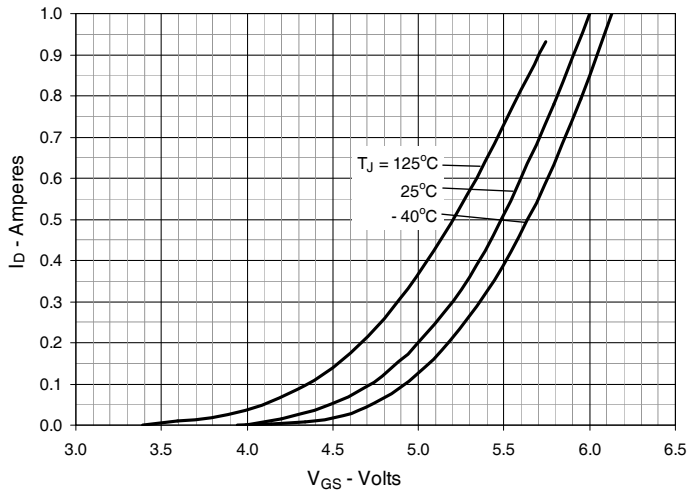


Fig. 8. Transconductance

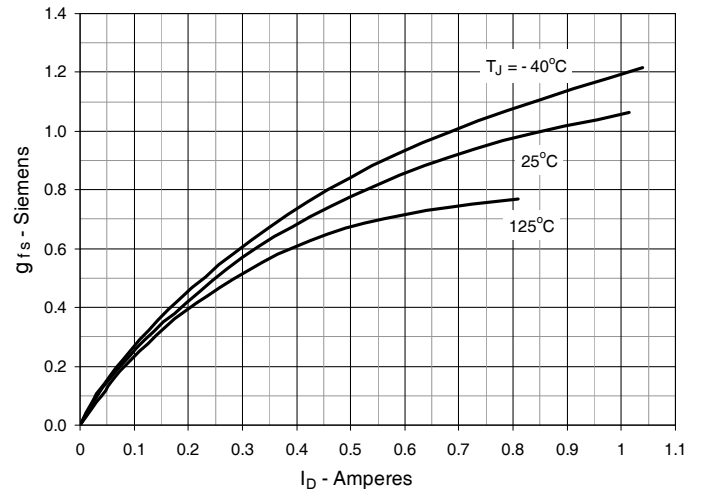


Fig. 9. Forward Voltage Drop of Intrinsic Diode

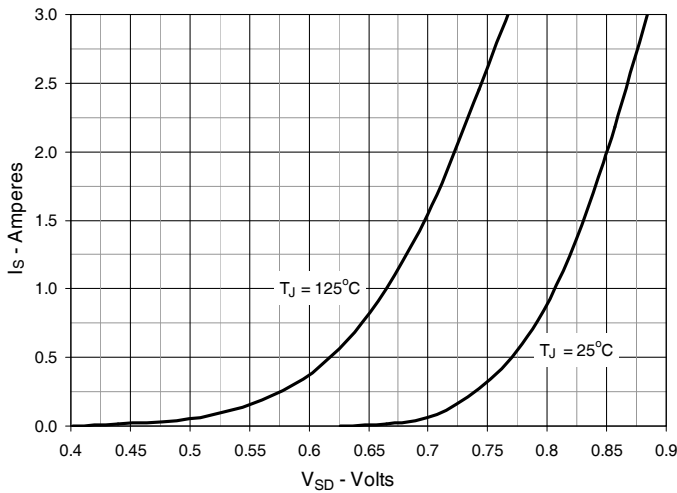


Fig. 10. Gate Charge

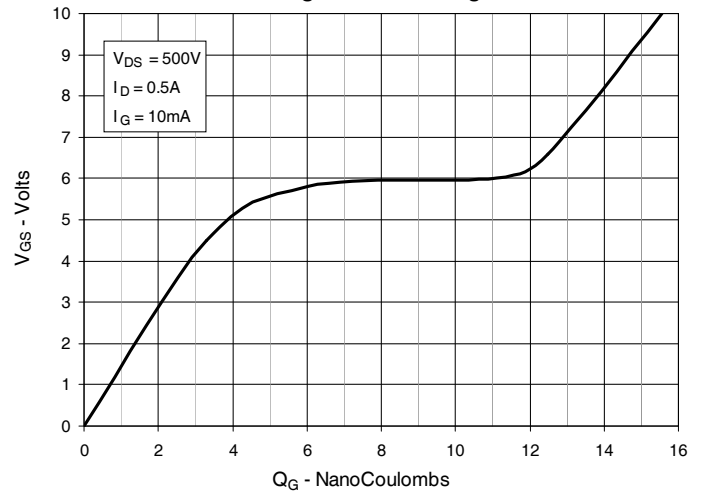


Fig. 11. Capacitance

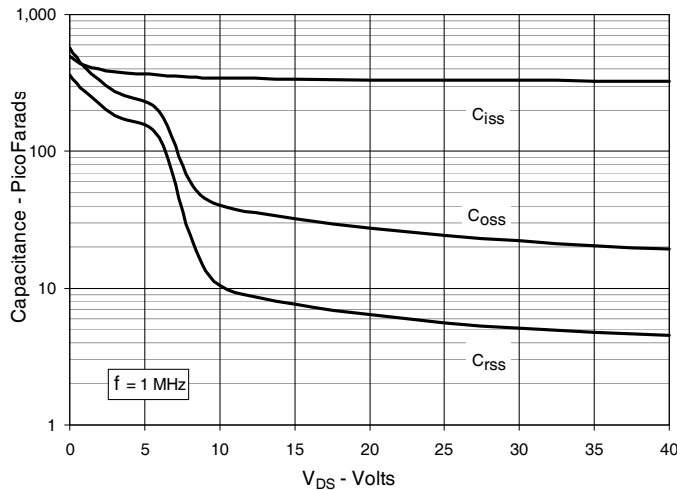
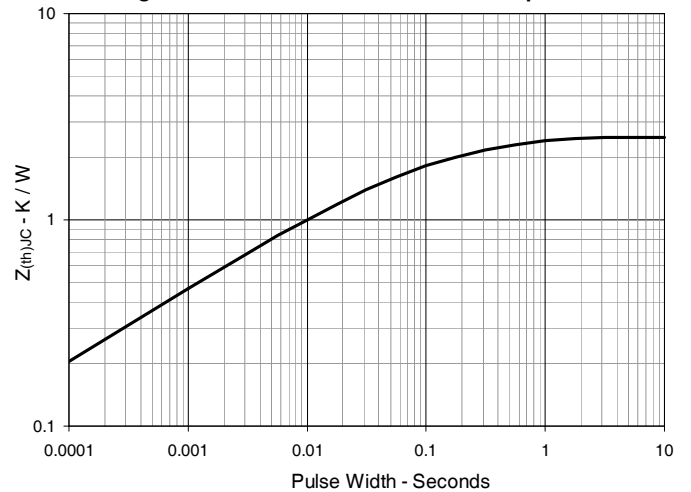
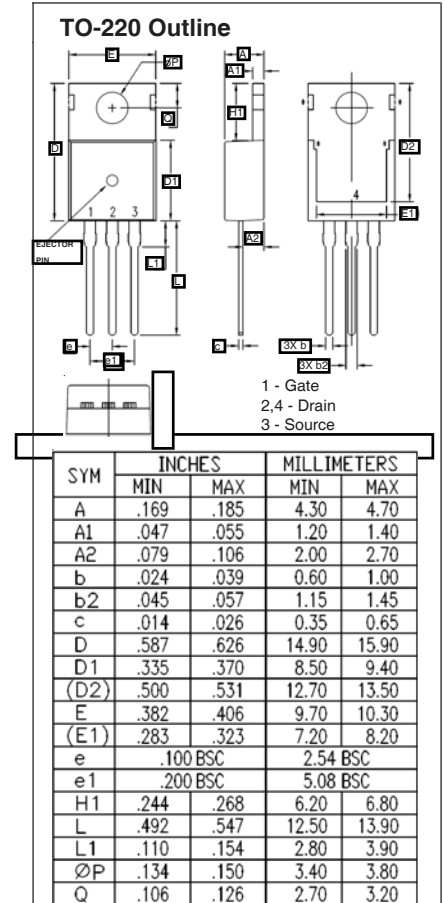
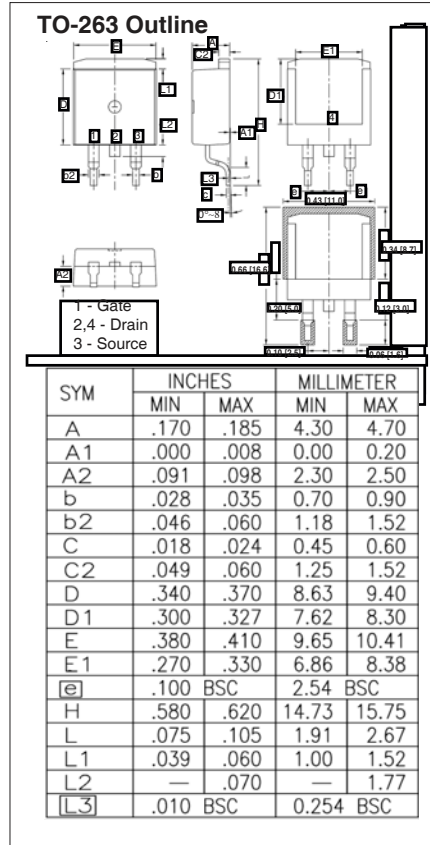
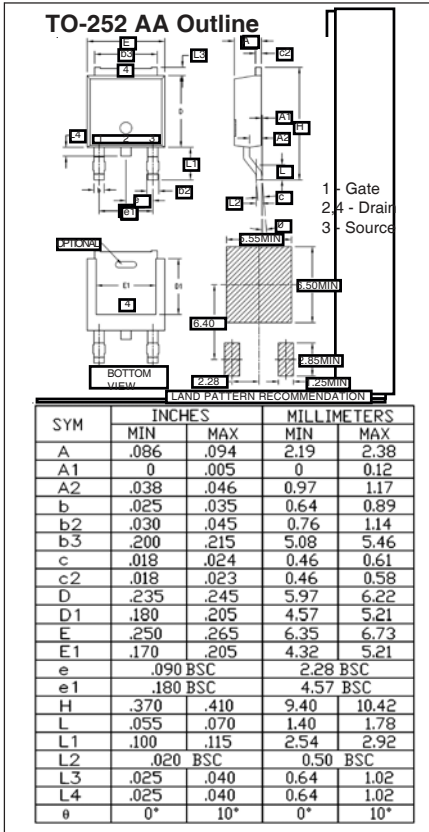


Fig. 12. Maximum Transient Thermal Impedance







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