

DPIULC6

ESD protection for internal DisplayPort™

Features

- Compliant with DisplayPort 1.1a
- IEC 61000-4-2 level 4 compliant
- Ultralarge bandwidth (> 5 GHz)
- Low capacitance variation: 0.05 pF
- 100 Ω ± 10% differential impedance (100% compatible with 100 Ω differential layout)
- 500 µm pitch for easy layout

Complies with the following standards

- IEC 61000-4-2 level 4
- 15 kV (air discharge)
 - 8 kV (contact discharge)
- MIL STD 883G-Method 3015-7: class 3B
 - 8 kV HBM (Human Body Model)
- VESA DisplayPort Standard Version 1.1a

Description

The DPIULC6-6DJL device provides fully integrated ESD protection ensuring full system robustness as required by the DisplayPort specification. Differentiated protection dedicated to each link ensures full compliance with the DisplayPort specification.

The bandwidth of each circuit ensures full transparency to the DisplayPort signals.

The DPIULC6-6DJL is packaged in DFN 5x6.



Figure 1. Schematic diagram (top view)



TM: DisplayPort is a trademark of the Video Electronics Standards Association (VESA)

1 Characteristics

Table 1.Absolute maximum ratings ($T_{amb} = 25$ °C)

Symbol	Parameter	Value	Unit
T _{stg}	Storage temperature range	-55 to +150	°C
Тj	Operating junction temperature range	-40 to +125	°C
ΤL	Maximum lead temperature for soldering during 10 s	260	°C

Table 2.Electrical characteristics: high speed differential pairs ($T_{amb} = 25 \degree C$)

Symbol	Parameter	Test conditions		Value			Unit
Symbol	Falance	1651 60	Min.	Тур.	Max.	Unit	
I _{RM}	Leakage current	V = 3.0 V		-	-	100	nA
V_{BR}	Breakdown voltage	$T_A = 25 \ ^\circ C, \ I_F$	_R = 1 mA	6	-	-	V
V _{CL}	Clamping voltage (Any I/O pin to ground)	t _p = 8/20 μs	I _{PP} = 1 A	-	-	12	v
			I _{PP} = 5 A	-	-	17	
Cura au	Capacitance between I/O and ground	V _R = 0 V, F=1 MHz		-	-	1.5	'nE
U/O -GND		V _R = 0 V, F = 1.4 GHz		-	-	1.5	PI
$\Delta C_{I/O - GND}$	Capacitance variation between 2 lines of the same pair	V _R = 0 V, F = 1.4 GHz		-	0.05	0.12	pF
Z _{Diff}	Differential impedance between input and output tr(20%-80%)=130 ps		=130 ps	90	100	110	Ω

Table 3.Electrical characteristics: auxiliary link ($T_{amb} = 25 \degree C$)

Symbol	Paramotors	Test conditions		Value			Unit
Symbol	Falameters			Min.	Тур.	Max.	Unit
I _{RM}	Leakage current	V = 3.0 V		-	-	100	nA
V _{BR}	Breakdown voltage between V _{BUS} and ground	$T_A = 25 \ ^\circ C, \ I_R = 1 \ mA$		6	-	-	۷
Va			I _{PP} = 1 A	-	-	12	V
V CL	Champing Voltage	ι _p – 0/20 μs	I _{PP} = 5 A	-	-	17	v
C _{i/o-GND}	Capacitance between I/O and	V _R =0 V, F=1 MHz		-	42	50	pF
F _c	Cut-off frequency			-	7.0	-	MHz







Figure 3. Leakage current versus junction F temperature (typical values)





Figure 5. Eye diagram at 2.7 Gbps (PCB alone)











2 Application information





DPIULC6





Figure 10. DisplayPort internal schematic on sink side



3 Ordering information scheme



ipsplayPort internal	
Itralow capacitance	
Breakdown voltage	
VBR = 6 V	
Number of lines	
Package	
DJL = DFN 5x6	



4 Package information

- Epoxy meets UL94, V0
- Lead-free package

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: *www.st.com*. ECOPACK[®] is an ST trademark.

Figure 12. DFN 5x6 package dimension definitions



Table 4. DFN 5x6 package dimension values

Def	Millimetres			Inches		
Rei.	Min.	Тур.	Max.	Min.	Тур.	Max.
A	0.80	0.90	1.0	0.031	0.035	0.039
A1	0.00	0.02	0.05	0.000	0.001	0.002
b	0.18	0.25	0.30	0.007	0.010	0.012
D	5.90	6.00	6.10	0.232	0.236	0.240
D2	4.00	4.15	4.25	0.157	0.163	0.167
е	-	0.5	-	-	0.020	-
E	4.90	5.00	5.10	0.193	0.197	0.201
E2	3.00	3.15	3.25	0.118	0.124	0.128
L	0.45	0.55	0.65	0.020	0.022	0.025





Figure 13. Footprint recommendations in millimetres (inches)

Figure 14. Marking

5 Ordering information

Table 5	Ordering	information
Table J.	ordening	mormation

Order code	Marking	Package	Weight	Base qty	Delivery mode
DPIULC6-6DJL	DPIL66	DFN 5x6 18 leads	78.8 mg	3000	Tape and reel

6 Revision history

Table 6.	Document revision history
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Date	Revision	Changes
27-Nov-2009	1	Initial release.
09-Apr-2010	2	Added central pad to comment in Figure 1.



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