



QUADRUPLE 2-INPUT OR GATES

Description

The 74LV32A provides provides four independent 2-input OR gates with standard push-pull outputs. The device is designed for operation with a power supply range of 2.0V to 5.5V.

The inputs are tolerant to 5.5V allowing this device to be used in a mixed voltage environment. The device is fully specified for partial power down applications using I_{OFF} . The I_{OFF} circuitry disables the output preventing damaging current backflow when the device is powered down.

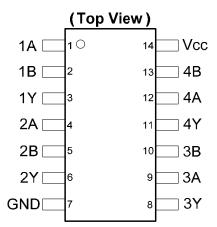
The gates perform the Boolean function:

$$Y = A + B$$
 or $Y = \overline{\overline{A} \bullet \overline{B}}$

Features

- Wide Supply Voltage Range from 2.0V to 5.5V
- Sinks or sources 12mA at V_{CC} = 4.5V
- CMOS low power consumption
- I_{OFF} Supports Partial -Power Down Operation
- Inputs or Outputs accept up to 5.5V
- Inputs can be driven by 3.3V or 5V allowing for voltage translation applications.
- Schmitt Trigger Action at All Inputs
- ESD Protection Tested per JESD 22
 - Exceeds 200-V Machine Model (A115)
 - Exceeds 2000-V Human Body Model (A114)
 - Exceeds 1000-V Charged Device Model (C101)
- Latch-Up Exceeds 100mA per JESD 78, Class I
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Pin Assignments



SO-14 / TSSOP-14

Applications

- General Purpose Logic
- Power Down Signal Isolation
- Wide array of products such as:
 - PCs, networking, notebooks, ultrabooks, netbooks
 - Computer peripherals, hard drives, CD/DVD ROM
 - TV, DVD, DVR, set top box

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

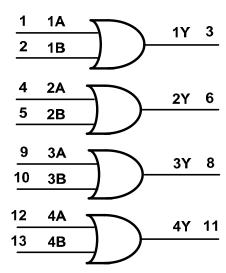
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Pin Descriptions

Pin Number Pin Name Description 1A Data Input 1 2 1B Data Input 3 1Y Data Output 4 2A Data Input 5 2B Data Input Data Output 6 2Y 7 GND Ground 8 3Y Data Output 9 3A Data Input 10 3B Data Input 11 4Y Data Output 4A 12 Data Input 13 4B Data Input 14 Vcc Supply Voltage

Logic Diagram



Function Table

Inp	Output	
Α	В	Y
L	L	L
Н	X	Н
Х	Н	Н

Absolute Maximum Ratings (Note 4) ($@T_A = +25^{\circ}C$, unless otherwise specified.)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
ESD MM	Machine Model ESD Protection	200	V
V _{CC}	Supply Voltage Range	-0.5 to +7.0	V
VI	Input Voltage Range (Note 4)	-0.5 to +7.0	V
I _{IK}	Input Clamp Current V ₁ < 0V	-20	mA
lok	Output Clamp Current V _O < -0V	-50	mA
Io	Continuous Output Current -0.5V < V _O V _{CC} +0.5V	±25	mA
Icc	Continuous Current Through V _{cc}	50	mA
I _{GND}	Continuous Current Through GND	-50	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T _{STG}	Storage Temperature	-65 to +150	°C
P _{TOT}	Total Power Dissipation	500	mW

Note:

4. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.



Recommended Operating Conditions (Note 5) (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Max	Unit
Vcc	Supply Voltage	_	2.0	5.5	V
VI	Input Voltage	_	0	5.5	V
Vo	Output Voltage	_	0	V _{CC}	V
		2.0V	_	-50	mA
	Lligh Lavel Output Current	2.3V to 2.7V	_	-2	μΑ
Іон	High-Level Output Current	3.0V to 3.6V	_	-6	mA
		4.5V to 5.5V	_	-12	mA
		2.0V	_	50	μA
	Low Lovel Output Current	2.3V to 2.7V	_	2	mA
l _{OL}	Low-Level Output Current	3.0V to 3.6V	_	6	mA
		4.5V to 5.5V	_	12	mA
	Local Transition Discon Fall	2.3V to 2.7V	_	200	
Δt/ΔV	Input Transition Rise or Fall Rate	3.0V to 3.6V	_	100	ns/V
	Tale	4.5V to 5.5V	_	20	
T _A	Operating Free-Air Temperature	_	-40	+125	°C

Note: 5. Unused inputs should be held at Vcc or Ground.

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

				T _A = -40°C	C to +85°C	T _A = -40°C	to +125°C	
Symbol	Parameter	Test Conditions	V _{CC}	Min	Max	Min	Max	Unit
		_	2.0V	1.5	_	1.5	_	
	High-Level Input	_	2.3V to 2.7V	V _{CC} X 0.7	_	V _{CC} X 0.7	_	V
V _{IH}	Voltage	_	3.0V to 3.6V	V _{CC} X 0.7	_	V _{CC} X 0.7	_	
		_	4.5V to 5.5V	V _{CC} X 0.7	_	V _{CC} X 0.7	_	
		_	2.0V	_	0.5	_	0.5	
.,	Low-Level Input	_	2.3V to 2.7V	_	V _{CC} X 0.3	_	V _{CC} X 0.3	V
V _{IL}	Voltage	_	3.0V to 3.6V	_	V _{CC} X 0.3	_	V _{CC} X 0.3	
		_	4.5V to 5.5V	_	V _{CC} X 0.3	_	V _{CC} X 0.3	
		I _{OH} = -50μA	2.0V to 5.5V	V _{CC} -0.1	_	V _{CC} -0.1	_	
.,	High-Level	I _{OH} = -2mA	2.3V	2.0	_	2.0	_	V
V _{OH}	Output Voltage	I_{OH} = -6mA	3.0V	2.48	_	2.48	_	V
		I _{OH} = -12mA	4.5V	3.8	_	3.8	_	
		I _{OL} = 50μA	2.0V to 5.5V	_	0.1	_	0.1	
.,	Low-Level	I _{OL} = 2mA	2.3V	_	0.4	_	0.4	V
V _{OL}	Output Voltage	I _{OL} = 6mA	3.0V	_	0.44	_	0.44	v
		I _{OL} = 12mA	4.5V	_	0.55	_	0.55	
l _{OFF}	Power Down Leakage Current	V_1 or $V_0 = 0$ to 5.5V	0V	_	5	_	5	μΑ
IĮ	Input Current	V _I = GND or 5.5V	0 to 5.5V	_	±1	_	±1	μΑ
Icc	Supply Current	$V_I = GND \text{ or } V_{CC}$ $I_O = 0$	5.5V	_	20	_	20	μΑ



Switching Characteristics

Symbol	Parameter	Test	V _{cc}	-	Γ _A = +25°0	;	-40°C to	o +85°C	-40°C to	+125°C	Unit
Symbol	Farailletei	Conditions	V CC	Min	Тур	Max	Min	Max	Min	Max	Oilit
		Figure 1	2.5V ± 0.2V	_	7.1	12.8	1	15	1	16	
	, Propagation	Figure 1 $C_L = 15pF$	$3.3V \pm 0.3V$	_	5	7.9	1	9.5	1	9.5	ns
_		CL = 15pF	5.0V ± 0.5V	_	3.6	5.5	1	6.5	1	6.5	
t _{PD}	Delay A _N to Y _N	Figure 1	2.5V ± 0.2V	_	9.6	16.2	1	19	1	20	
		Figure 1 $C_1 = 50pF$	$3.3V \pm 0.3V$	_	6.9	11.4	1	13	1	13	ns
		CL - 30pi	5.0V ± 0.5V	_	4.9	7.5	1	8.5	1	8.5	

Operating Characteristics

T_A = +25°C

	Parameter	Test Conditions	V _{cc}	Тур	Unit
_	Power Dissipation	f = 10MHz	3.3V	9.5	pF
C_{pd}	Capacitance per Gate	$C_L = 50pF$	5.0V	11.5	рг

Noise Characteristics

 $V_{CC} = 3V, C_L = 50pF, T_A = +25^{\circ}C$

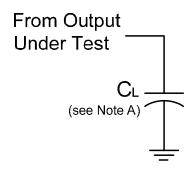
Symbol	Parameter		Тур	Max	Unit
$V_{OL(p)}$	Quiet output, maximum dynamic V _{OL}		0.2	0.8	V
V _{OL(V)}	Quiet output, minimum dynamic V _{OL}	_	-0.1	-0.8	V
$V_{OH(V)}$	Quiet output, minimum dynamic V _{OH}	_	3.1	-	V
$V_{IH(D)}$	High Level dynamic input voltage	2.31	-	-	V
$V_{IL(D)}$	Low Level dynamic input voltage	_	_	0.99	V

Package Characteristics

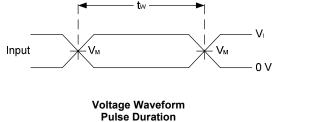
Î	Symbol	Parameter	Test Conditions	V _{cc}	Min	Тур	Max	Unit
I	Ci	Input Capacitance	$V_i = V_{CC} - \text{or GND}$	2.0 to 5.5V	_	3.3	10	pF

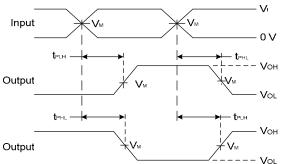


Parameter Measurement Information



V	Inp	outs	V	•
V _{CC}	Vı	t _r /t _f	V _M	C _L
2.0V to 5.5V	V _{CC}	<3ns	V _{CC} /2	15pF or 50pF





Voltage Waveform Propagation Delay Times **Inverting and Non Inverting Outputs**

Notes: A. Includes test lead and test apparatus capacitance.

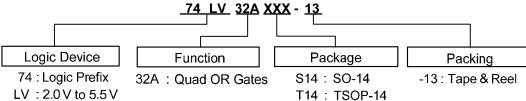
B. All pulses are supplied at pulse repetition rate ≤ 10MHz.

- C. Inputs are measured separately one transition per measurement.
- D. t_{PLH} and t_{PHL} are the same as t_{PD} .

Figure 1 Load Circuit and Voltage Waveforms



Ordering Information



Family

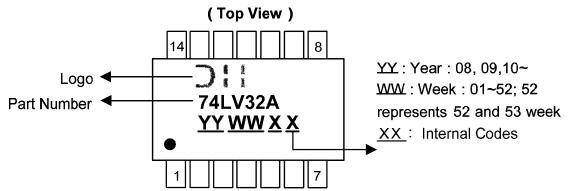
Davisa	Dockara Coda	Packaging	13" Tape	and Reel
Device	Package Code	(Note 6)	Quantity	Part Number Suffix
74LV32AS14-13	S14	SO-14	2500/Tape & Reel	-13
74LV32AT14-13	T14	TSSOP-14	2500/Tape & Reel	-13

Note:

6. The taping orientation and tape details can be found at http://www.diodes.com/datasheets/ap02007.pdf

Marking Information

(1) SO14, TSSOP14



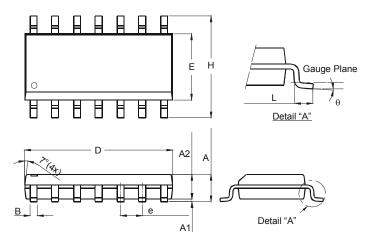
Part Number	Package
74LV32AS14	SO-14
74LV32AT14	TSSOP-14



Package Outline Dimensions (All Dimensions in mm)

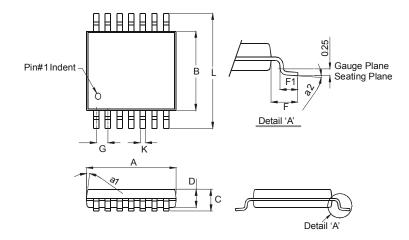
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

Package Type: SO-14



	SO-14						
Dim	Min	Max					
Α	1.47	1.73					
A 1	0.10	0.25					
A2	1.45	Тур					
В	0.33	0.51					
D	8.53	8.74					
Е	3.80	3.99					
е	1.27	Тур					
Н	5.80	6.20					
L	0.38	1.27					
θ	0°	8°					
All Di	mensions	s in mm					

Package Type: TSSOP-14



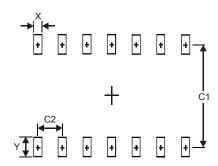
TSSOP-14		
Dim	Min	Max
a1	7° (4X)	
a2	0°	8°
Α	4.9	5.10
В	4.30	4.50
С	_	1.2
D	8.0	1.05
F	1.00 Typ	
F1	0.45	0.75
G	0.65 Typ	
K	0.19	0.30
Ĺ	6.40 Typ	
All Dimensions in mm		



Suggested Pad Layout

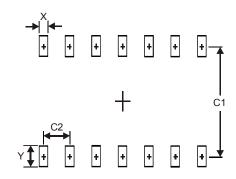
Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

Package Type: SO-14



Dimensions	Value (in mm)
Х	0.60
Υ	1.50
C1	5.4
C2	1.27

Package Type: TSSOP-14



Dimensions	Value (in mm)	
Х	0.45	
Y	1.45	
C1	5.9	
C2	0.65	



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