



LOW V_{CE(SAT)} PNP SURFACE MOUNT TRANSISTOR

Features

- Epitaxial Planar Die Construction
- Low Collector-Emitter Saturation Voltage
- Ideal for Low Power Amplification and Switching
- Complementary NPN Type Available (2DD2656)
- Ultra-Small Surface Mount Package
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green Device" (Note 2)

Mechanical Data

- Case: SOT-323
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
 Terminals: Finish Matte Tin annealed over Alloy 42 leadframe.
- Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.006 grams (approximate)



B E Device Schematic

Maximum Ratings @TA = 25°C unless otherwise specified

V _{CBO}	-30	V
V _{CEO}	-30	V
V _{EBO}	-6	V
lc	-1	A
Ісм	-2	A
	V _{CEO} V _{EBO} I _C	$\begin{array}{c c} V_{CEO} & -30 \\ \hline V_{EBO} & -6 \\ \hline I_C & -1 \\ \hline \end{array}$

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3) @ $T_A = 25^{\circ}C$	PD	300	mW
Thermal Resistance, Junction to Ambient (Note 3) @ T _A = 25°C	R _{0JA}	417	°C/W
Power Dissipation (Note 4) @ $T_A = 25^{\circ}C$	PD	500	mW
Thermal Resistance, Junction to Ambient (Note 4) @ T _A = 25°C	R _{0JA}	250	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	۵°

Electrical Characteristics @TA = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Conditions
OFF CHARACTERISTICS	- ,		- 71-			
Collector-Base Breakdown Voltage	V _{(BR)CBO}	-30			V	I _C = -10μA, I _E = 0
Collector-Emitter Breakdown Voltage (Note 5)	V _{(BR)CEO}	-30	_	_	V	I _C = -1mA, I _B = 0
Emitter-Base Breakdown Voltage	V _{(BR)EBO}	-6	_		V	I _E = -10μA, I _C = 0
Collector Cut-Off Current	I _{CBO}		_	-0.1	μA	V _{CB} = -30V, I _E = 0
Emitter Cut-Off Current	I _{EBO}	_	_	-0.1	μA	$V_{EB} = -6V, I_{C} = 0$
ON CHARACTERISTICS (Note 5)				•		-
Collector-Emitter Saturation Voltage	V _{CE(SAT)}		-180	-380	mV	I _C = -500mA, I _B = -25mA
DC Current Gain	h _{FE}	270	_	680	_	V _{CE} = -2V, I _C = -100mA
SMALL SIGNAL CHARACTERISTICS						
Output Capacitance	C _{obo}	_	16	_	pF	V_{CB} = -10V, I _E = 0, f = 1MHz
Current Gain-Bandwidth Product	f⊤		300		MHz	V _{CE} = -2V, I _C = -100mA, f = 100MHz

Notes: 1. No purposefully added lead.

2. Diode's Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.

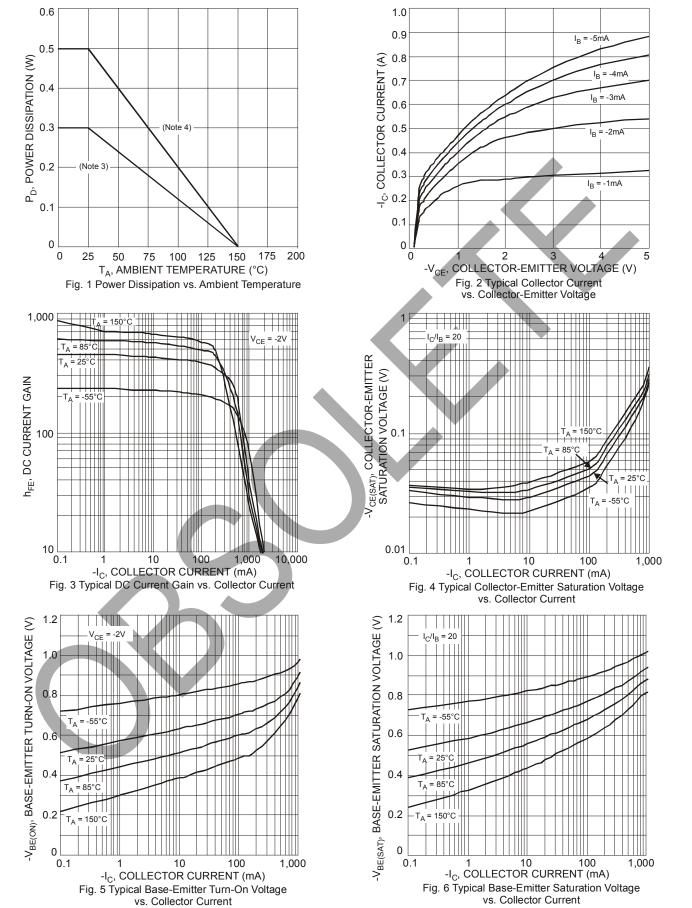
3. Device mounted on FR-4 PCB with minimum recommended pad layout.

4. Device mounted on FR-4 PCB with 1 inch² copper pad layout.

5. Measured under pulsed conditions. Pulse width = 300μ s. Duty cycle $\leq 2\%$.

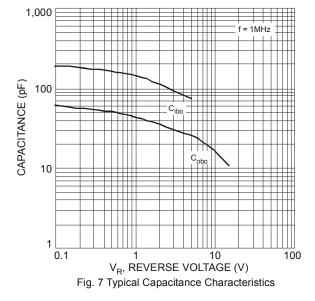


2DB1694





2DB1694



Ordering Information (Note 6)

		*
Part Number	Case	Packaging
2DB1694-7	SOT-323	3000/Tape & Reel

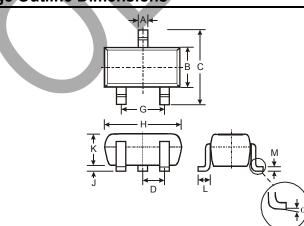
Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information

Π	
RP1 ₹	RP1 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: V = 2008) M = Month (ex: 9 = September)

Date Code Key												
Year	2008		2009	2010		2011	2012		2013	2014		2015
Code	V		W	X		Y	Z		А	В		С
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Νον	Dec
Code	1	2	3	4	5	6	7	8	9	0	Ν	D

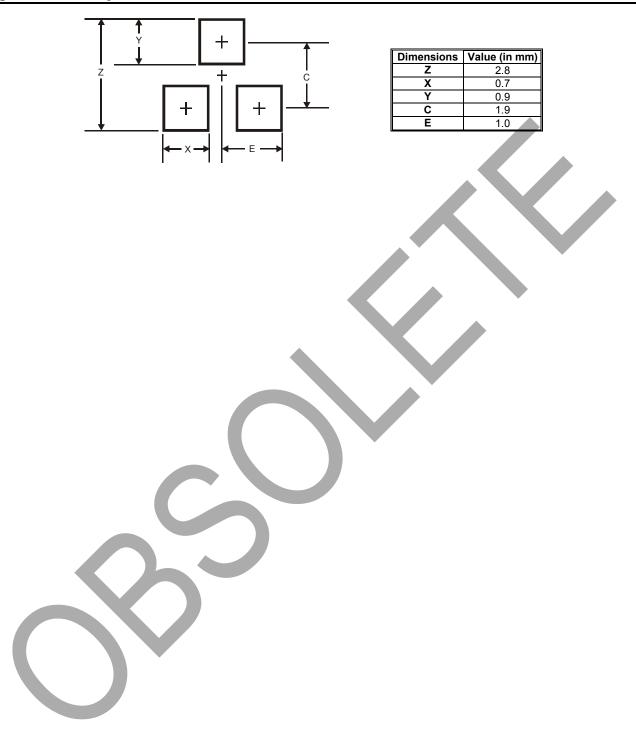
Package Outline Dimensions



SOT-323									
Dim	Dim Min Max Typ								
Α	0.25	0.40	0.30						
В	1.15	1.35	1.30						
С	2.00	2.20	2.10						
D	-	-	0.65						
G	1.20	1.40	1.30						
Н	1.80	2.20	2.15						
J	0.0 0.10		0.05						
κ	0.90	1.00	1.00						
L	0.25	0.40	0.30						
Μ	0.10	0.18	0.11						
α	0°	8°	-						
All Dimensions in mm									



Suggested Pad Layout





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