



N-Channel 100-V (D-S) 175 °C MOSFET

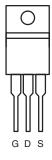
PRODUCT SUMMARY				
V _{(BR)DSS} (V)	$r_{DS(on)}\left(\Omega\right)$	I _D (A)		
100	0.030 at V _{GS} = 10 V	40		
	0.034 at V _{GS} = 6 V	37.5		

FEATURES

- TrenchFET® Power MOSFET
- 175 °C Junction Temperature



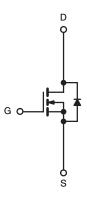




Top View

Ordering Information: SUP40N10-30

SUP40N10-30-E3 (Lead (Pb)-free)



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_C = 25$ °C, unless otherwise noted					
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	100	V	
Gate-Source Voltage		V _{GS}	V _{GS} ± 20		
Continuous Drain Current (T _J = 175 °C)	T _C = 25 °C	I-	40		
	T _C = 125 °C	I _D	23		
Pulsed Drain Current		I _{DM}	75	A	
Avalanche Current		I _{AS}	35		
Single Pulse Avalanche Energy ^a	L = 0.1 mH	E _{AS}	61	mJ	
Maximum Power Dissipation ^a	T _C = 25 °C	В	107 ^b	147	
	T _A = 25 °C ^c	$ P_D$	3.75	W	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS				
Parameter		Symbol	Limit	Unit
Junction-to-Ambient	PCB Mount ^c	. R _{thJA}	40	°C/W
Sundion to Ambient	Free Air		62.5	
Junction-to-Case (Drain)		R _{thJC}	1.4	

Notes:

- a. Duty cycle \leq 1 %.
- b. See SOA curve for voltage derating.
- c. When Mounted on 1" square PCB (FR-4 material).

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply.

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SPECIFICATIONS $T_J = 25$	°C, unless	otherwise noted					
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	100			V	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2		4		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = 80 \text{ V}, V_{GS} = 0 \text{ V}$			1		
	I _{DSS}	$V_{DS} = 80 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125 ^{\circ}\text{C}$			50	μΑ	
		$V_{DS} = 80 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 175 ^{\circ}\text{C}$			250	1	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	75			Α	
		V _{GS} = 10 V, I _D = 15 A		0.024	0.030		
	r · ·	V _{GS} = 6 V, I _D = 10 A		0.026	0.034	0	
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = 10 V, I _D = 15 A, T _J = 125 °C			0.054	Ω	
		V _{GS} = 10 V, I _D = 15 A, T _J = 175 °C			0.067		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 15 A	10			S	
Dynamic ^b	•						
Input Capacitance	C _{iss}			2400		pF	
Output Capacitance	C _{oss}	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$		270			
Reverse Transfer Capacitance	C _{rss}			90			
Total Gate Charge ^c	Qg			35	60	nC	
Gate-Source Charge ^c	Q_{gs}	$V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 40 \text{ A}$		11			
Gate-Drain Charge ^c	Q_{gd}			9			
Gate Resistance	R_{g}			1.7		Ω	
Turn-On Delay Time ^c	t _{d(on)}			11	20	ns	
Rise Time ^c	t _r	V_{DD} = 50 V, R_L = 1.25 Ω I_D \cong 40 A, V_{GEN} = 10 V, R_G = 2.5 Ω		12	20		
Turn-Off Delay Time ^c	t _{d(off)}			30	45		
Fall Time ^c	t _f			12	20		
Source-Drain Diode Ratings and Ch	aracteristics	(T _C = 25 °C) ^b		•	'		
Continuous Current	I _S				40		
Pulsed Current	I _{SM}				75	A	
Forward Voltage ^a	V _{SD}	$I_F = 30 \text{ A}, V_{GS} = 0 \text{ V}$		1.0	1.5	V	
Reverse Recovery Time	t _{rr}			60	100	ns	
Peak Reverse Recovery Current	I _{RM(REC)}	I _F = 30 A, di/dt = 100 A/μs		5	8	Α	
Reverse Recovery Charge	Q _{rr}			0.15	0.4	μC	

Notes:

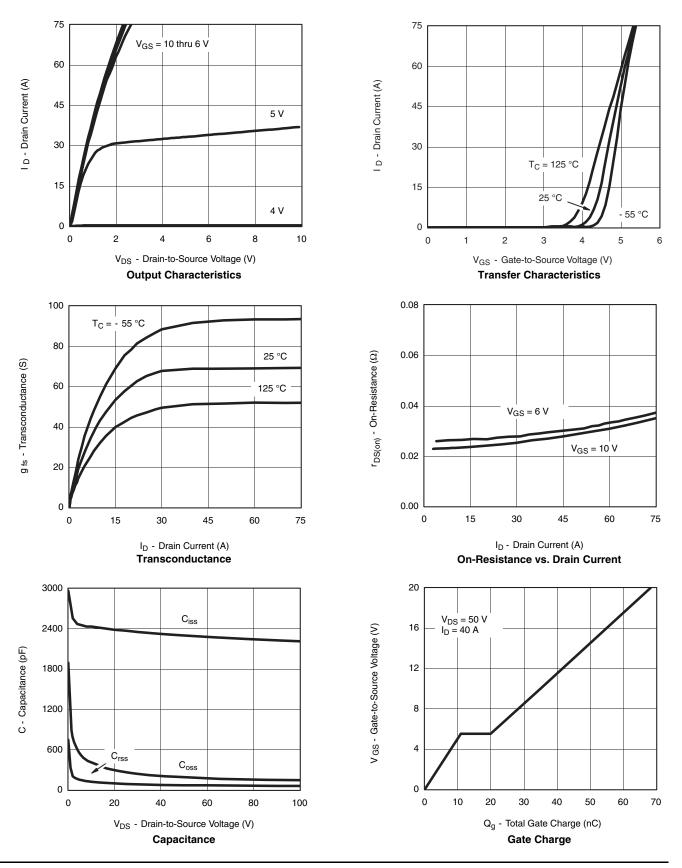
- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.





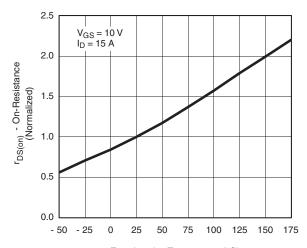
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



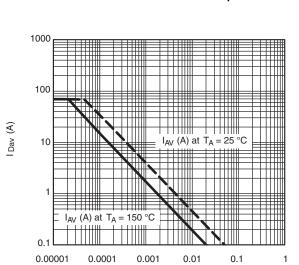
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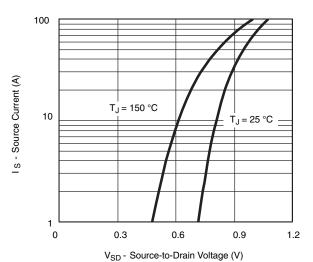
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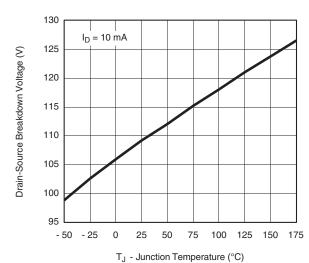
 T_J - Junction Temperature (°C) **On-Resistance vs. Junction Temperature**



 $t_{\text{in}} \; \left(\text{Sec} \right)$ Avalanche Current vs. Time



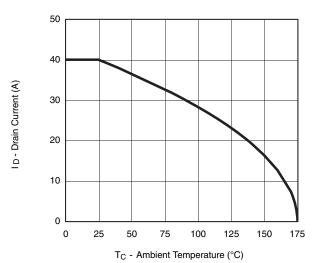
Source-Drain Diode Forward Voltage



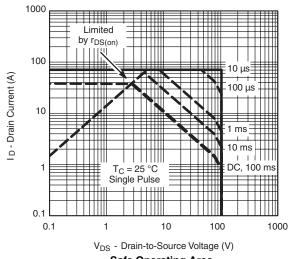
Drain-Source Breakdown Voltage vs. Junction Temperature



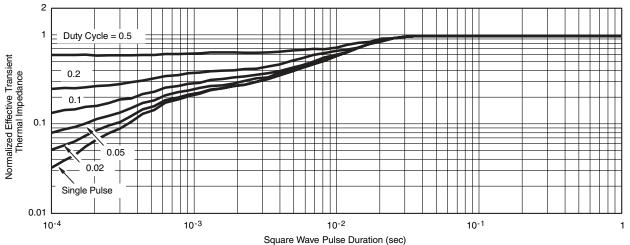
THERMAL RATINGS



Maximum Avalanche and Drain Current vs. Case Temperature



Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case

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