## SB05-05C

# ON Semiconductor®

http://onsemi.com

# Schottky Barrier Diode 50V, 0.5A, Low IR, Single CP

#### **Applications**

· High frequency rectification (switching regulators, converters, choppers)

#### **Features**

- Low forward voltage (VF max=0.55V)
- · Low switching noise
- · Low leakage current and high reliability due to highly reliable planar structure

#### **Specifications**

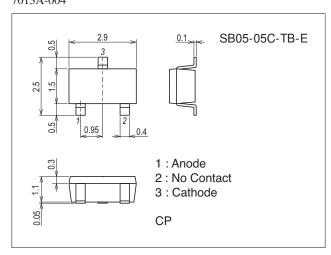
#### **Absolute Maximum Ratings** at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Repetitive Peak Reverse Voltage	VRRM		50	V
Nonrepetitive Peak Reverse Surge Voltage	VRSM		55	V
Average Output Current	IO		500	mA
Surge Forward Current	IFSM	50Hz sine wave, 1 cycle	5	Α
Junction Temperature	Tj		-55 to +125	°C
Storage Temperature	Tstg		-55 to +125	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

#### **Package Dimensions**

unit : mm (typ) 7013A-004



#### **Product & Package Information**

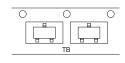
• Fast reverse recovery time (t<sub>rr</sub> max=10ns)

• Package : CP

• JEITA, JEDEC : SC-59, TO-236, SOT-23, TO-236AB

• Minimum Packing Quantity: 3,000 pcs./reel

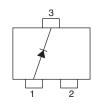
#### Packing Type: TB





Marking

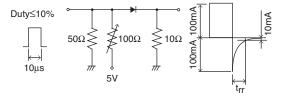
#### **Electrical Connection**



#### Electrical Characteristics at Ta=25°C

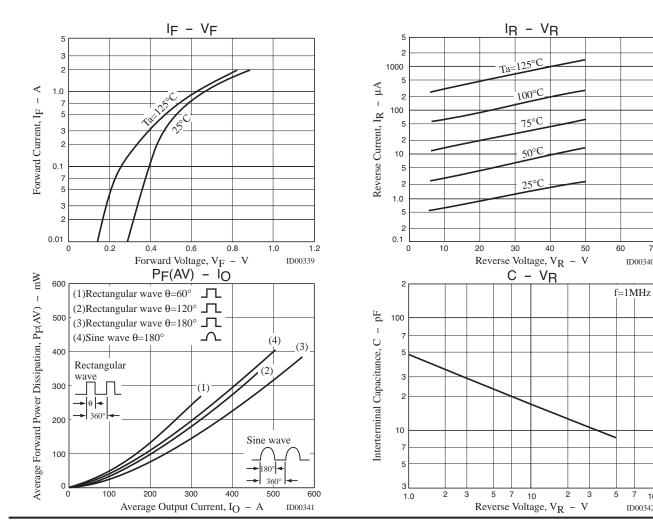
Parameter	Symbol	Conditions	Ratings			Unit
		Conditions	min	typ	max	Offile
Reverse Voltage	VR	I <sub>R</sub> =200μA, Tj=25°C	50			V
Forward Voltage	VF	I <sub>F</sub> =500mA, Tj=25°C			0.55	V
Reverse Current	IR	R V <sub>R</sub> =25V, Tj=25°C			50	μΑ
Interterminal Capacitance C		V <sub>R</sub> =10V, f=1MHz		17		pF
Reverse Recovery Time t <sub>rr</sub>		IF=IR=100mA, Tj=25°C, See specified Test Circuit.			10	ns
	Rth(j-a)1			420		°C / W
Thermal Resistance	Rth(j-a)2	Mounted in Cu-foiled area of 16mm <sup>2</sup> ×0.2mm	330			°C/W
		on glass epoxy board		330		C / W

#### trr Test Circuit



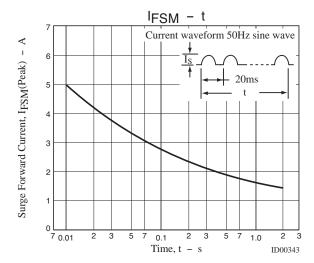
#### **Ordering Information**

Device	Package	Shipping	memo	
SB05-05C-TB-E	СР	3,000pcs./reel	Pb Free	



ID00342

ID00340

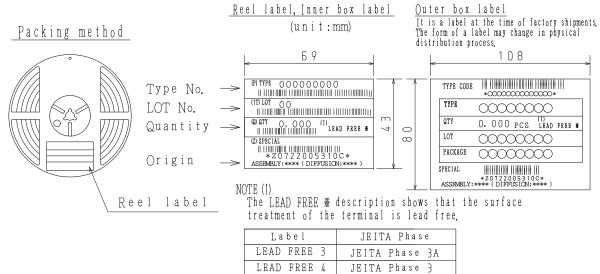


#### **Embossed Taping Specification**

#### SB05-05C-TB-E

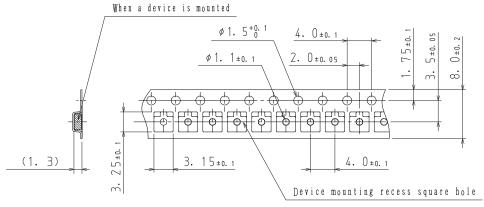
#### 1. Packing Format

Package Name	Carrier Tape	Maximum Number of devices contained (pcs)			Packing format		
	Туре	Reel	Inner box	Outer box	Inner $BOX(C-1)$	Outer BOX (A-7)	
СР	СР	3, 000	15, 000	90,000	5 reels contained	6 inner boxes contained	
					Dimensions:mm (external)	Dimensions:mm (external)	
					183×72×185	440×195×210	

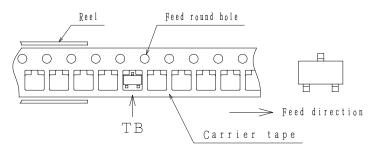


#### 7. Taping configuration

2-1. Carrier tape size (unit:mm)



2-2. Device placement direction



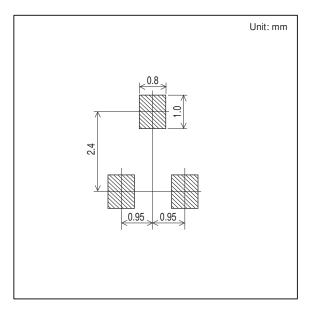
Those with one electrode terminal on the feed hole side·····TB

#### **Outline Drawing**

SB05-05C-TB-E

### Mass (g) Unit 0.013 \*For reference mm 0. 1+0. 1 0. 5+0. 25 2. 9±0.15 A 3 1. 5±0. 15 2. 5±0. 2 2 0. 4<sup>+0. 1</sup> 0. 1 M A 0. 5-0. 15 0. 95 0. 3±0.1 1, 1±0, 15 0. 05±0.05 \*1:Lot indication

#### **Land Pattern Example**



ON Semiconductor and the ON logo are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equa