

**IGBT** 

# SGR15N40L / SGU15N40L

## **General Description**

Insulated Gate Bipolar Transistors (IGBTs) with a trench gate structure provide superior conduction and switching performance in comparison with transistors having a planar gate structure. They also have wide noise immunity. These devices are very suitable for strobe applications

### **Features**

- · High input impedance
- · High peak current capability (130A)
- · Easy gate drive

# **Application**

Strobe flash.







# Absolute Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted

Symbol	Description	SGR / SGU15N40L	Units	
V <sub>CES</sub>	Collector - Emitter Voltage	400	V	
V <sub>GES</sub>	Gate - Emitter Voltage	± 6	V	
I <sub>CM (1)</sub>	Pulsed Collector Current	130	Α	
P <sub>C</sub>	Maximum Power Dissipation @ T <sub>C</sub> =	25°C 45	W	
T <sub>J</sub>	Operating Junction Temperature	-40 to +150	°C	
T <sub>stg</sub>	Storage Temperature Range	-40 to +150	°C	
T <sub>L</sub>	Maximum Lead Temp. for soldering purposes, 1/8" from case for 5 seconds	300	°C	

Notes:
(1) Repetitive rating: Pulse width limited by max. junction temperature

### **Thermal Characteristics**

Symbol	Parameter	Тур.	Max.	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case		3.0	°C/W
R <sub>θJA</sub> (D-PAK)	Thermal Resistance, Junction-to-Ambient (PCB Mount) (2)		50	°C/W
R <sub>θJA</sub> (I-PAK)	Thermal Resistance, Junction-to-Ambient		110	°C/W

(2) Mounted on 1" square PCB (FR4 or G-10 Material)

Symbol	bol Parameter Test Conditions		Min.	Тур.	Max.	Units
Off Cha	racteristics					
BV <sub>CES</sub>	Collector - Emitter Breakdown Voltage	Collector - Emitter Breakdown Voltage $V_{GE} = 0V$ , $I_{C} = 1mA$				V
I <sub>CES</sub>	Collector Cut-Off Current	$V_{CE} = V_{CES}, V_{GE} = 0V$			10	uA
I <sub>GES</sub>	G - E Leakage Voltage	$V_{GE} = V_{GES}, V_{CE} = 0V$			± 0.1	uA
	racteristics					1
$V_{GE(th)}$	G - E Threshold Voltage	$I_C = 1 \text{mA},  V_{CE} = V_{GE}$		1.0	1.4	V
V <sub>CE(sat)</sub>	C - E Saturation Current	$I_C = 130A, V_{GE} = 4.5V$	2.0	4.5	8.0	V
•	c Characteristics				I	_
C	Input Capacitance	V 0V V 00V		3000		pF
Cies	• •	∀○□ = UV, V○□ = 3UV.		45		E
C <sub>oes</sub>	Output Capacitance	$V_{GE} = 0V, V_{CE} = 30V,$ f = 1MHz		45		pF
C <sub>oes</sub>	• •	GE		30		рF
C <sub>ies</sub> C <sub>oes</sub> C <sub>res</sub>	Output Capacitance	GE				
C <sub>oes</sub> C <sub>res</sub> Switchin	Output Capacitance Reverse Transfer Capacitance	f = 1MHz				
C <sub>oes</sub> C <sub>res</sub> Switchir	Output Capacitance Reverse Transfer Capacitance  ng Characteristics	f = 1MHz V <sub>CC</sub> = 300V, I <sub>C</sub> = 130A,		30		pF
C <sub>oes</sub> C <sub>res</sub>	Output Capacitance Reverse Transfer Capacitance  ng Characteristics Turn-On Delay Time	f = 1MHz		30		pF

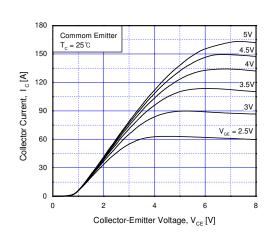


Fig 1. Typical Output Characteristics

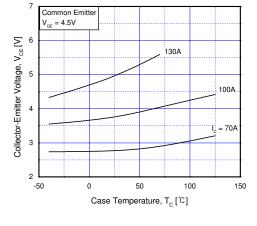


Fig 2. Saturation Voltage vs. Case Temperature at Variant Current Level

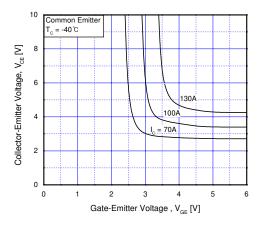


Fig 3. Saturation Voltage vs.  $V_{\text{GE}}$ 

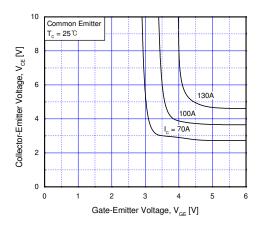


Fig 4. Saturation Voltage vs.  $V_{\text{GE}}$ 

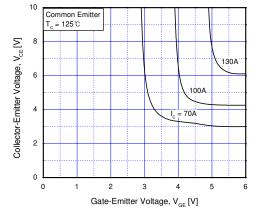


Fig 5. Saturation Voltage vs.  $V_{\text{GE}}$ 

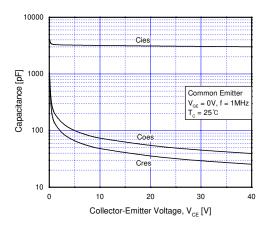
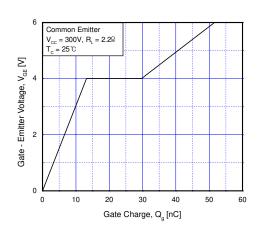


Fig 6. Capacitance Characteristics



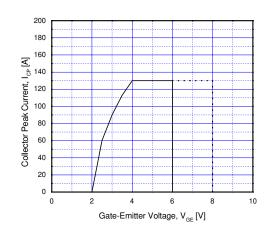
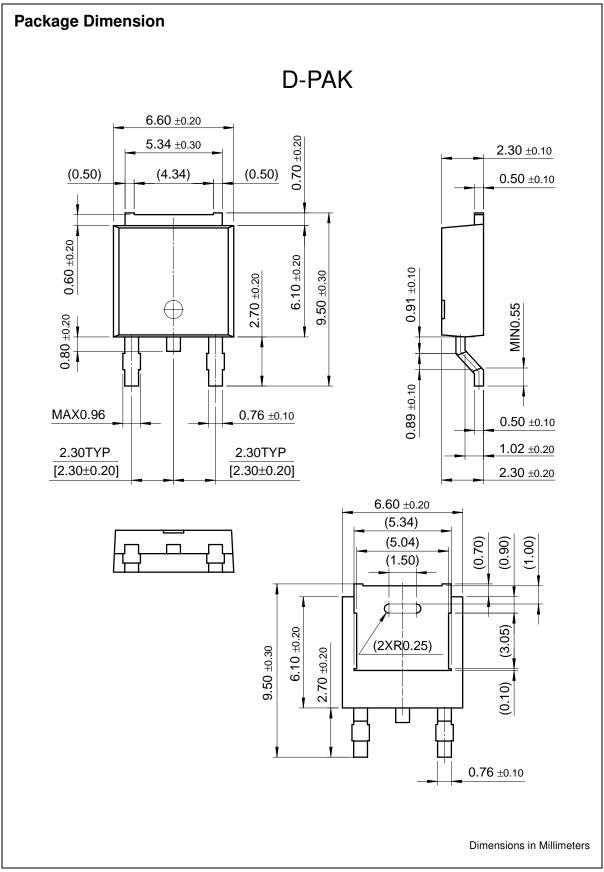
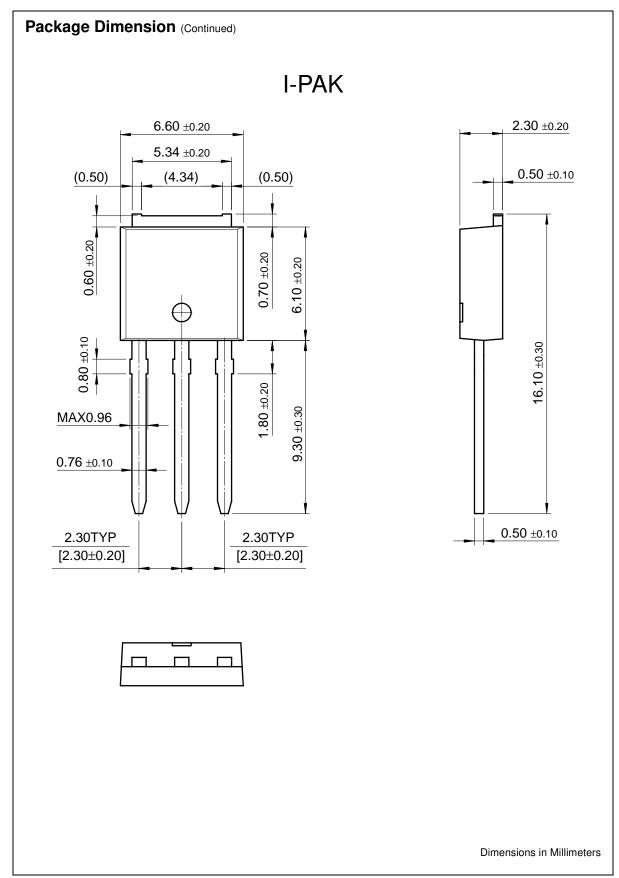


Fig 7. Gate Charge Characteristics

Fig 8. Collector Current Limit vs.

Gate - Emitter Voltage Limit





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Product	Product status	Pricing*	Package type	Leads	Packing method
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Product Folder - Fairchild P/N SGU15N40L - Discrete, IGBT

\* 1,000 piece Budgetary Pricing

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Jul 19, 2002

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ProductProduct statusPricing\*Package typeLeadsPacking methodSGR15N40LTFFull Production\$1.36TO-252(DPAK)2TAPE REEL

SGR15N40LTM Full Produc	tion \$1.36	TO-252(DPAK)	2	TAPE REEL
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<sup>\* 1,000</sup> piece Budgetary Pricing

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