APT30DQ100BG

Datasheet Ultrafast Soft Recovery Rectifier Diode

Final April 2018



Power Matters."



Contents

1	Revi	sion History	1
	1.1	Revision D	. 1
	1.2	Revision C	. 1
	1.3	Revision B	. 1
	1.4	Revision A	. 1
2	Prod	uct Overview	2
	2.1	Features	. 2
	2.2	Benefits	. 2
	2.3	Applications	. 2
3	Elect	trical Specifications	3
3	Elect 3.1	trical Specifications Absolute Maximum Ratings	
3			. 3
3	3.1	Absolute Maximum Ratings	. 3 . 3
3	3.1 3.2	Absolute Maximum Ratings Electrical Performance	. 3 . 3 . 4
3	3.1 3.2 3.3	Absolute Maximum Ratings Electrical Performance Dynamic Characteristics	3 3 4
3	3.1 3.2 3.3 3.4	Absolute Maximum Ratings Electrical Performance Dynamic Characteristics Thermal and Mechanical Characteristics	3 3 4 4 5
	 3.1 3.2 3.3 3.4 3.5 3.6 	Absolute Maximum Ratings Electrical Performance Dynamic Characteristics Thermal and Mechanical Characteristics Typical Performance Curves	. 3 . 4 . 4 . 5 . 7



1 Revision History

The revision history describes the changes that were implemented in the document. The changes are listed by revision, starting with the most current publication.

1.1 Revision D

Revision D was published in April 2018. The following is a summary of the changes in revision D of this document.

- Product image was updated.
- Product features were updated. For information, see Product Overview (see page 2).
- The lead thickness in the package outline drawing was updated. For more information, see Package Outline Drawing (see page 8).

1.2 Revision C

Revision C was published in May 2011. The following is a summary of the changes in Revision C of this document.

- Patent Information was removed.
- B-Pack Information was updated.
 - Changed max lead thickness from 0.79 (.031) to 1.016 (.040).

1.3 Revision B

Revision B was published in July 2009. The following is a summary of the changes in Revision B of this document.

• Graphs were added.

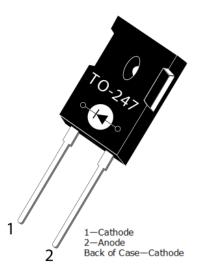
1.4 Revision A

Revision A was published in January 2006. It is the first publication of this document.



2 Product Overview

This section outlines the product overview for the APT30DQ100BG device.



2.1 Features

The following are key features of the APT30DQ100BG device:

- Ultrafast recovery times
- Soft recovery characteristics
- Low forward voltage
- Low leakage current
- Avalanche energy rated
- RoHS compliant
- AEC-Q101 qualified

2.2 Benefits

The following are benefits of the APT30DQ100BG device:

- High switching frequency
- Low switching losses
- Low noise (EMI) switching
- Higher reliability systems
- Increased system power density

2.3 Applications

The APT30DQ100BG device is designed for the following applications:

- Power factor correction (PFC)
- Anti-parallel diode
 - Switch-mode power supply
 - Inverters/converters
 - Motor controllers
- Freewheeling diode
 - Switch-mode power supply
 - Inverters/converters
- Snubber/clamp diode



3 Electrical Specifications

This section details the electrical specifications for the APT30DQ100BG device.

3.1 Absolute Maximum Ratings

The following table shows the maximum ratings for the APT30DQ100BG device.

All ratings: Tc = 25 °C unless otherwise specified.

Table 1 • Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit
VR	Maximum DC reverse voltage	1000	V
VRRM	Maximum peak repetitive reverse voltage	1000	
VRWM	Maximum working peak reverse voltage	1000	
IF(AV)	Maximum average forward current (Tc = 102 °C, duty cycle = 0.5)	30	А
IF(RMS)	RMS forward current	43	
IFSM	Non-repetitive forward surge current (T _J = 45 °C, 8.3 ms)	150	
Eavl	Avalanche energy (1 A, 40 mH)	20	mJ
Tj , Tstg	Operating and storage temperature range	–55 to 175	°C
Τι	Lead temperature for 10 s	300	

3.2 Electrical Performance

The following table shows the static electrical characteristics of the APT30DQ100BG device.

Table 2 • Static Electrical Characteristics

Symbol	Characteristic	Test Conditions	MIN	ТҮР	MAX	Unit
VF	Forward voltage	IF = 30 A		2.5	3.0	V
		IF = 60 A		3.06		-
		IF = 30 A, TJ = 125 °C		1.92		_
Irm	Maximum reverse	V _R = 1000 V			100	μA
	leakage current	V _R = 1000 V, T _J = 125 °C			500	-
Ст	Junction capacitance	V _R = 200 V		26		pF



3.3 Dynamic Characteristics

The following table shows the dynamic characteristics of the APT30DQ100BG device.

Table 3 • Dynamic Characteristics

Symbol	Characteristic	Test Conditions	MIN	ТҮР	MAX	Unit
trr	Reverse recovery time	IF = 1 A, diF/dt = -100 A/μs		24		ns
		V _R = 30 V, T _J = 25 °C				
trr	Reverse recovery time	IF = 30 A, diF/dt = -200 A/µs		295		-
Qrr	Reverse recovery charge	- V _R = 667 V, T _C = 25 °C		440		nC
IRRM	Maximum reverse recovery current	-		4		А
trr	Reverse recovery time	IF = 30 A, diF/dt = -200 A/µs		330		ns
Qrr	Reverse recovery charge	- V _R = 667 V, T _c = 125 °C		1550		nC
IRRM	Maximum reverse recovery current	-		8		А
trr	Reverse recovery time	IF = 30 A, diF/dt = -1000 A/µs		150		ns
Qrr	Reverse recovery charge	- Vr = 667 V, Tc = 125 °C		2250		nC
IRRM	Maximum reverse recovery current	-		25		А

3.4 Thermal and Mechanical Characteristics

This section shows the thermal and mechanical characteristics of the APT30DQ100BG device.

Table 4 • Thermal and Mechanical Characteristics

Symbol	Characteristic/Test Conditions	MIN	ТҮР	MAX	UNIT
Rөлс	Junction-to-case thermal resistance			0.80	°C/W
WT	Package weight		0.22		oz
			5.9		g
Torque	Maximum mounting torque			10	lb-in
				1.1	N-m

Microsemi reserves the right to change, without notice, the specifications and information contained herein.



3.5 Typical Performance Curves

This section shows the typical performance curves for the APT30DQ100BG device.

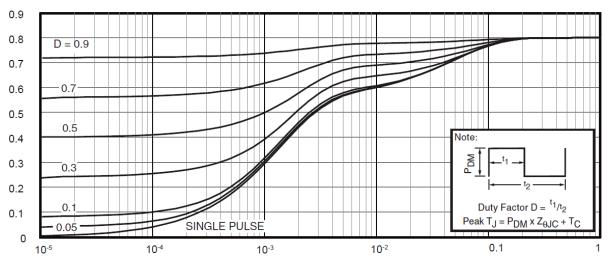


Figure 1 • Maximum Transient Thermal Impedance

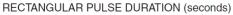


Figure 2 • Forward Current vs. Forward Voltage

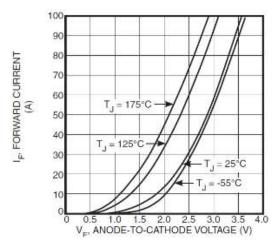


Figure 3 • trr vs. Current Rate of Change

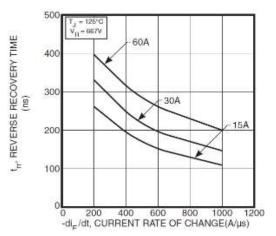




Figure 4 • Reverse Recovery Charge vs. Current Rate of Change

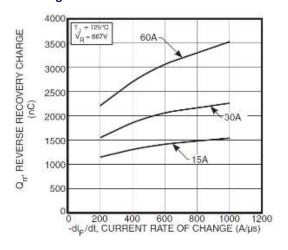


Figure 6 • Dynamic Parameters vs. Junction

Temperature

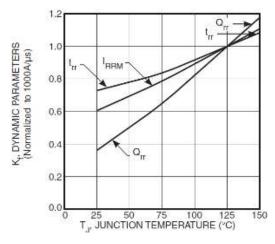


Figure 8 • Junction Capacitance vs. Reverse Voltage

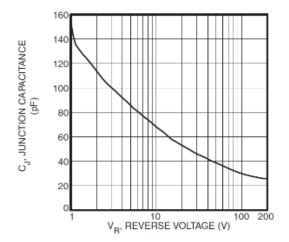


Figure 5 • Reverse Recovery Current vs. Current Rate of Change

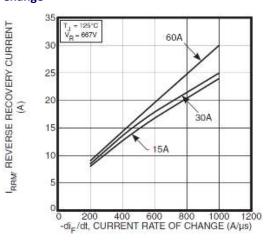
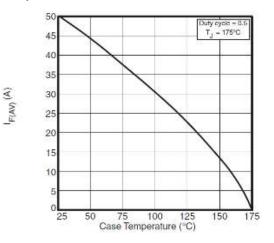


Figure 7 • Maximum Average Forward Current vs. Case Temperature





3.6 Reverse Recovery Overview

The following figures illustrate the reverse recovery testing and measurement information for the APT30DQ100BG device.

Figure 9 • Diode Test Circuit

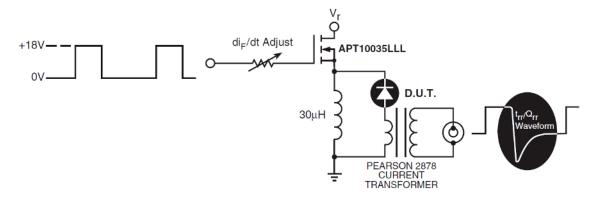
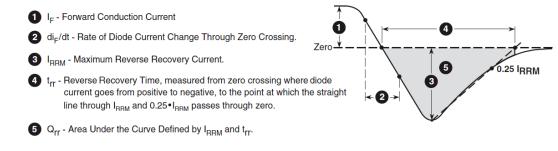


Figure 10 • Diode Reverse Recovery Waveform and Definitions





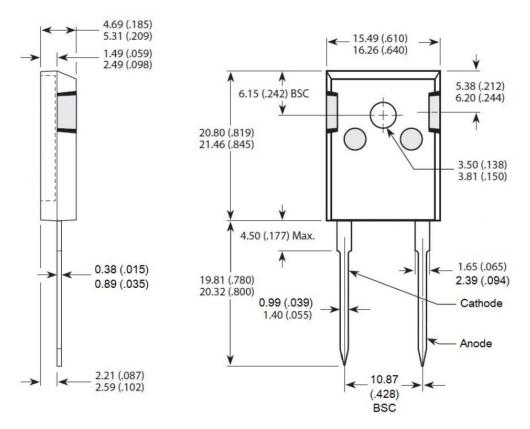
4 Package Specification

This section outlines the package specification for the APT30DQ100BG device.

4.1 Package Outline Drawing

This section details the TO-247 package drawing of the APT30DQ100BG device.

Figure 11 • Package Outline Drawing







Power Matters."

Microsemi Corporate Headquarters One Enterprise, Aliso Viejo, CA 92656 USA Within the USA: +1 (800) 713-4113 Outside the USA: +1 (949) 380-6100 Fax: +1 (949) 215-4996 Email: sales.support@microsemi.com www.microsemi.com

© 2018 Microsemi Corporation. All rights reserved. Microsemi and the Microsemi logo are trademarks of Microsemi Corporation. All other trademarks and service marks are the property of their respective owners. Microsemi makes no warranty, representation, or guarantee regarding the information contained herein or the suitability of its products and services for any particular purpose, nor does Microsemi assume any liability whatsoever arising out of the application or use of any product or circuit. The products sold hereunder and any other products sold by Microsemi have been subject to limited testing and should not be used in conjunction with mission-critical equipment or applications. Any performance specifications are believed to be reliable but are not verified, and Buyer must conduct and complete all performance and other testing of the products, alone and together with, or installed in, any end-products. Buyer shall not rely on any data and performance specifications or parameters provided by Microsemi. It is the Buyer's responsibility to independently determine suitability of any products and to test and verify the same. The information provided by Microsemi hereunder is provided "as i, where is" and with all faults, and the entire risk associated with such information is entirely with the Buyer. Microsemi does not grant, explicitly or implicitly, to any parta ny patent rights, licenses, or any other IP rights, whether with regard to such information itself or anything described by such information. Information provided in this document is proprietary to Microsemi and Microsemi reserves the right to make any changes to the information in this document or any products and services at any time without notice.

Microsemi Corporation (Nasdaq: MSCC) offers a comprehensive portfolio of semiconductor and system solutions for aerospace & defense, communications, data center and industrial markets. Products include high-performance and radiation-hardened analog mixed-signal integrated circuits, FPGAs, SoCs and ASICs; power management products; timing and synchronization devices and precise time solutions, setting the world's standard for time; voice processing devices; RF solutions; discrete components; enterprise storage and communication solutions; security technologies and scalable anti-tamper products; Ethernet solutions; Power-over-Ethernet ICs and midspans; as well as custom design capabilities and services. Microsemi is headquartered in Aliso Viejo, California, and has approximately 4.800 employees globally. Learn more at www.microsemi.com.

053-4239