

RCD075N19

Nch 190V 7.5A Power MOSFET

V_{DSS}	190V
R _{DS(on)} (Max.)	336m $Ω$
I _D	7.5A
P_D	52W

Features

- 1) Low voltage drive (4V drive).
- 2) Low on-resistance.
- 3) Fast switching speed.
- 4) Drive circuits can be simple.
- 5) Parallel use is easy.
- 6) Pb-free lead plating; RoHS compliant
- 7) 100% Avalanche tested

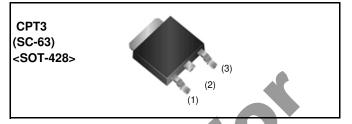
Application

Switching Power Supply

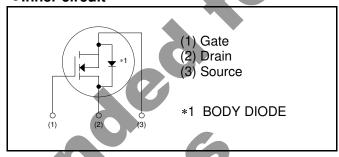
Automotive Motor Drive

Automotive Solenoid Drive

Outline



•Inner circuit



Packaging specifications

	Packaging	Taping
	Reel size (mm)	330
Type	Tape width (mm)	16
Туре	Basic ordering unit (pcs)	2,500
	Taping code	TL
	Marking	C07N19

● Absolute maximum ratings(T_a = 25°C)

Parameter	10	Symbol	Value	Unit
Drain - Source voltage	$V_{ extsf{DSS}}$	190	V	
Continuous drain current	I _D *1	±7.5	А	
Continuous drain current	I _D *1	±4.1	Α	
Pulsed drain current	I _{D,pulse} *2	±30	Α	
Gate - Source voltage	V_{GSS}	±20	V	
Avalanche energy, single pulse		E _{AS} *3	4.81	mJ
Avalanche current		I _{AR} *3	3.8	Α
Power dissipation	T _c = 25°C	P_{D}	52	W
rower dissipation	$T_a = 25^{\circ}C^{*4}$	P_{D}	0.85	W
Junction temperature		T _j	150	°C
Range of storage temperature		T _{stg}	-55 to +150	°C

●Thermal resistance

Parameter	Symbol	Values			Unit
- Farameter	Symbol	Min.	Тур.	Max.	Offic
Thermal resistance, junction - case	R_{thJC}	-	-	2.36	°C/W
Thermal resistance, junction - ambient *4	R_{thJA}	-	-	147	°C/W
Soldering temperature, wavesoldering for 10s	T _{sold}	-	-	265	°C

• Electrical characteristics ($T_a = 25$ °C)

Darameter	Cumbal	Canditions	Values			Linit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V$, $I_D = 1mA$	190	-	-	V
Zero gate voltage drain current	lace	$V_{DS} = 190V, V_{GS} = 0V$ $T_j = 25^{\circ}C$	-	33	10	^
Zero gate voltage drain current	I _{DSS}	$V_{DS} = 190V, V_{GS} = 0V$ $T_j = 125^{\circ}C$		-	100	μΑ
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±100	nA
Gate threshold voltage	V _{GS (th)}	$V_{DS} = 10V, I_D = 1mA$	0.5	ı	2.5	V
		$V_{GS} = 10V, I_D = 3.8A$	-	240	336	
Static drain - source	*5	$V_{GS} = 4.0V, I_D = 3.8A$	-	248	347	m()
on - state resistance	R _{DS(on)} *5	$V_{GS} = 10V, I_D = 3.8A$ $T_j = 125$ °C	-	360	505	mΩ
Forward transfer admittance	9 _{fs}	$V_{DS} = 10V, I_D = 3.8A$	4	8	-	S



●Electrical characteristics(T_a = 25°C)

Parameter	Symbol Conditions		Values			Unit
r arameter	Syllibol	Conditions	Min.	Тур.	Max.	Offic
Input capacitance	C _{iss}	$V_{GS} = 0V$	-	1100	-	
Output capacitance	C _{oss}	V _{DS} = 25V	-	60	-	pF
Reverse transfer capacitance	C_{rss}	f = 1MHz	-	35		
Turn - on delay time	$t_{d(on)}^{*5}$	$V_{DD} \simeq 100V, V_{GS} = 10V$	-	12		
Rise time	t _r *5	$I_{D} = 3.8A$	-	14	-	no
Turn - off delay time	t _{d(off)} *5	$R_L = 26.7\Omega$	- (80	-	ns
Fall time	t _f *5	$R_G = 10\Omega$		45	-	

•Gate Charge characteristics($T_a = 25$ °C)

Parameter	Symbol	Conditions	Values			Unit
r arameter	Syllibol	Conditions	Min.	Тур.	Max.	Offic
Total gate charge	Q_g^{*5}	V _{DD} ≃ 100V	C	30	-	
Gate - Source charge	Q _{gs} *5	$I_D = 7.5A$	-	3.0	-	nC
Gate - Drain charge	Q _{gd} *5	V _{GS} = 10V	-	6.5	-	
Gate plateau voltage	V _(plateau)	$V_{DD} \approx 100V, I_D = 7.5A$	-	2.5	-	V

ullet Body diode electrical characteristics (Source-Drain)(T_a = 25°C)

Parameter	Symbol Conditions		Values			Unit
1 aranteter	Syllidor	Conditions	Min.	Тур.	Max.	Offic
Continuous source current	l _S *1	T _c = 25°C	-	-	7.5	Α
Pulsed source current	I _{SM} *2	1 _c = 25 0	-	ı	30	Α
Forward voltage	V_{SD}^{*5}	$V_{GS} = 0V, I_S = 7.5A$	-	1	1.5	V
Reverse recovery time	t _{rr} *5	I _S = 3.8A	-	70	ı	ns
Reverse recovery charge	Q _{rr} *5	di/dt = 100A/μs	-	180	-	nC

^{*1} Limited only by maximum temperature allowed.

^{*2} Pw \leq 10 μ s, Duty cycle \leq 1%

^{*3} L $^{\simeq}$ 500 μ H, V_{DD} = 50V, Rg = 25 Ω , starting T_j = 25°C

^{*4} Mounted on a epoxy PCB FR4 (20mm × 30mm × 0.8mm)

^{*5} Pulsed

Fig.1 Power Dissipation Derating Curve

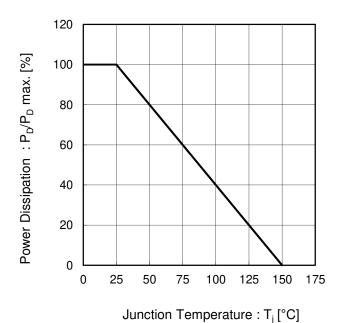
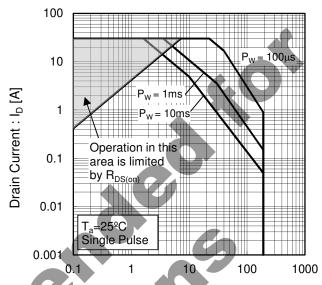
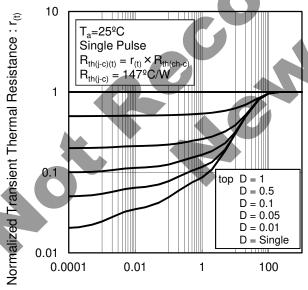


Fig.2 Maximum Safe Operating Area



Drain - Source Voltage : V_{DS} [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width



Pulse Width: Pw[s]

Fig.4 Avalanche Current vs Inductive Load

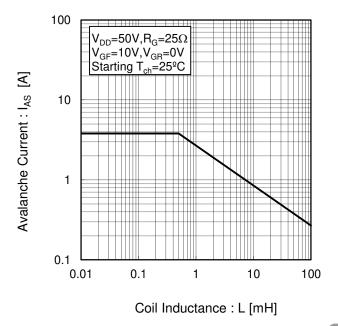
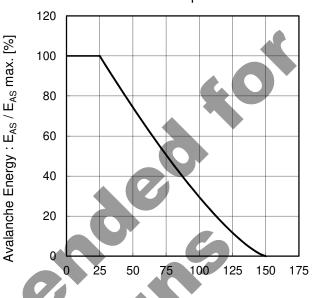
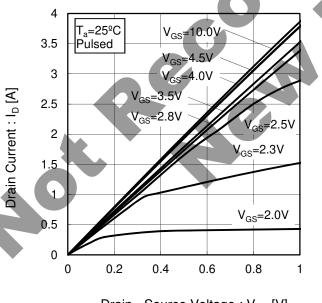


Fig.5 Avalanche Energy Derating Curve vs Junction Temperature



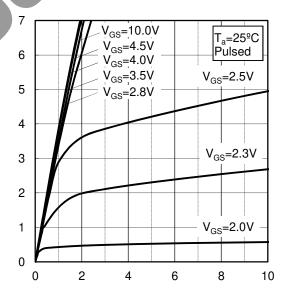
Junction Temperature : T_i [°C]

Fig.6 Typical Output Characteristics(I)



Drain - Source Voltage : $V_{DS}\left[V\right]$

Fig.7 Typical Output Characteristics(II)



Drain - Source Voltage : V_{DS} [V]

Drain Current : I_D [A]

5

• Electrical characteristic curves

Fig.8 Breakdown Voltage vs. Junction Temperature 250 Normarize Drain - Source Breakdown Voltage $V_{GS} = 0V$ 240 $I_D = 1mA$ 230 220 210 $: V_{(BR)DSS}[V]$ 200 190 180 170 160 150 -50 0 50 100 150 Junction Temperature : T_i [°C]

Fig.9 Typical Transfer Characteristics

Gate - Source Voltage : V_{GS} [V]

Fig.11 Transconductance vs. Drain Current

Fig.10 Gate Threshold Voltage vs. Junction Temperature 3.0 $V_{DS} = 10V$ $I_D = 1mA$ Gate Threshold Voltage: V_{GS(th)} [V] 2.5 2.0 1.5 1.0 0.5 0.0 -50 -25 0 25 50 75 100 125 150 Junction Temperature : T_i [°C]

100 V_{DS}= 10V T_a= -25°C T_a=25°C T_a=75°C T_a=125°C T_a=125

200

0

• Electrical characteristic curves

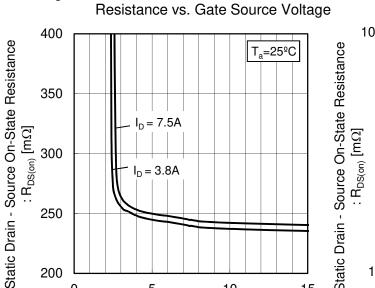


Fig.12 Static Drain - Source On - State

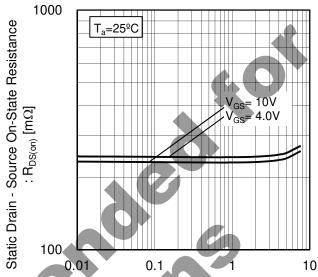
Gate - Source Voltage : V_{GS} [V]

10

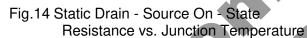
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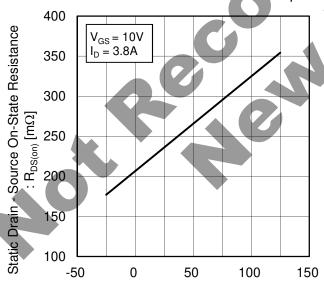
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Fig.13 Static Drain - Source On - State Resistance vs. Drain Current(I)



Drain Current : I_D [A]





Junction Temperature : T_j [${}^{\circ}C$]

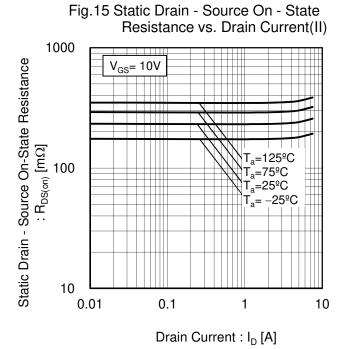


Fig.16 Static Drain - Source On - State
Resistance vs. Drain Current(III)

1000

V_{GS}= 4.0V

T_a=125°C

T_a=75°C

T_a=75°C

T_a=25°C

T_a=-25°C

Drain Current: I_D [A]

Fig.17 Drain Current Derating Curve

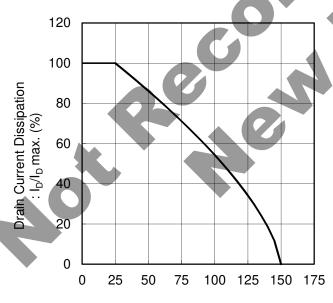


Fig.18 Typical Capacitance vs. Drain - Source Voltage

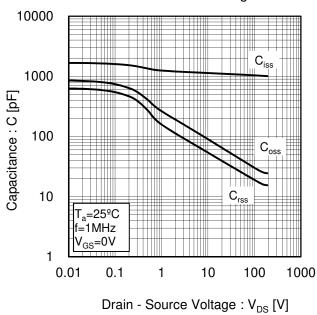
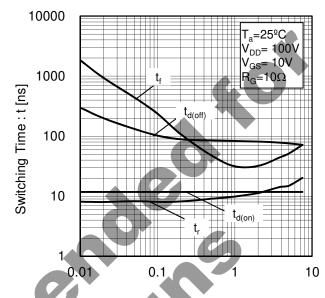
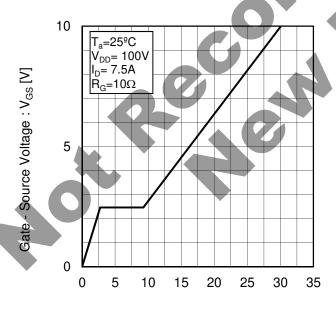


Fig.19 Switching Characteristics

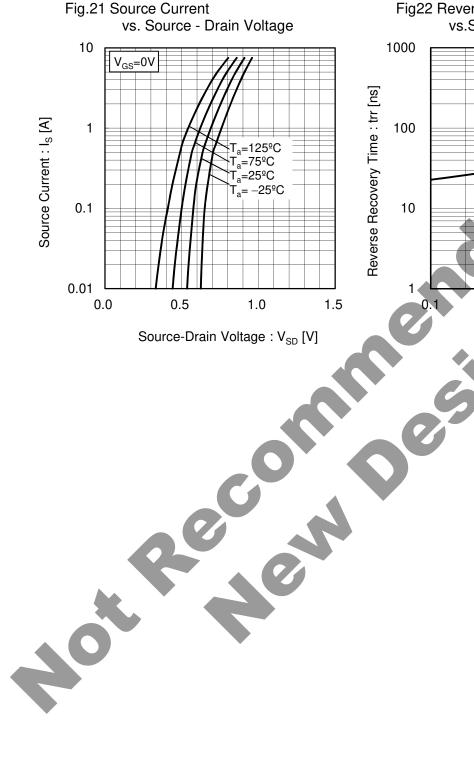


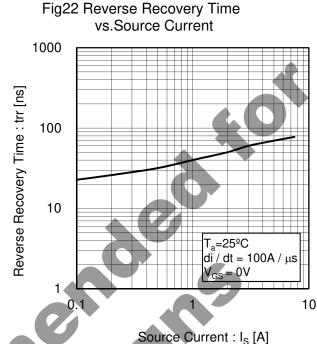
Drain Current : I_D [A]

Fig.20 Dynamic Input Characteristics



Total Gate Charge : Q_g [nC]







Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

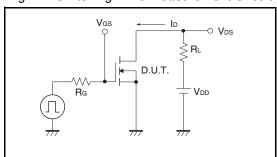


Fig.2-1 Gate Charge Measurement Circuit

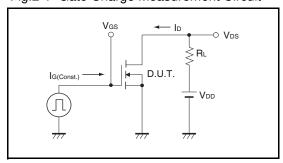


Fig.3-1 Avalanche Measurement Circuit

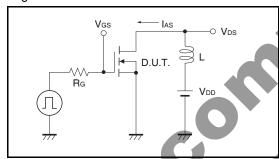


Fig.1-2 Switching Waveforms

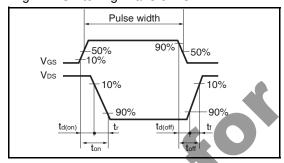


Fig.2-2 Gate Charge Waveform

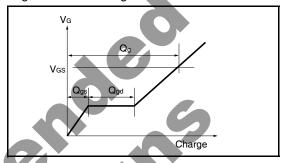
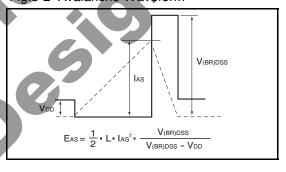
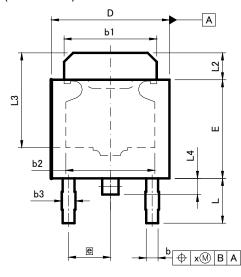


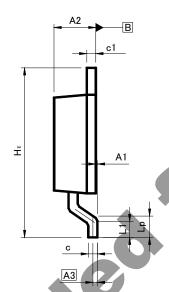
Fig.3-2 Avalanche Waveform

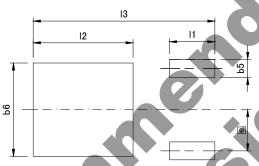


●Dimensions (Unit:mm)









DIM	MILIM	ETERS	INC	HES	
DIM	MIN	MAX	MIN	MAX	
A1	0.00	0.15	0	0.006	
A2	2.20	2.50	0.087	0.098	
A3	0.:	25	0.	01	
b	0.55	0.75	0.022	0.03	
b1	5.00	5.30	0.197	0.209	
b2	5.0	00	0.:	20	
b3	0.	75	0.	03	
С	0.40	0.60	0.016	0.024	
c1	0.40	0.60	0.016	0.024	
D	6.30	6.70	0.248	0.264	
E	5.40	5.80	0.213	0.228	
е	2.3	30	0.09		
HE	9.00	10.00	0.354	0.394	
L	2.20	2.80	0.087	0.11	
L1	0.80	1.40	0.031	0.055	
L2	1.20	1.80	0.047	0.071	
L3	5.30		0.209		
L4	0.90		0.035		
Lp	1.00	1.60	0.039	0.063	
х	_	0.25	_	0.01	

DIM	MILIM	ETERS	INCHES	
	MIN	MAX	MIN	MAX
b5	_	1.00	_	0.04
b6	-	5.20	_	0.205
I1	-	2.50	-	0.098
12	-	5.50	_	0.217
13	_	10.00	_	0.394

Dimension in mm/inches

Rev.003

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 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
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- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power, exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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