

Product Brief

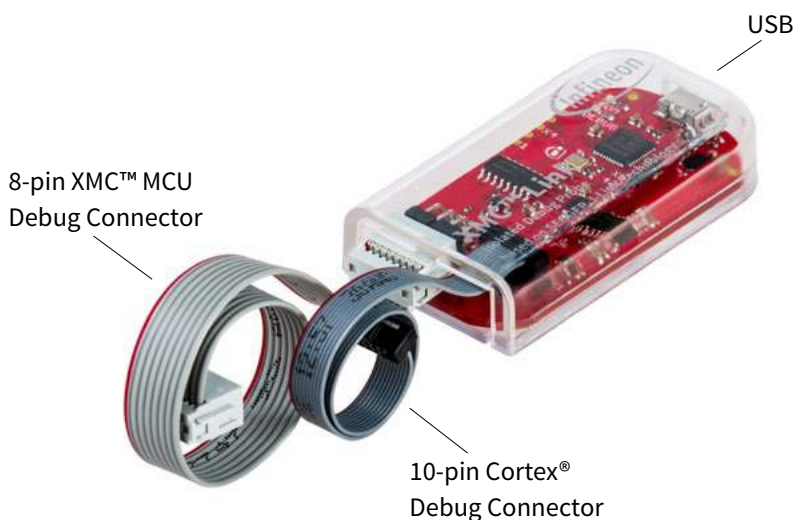
XMC™ Link – Isolated Debug Probe

Based on SEGGER J-Link Technology

XMC™ Link is an Isolated Debug Probe for all XMC™ Microcontrollers. The debug probe is based on SEGGER J-Link debug firmware, which enables use with DAVE™ and all major third-party compiler/IDEs known from the wide ARM® ecosystem.



XMC™ Link



Order number:
KIT_XMC_LINK_SEGGER_V1

Features

- > 1 kV DC functional isolation
- > Debug protocols
 - JTAG
 - Serial wire debug (SWD)
 - Infineon's single pin debug (SPD)
 - Serial wire viewer/output (SWV/SWO)
 - Virtual COM port (UART-to-USB bridge)

Target connectors

- > 10-pin Cortex® Debug Connector
- > 8-pin XMC™ MCU Debug Connector
- > 2.5 V to 5.5 V target voltage operation

Compiler/IDEs

- > Altium Limited
- > Atollic
- > ARM®/KEIL
- > IAR Systems
- > Infineon
- > Rowley Associates

| XMC™ MCUs | XMC1000 family | | XMC4000 family |
|-------------------------------------|--------------------------------|--------------------------------|--------------------------------|
| | 8-pin XMC™ MCU Debug Connector | 10-pin Cortex® Debug Connector | 10-pin Cortex® Debug Connector |
| Serial wire debug (SWD) | ☑ | ☑ | ☑ |
| Infineon's single pin debug (SPD) | ☑ | ☑ | |
| Serial wire viewer/output (SWV/SWO) | | | ☑ |
| UART-to-USB bridge (Virtual COM) | ☑ | | |
| JTAG | | | ☑ |

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