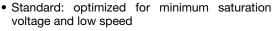


Insulated Gate Bipolar Transistor Ultralow V_{CE(on)}, 250 A



PRIMARY CHARACTERISTICS						
V _{CES}	600 V					
V _{CE(on)} (typical) at 200 A, 25 °C	1.16 V					
I _C at T _C = 90 °C	250 A					
Speed	DC to 1 kHz					
Package	SOT-227					
Circuit configuration	Single switch no diode					

FEATURES





ROHS

· Lowest conduction losses available

- Fully isolated package (2500 V_{AC})
- Very low internal inductance (5 nH typical)
- · Industry standard outline
- · Designed and qualified for industrial level
- UL approved file E78996
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

BENEFITS

- Designed for increased operating efficiency in power conversion: UPS, SMPS, TIG welding, induction heating
- Easy to assemble and parallel
- · Direct mounting to heatsink
- Plug-in compatible with other SOT-227 packages

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Collector to emitter voltage	V _{CES}		600	V		
Continuous collector current	I.	T _C = 25 °C	359	9		
Continuous collector current	IC	T _C = 90 °C	250	^		
Pulsed collector current	I _{CM}	$T_C = 175 ^{\circ}\text{C}, t_p = 6 \text{ms}, V_{GE} = 15 \text{V}$	945	A		
Clamped Inductive load current	I _{LM}		250			
Gate to emitter voltage	V_{GE}		± 20	V		
Power dissipation	D.	T _C = 25 °C	750	w		
rower dissipation	P _D	T _C = 90 °C	425	7 **		
Isolation voltage	V _{ISOL}	Any terminal to case, t = 1 min	2500	V		

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V _{(BR)CES}	$V_{GE} = 0 \text{ V}, I_{C} = 0.4 \text{ mA}$		600	-	-	
		I _C = 100 A		-	1.01	1.16	v
		I _C = 200 A		-	1.16	-	
Callactar to amittar valtage	V _{CE(on)}	$I_{C} = 100 \text{ A}, T_{J} = 125 ^{\circ}\text{C}$	V _{GE} = 15 V	-	0.96	-	
Collector to emitter voltage		I _C = 200 A, T _J = 125 °C		-	1.18	-	
		I _C = 100 A, T _J = 150 °C		-	0.95	-	
		I _C = 200 A, T _J = 150 °C		-	1.18	-	
Gate threshold voltage	and the second of the second			3.8	4.9	6.3	
Gate threshold voltage	V _{GE(th)}	$V_{CE} = V_{GE}$, $I_C = 2$ mA, T_C	_J = 125 °C	-	3.5	-	
Temperature coefficient of threshold voltage	$\Delta V_{GE(th)}/\Delta T_{J}$	$V_{CE} = V_{GE}, I_{C} = 2 \text{ mA}, 25$	5 °C to 125 °C	-	-14	-	mV/°C
	I _{CES}	$V_{GE} = 0 \text{ V}, V_{CE} = 600 \text{ V}$		-	0.2	100	
Collector to emitter leakage current		$V_{GE} = 0 \text{ V}, V_{CE} = 600 \text{ V}, T_{J} = 125 ^{\circ}\text{C}$		-	51	-	μA
		$V_{GE} = 0 \text{ V}, V_{CE} = 600 \text{ V},$	T _J = 150 °C	-	508	-	
Gate to emitter leakage current	I _{GES}	V _{GE} = ± 20 V		-	-	± 250	nA



SWITCHING CHARACTERISTIC	CS (T _J = 25	°C unless otherwise	specified)				
PARAMETER	SYMBOL	TEST CONDIT	IONS	MIN.	TYP.	MAX.	UNITS
Total gate charge (turn-on)	Qg			-	909	-	
Gate-to-emitter charge (turn-on)	Q _{ge}	$I_C = 75 \text{ A}, V_{CC} = 520 \text{ V},$	I _C = 75 A, V _{CC} = 520 V, V _{GE} = 15 V		139	-	nC
Gate-to-collector charge (turn-on)	Q _{gc}			-	249	-	
Turn-on switching loss	Eon			-	1.61	-	
Turn-off switching loss	E _{off}	T _J = 25 °C		-	6.65	-	mJ
Total switching loss	E _{tot}	I _C = 100 A		-	8.26	-	
Turn-on delay time	t _{d(on)}	V _{CC} = 480 V V _{GE} = 15 V		-	469	-	
Rise time	t _r	$R_0 = 5.0 \Omega$		-	36	-	
Turn-off delay time	t _{d(off)}	L = 500 µH	Energy	-	539	-	ns
Fall time	t _f		losses	-	109	-	
Turn-on switching loss	E _{on}	$T_{J} = 125 ^{\circ}\text{C}$ $I_{C} = 100 \text{A}$ $V_{CC} = 480 \text{V}$ $V_{GE} = 15 \text{V}$ $R_{g} = 5.0 \Omega$ $L = 500 \mu\text{H}$	C = 100 A C = 480 V C = 480 V C = 15 V C = 15 V C = 15 V	-	2.03	-	mJ
Turn-off switching loss	E _{off}			-	9.65	-	
Total switching loss	E _{tot}			-	11.68	-	
Turn-on delay time	t _{d(on)}			-	498	-	
Rise time	t _r			-	43	-	
Turn-off delay time	t _{d(off)}			-	640	-	ns
Fall time	t _f			-	128	-	
Internal emitter inductance	LE	Between lead and center of die contact		-	5.0	-	nH
Input capacitance	C _{ies}	V _{GE} = 0 V, V _{CC} = 25 V, f = 1.0 MHz		-	24 200	-	
Output capacitance	C _{oes}			-	300	-	pF
Reverse transfer capacitance	C _{res}			-	84	-	
Reverse bias safe operating area	RBSOA	$T_J = 175~^{\circ}\text{C}, \ I_C = 250~\text{A}, \ R_g = 5.0~\Omega, \\ V_{GE} = 15~\text{V to 0 V}, \ V_{CC} = 400~\text{V}, \\ V_p = 600~\text{V} $ Fullsquare					

THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction and storage temperature range	T _J , T _{Stg}		-40	-	175	°C
Thermal resistance junction to case	R _{thJC}		-	-	0.2	°C/W
Thermal resistance case to heatsink	R _{thCS}	Flat, greased surface	-	0.05	-	C/VV
Weight			-	30	-	g
Mounting towns		Torque to terminal		-	1.1 (9.7)	Nm (lbf.in)
Mounting torque		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)
Case style		SOT-227				

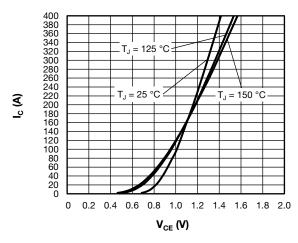


Fig. 1 - Typical Trench IGBT Output Characteristics, V_{GE} = 15 V

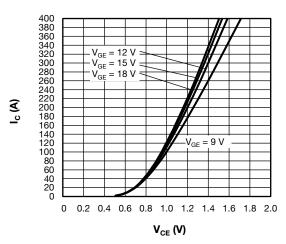


Fig. 2 - Typical Trench IGBT Output Characteristics, T_J = 125 °C

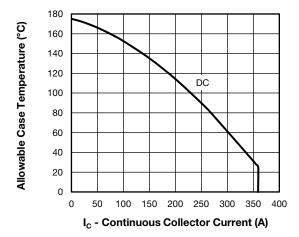


Fig. 3 - Typical Trench IGBT Continuous Collector Current vs. Case Temperature

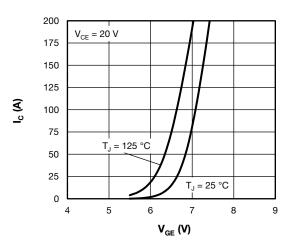


Fig. 4 - Typical Trench IGBT Transfer Characteristics

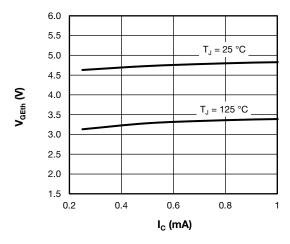


Fig. 5 - Typical Trench IGBT Gate Threshold Voltage

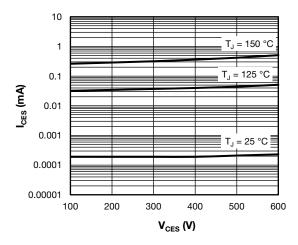


Fig. 6 - Typical Trench IGBT Zero Gate Voltage Collector Current

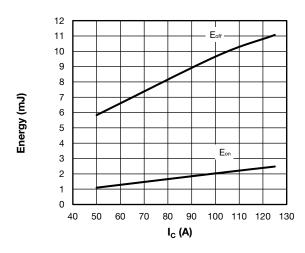


Fig. 7 - Typical Trench IGBT Energy Loss vs. I_C T_J = 125 °C, V_{CC} = 480 V, R_g = 5 Ω , V_GE = +15 V/-15 V, L = 500 μ H

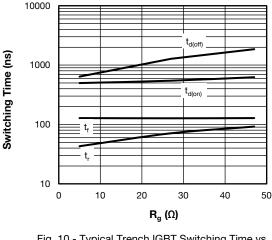


Fig. 10 - Typical Trench IGBT Switching Time vs. R_g T_J = 125 °C, V_{CC} = 480 V, I_C = 100 A, V_{GE} = +15 V/-15 V, L = 500 μH

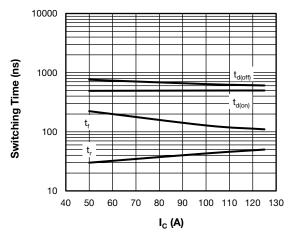


Fig. 8 - Typical Trench IGBT Switching Time vs. I_C T $_J$ = 125 °C, V $_C$ = 480 V, R $_q$ = 5 $\Omega,$ V $_G$ = +15 V/-15 V, L = 500 μH

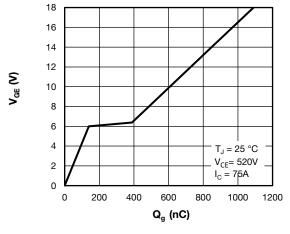


Fig. 11 - Typical Trench IGBT Gate Charge vs. Gate to Emitter Voltage

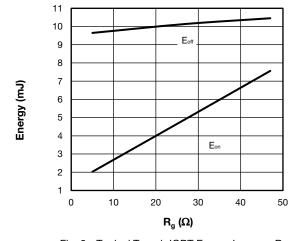


Fig. 9 - Typical Trench IGBT Energy Loss vs. R_g T_J = 125 °C, V_{CC} = 480 V, I_C = 100 A, V_{GE} = +15 V/-15 V, L = 500 μH

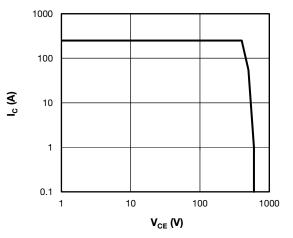


Fig. 12 - Typical Trench IGBT Reverse BIAS SOA T_J = 175 °C, I_C = 250 A, R_g = 4.7 Ω , V_{GE} = +15 V/0 V, V_{CC} = 400 V, V_p = 600 V

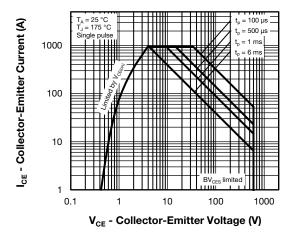


Fig. 13 - Typical Trench IGBT Safe Operating Area

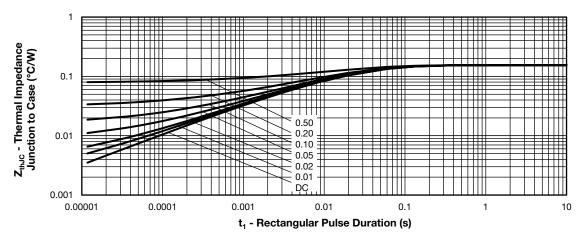
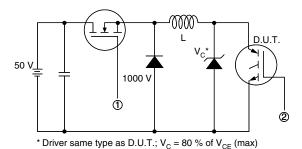


Fig. 14 - Maximum Thermal Impedance Z_{thJC} Characteristics



Note: Due to the 50 V power supply, pulse width and inductor will increase to obtain rated I_d

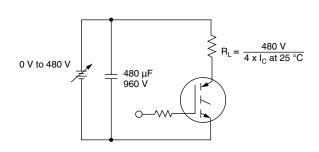


Fig. 15 - Clamped Inductive Load Test Circuit

Fig. 16 - Pulsed Collector Current Test Circuit

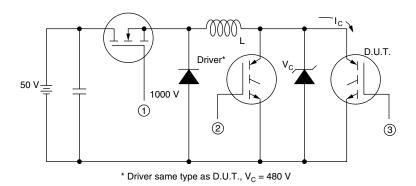


Fig. 17 - Switching Lost Test Circuit

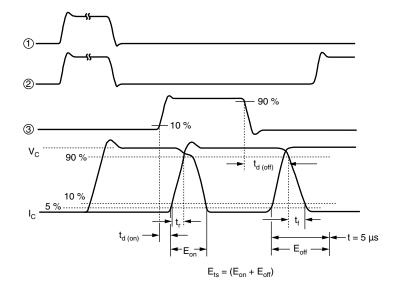
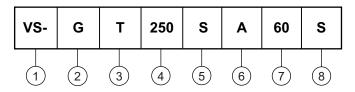


Fig. 18 - Switching Loss Waveforms

ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Insulated gate bipolar transistor (IGBT)

3 - Trench IGBT silicon

4 - Current rating (250 = 250 A)

5 - Circuit configuration (S = single switch no diode)

6 - Package indicator (A = SOT-227)

7 - Voltage rating (60 = 600 V)

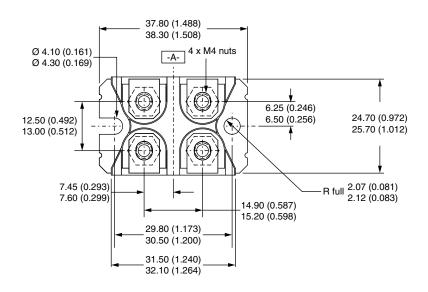
Speed/type (S = standard speed)

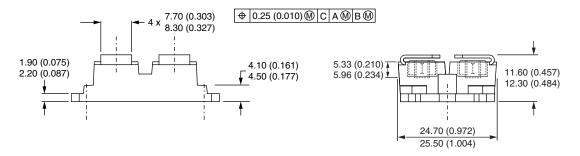
CIRCUIT CONFIGURATION						
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING				
Single switch, no diode	S	2 (G) O Lead Assignment 4 1 N-channel				

LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95423</u>					
Packaging information	www.vishay.com/doc?95425				

SOT-227 Generation 2

DIMENSIONS in millimeters (inches)





Note

· Controlling dimension: millimeter



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