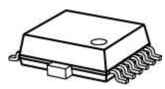
Smart High-Side Power Switch
Two Channels: 2 x 25mΩ
IntelliSense

Product Summary

Operating voltage	$V_{bb(on)}$	4,5...28 (Loaddump: 40 V)		V
Active channels		one	two parallel	
On-state resistance	R_{ON}	25	13	mΩ
Nominal load current	$I_{L(nom)}$	6	9,1	A
Current limitation Low	$I_{L(SCr)}$	10		A
High		40		

Package



P-DSO-12-2

General Description

- N channel vertical power MOSFET with charge pump, ground referenced CMOS compatible input and diagnostic feedback, monolithically integrated in Smart SIPMOS® technology.
- Providing embedded protective functions.
- Extern adjustable current limitation.

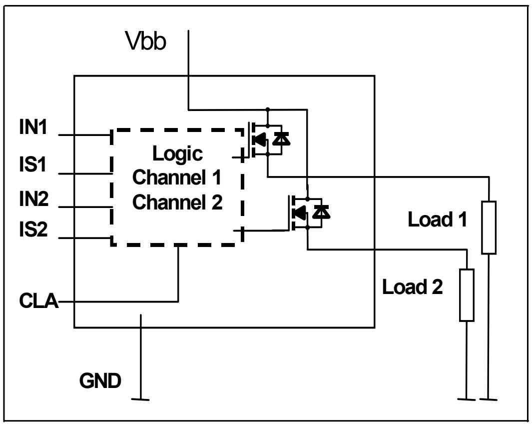
Application

- All types of resistive, inductive and capacitive loads
- µC compatible high-side power switch with diagnostic feedback for 12 V grounded loads
- Due to the adjustable current limitation best suitable for loads with high inrush currents, so as lamps
- Replaces electromechanical relays, fuses and discrete circuits

Basic Functions

- Very low standby current
- CMOS compatible input
- Improved electromagnetic compatibility (EMC)
- Stable behaviour at low battery voltage

Block Diagram



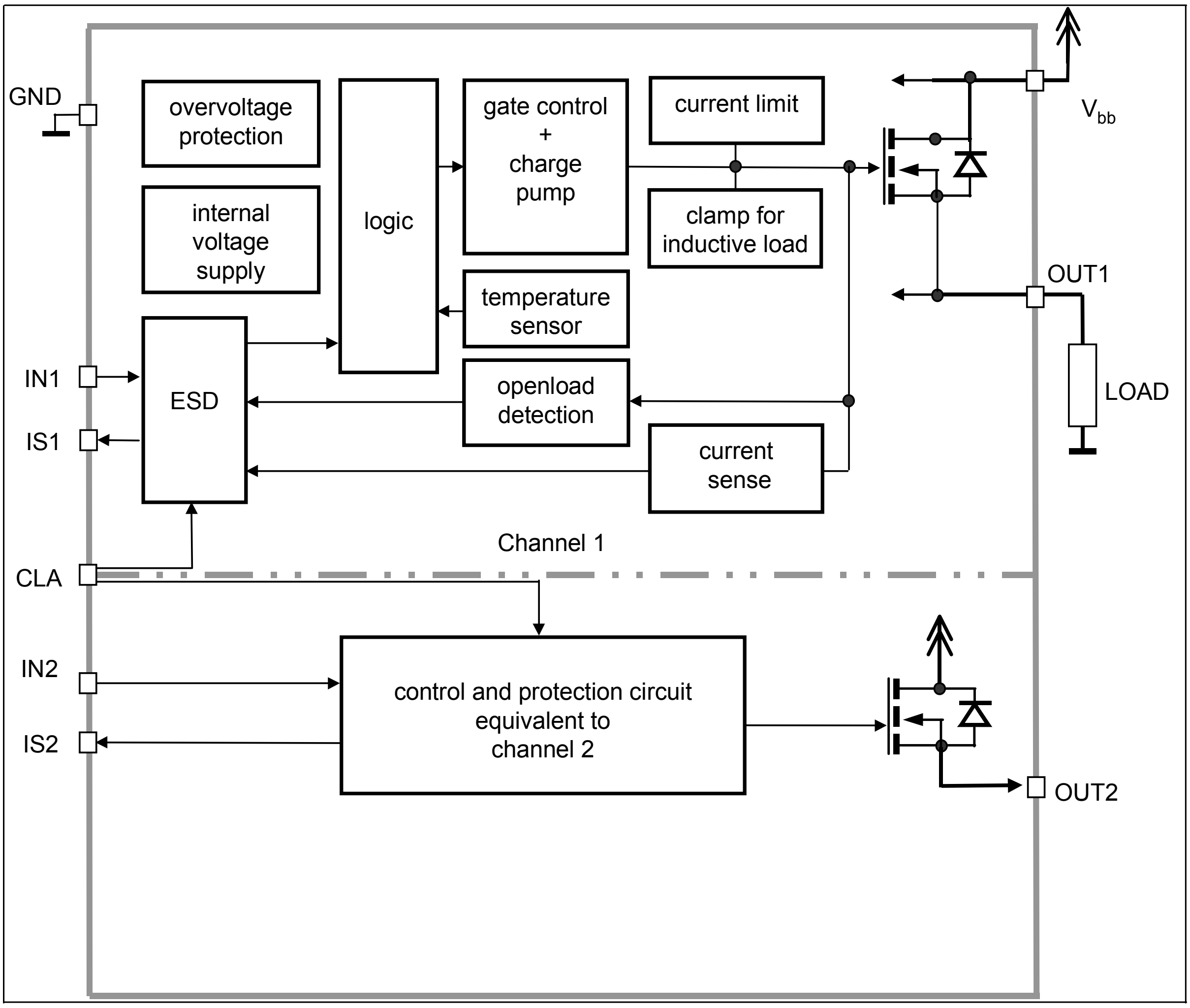
Protection Functions

- Reverse battery protection with external resistor
- Short circuit protection
- Overload protection
- Current limitation
- Thermal Shutdown
- Overvoltage protection with external resistor
- Loss of GND and loss of V_{bb} protection
- Electrostatic discharge Protection (ESD)

Diagnostic Function: IntelliSense

- Proportional load current sense (with defined fault signal during thermal shutdown and overload)
- Additional open load detection in OFF - state
- Suppressed thermal toggling of fault signal

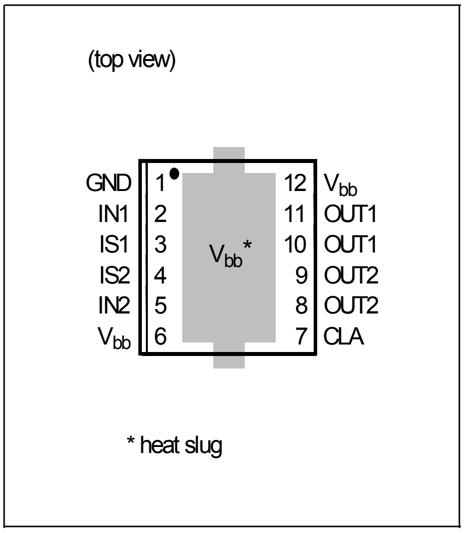
Functional diagram



Pin definition and function

Pin	Symbol	Function
2	IN1	**Input 1,2** activates channel1,2 in case of logic high signal
5	IN2	
3	IS1	**Diagnostic feedback 1 & 2** of channel 1,2 On state: advanced current sense with defined signal in case of overload or short circuit Off state: High on failure
4	IS2	
1	GND	**Ground** of chip
6,12, heat slug	V_{bb}	**Positive power supply voltage**. Design the wiring for the simultaneous max. short circuit currents from channel 1 to 2 and also for low thermal resistance
7	CLA	**Current limit adjust**, the current limit for both channels can be chosen as high (potential < 2V) or low (potential > 4V).
8,9	OUT2	**Output 1,2** protected high-side power output of channel 1,2. Design the wiring for the max. short circuit current.
10,11	OUT1	

Pin configuration



Maximum Ratings at T_j=25°C, unless otherwise specified

Parameter	Symbol	Value	Unit
Supply voltage (overvoltage protection see page 6)	V_{bb}	28[1]	V
Supply voltage for full short circuit protection; T_j = -40...150°C	$V_{bb(SC)}$	28[2]	
Maximum voltage across DMOS	V_{ON}	52	
Load dump protection[3] $V_{LoadDump} = V_A + V_S$; V_A = 13,5 V In = low or high; t_d = 400 ms; R_I[4] = 2 Ω R_L = 2.25 Ω R_L = 6.8 Ω	$V_{Loaddump}$	 40 53	
Load current (Short - circuit current, see page 7)	I_L	$I_{L(lim)}$[5]	A
Operating temperature range	T_j	-40...+150	°C
Storage temperature range	T_{stg}	-55...+150	
Dynamical temperature rise at switching	dT	60	K
Power dissipation[6] (DC), one channel active T_A = 85 °C	P_{tot}	1,4	W
Maximal switchable inductance, single pulse V_{bb}=12V, T_{jstart}=150°C; (see diagrams on page 12) I_L = 6 A, E_{AS} = 0.319 J, R_L = 0 Ω, one channel: I_L = 12 A, E_{AS} = 0.679 J, R_L = 0 Ω, two parallel channels:	$Z_{L(s)}$	 9.8 5.2	mH
Electro**s**tatic **d**ischarge voltage IN: (Human Body Model) IS: according to ANSI EOS/ESD - S5.1 - 1993 , ESD STM5.1 - 1998 OUT:	V_{ESD}	1.0 2.0 4.0	kV
Continuous input voltage	V_{IN}	-10...16	V
Voltage at current limit adjustment pin	V_{CLA}	-10...16	
Current through current limit adjustment pin	I_{CLA}	±5.0	mA
Current through input pin (DC)	I_{IN}	±5.0	
Current through sense (DC) (see page 11)	I_{IS}	-5...+10	

[1] 18...28 V for 100 hours

[2] only single pulse, R_L = 200 mΩ ; L = 8 µH ; R and L are describing the complete circuit impedance including line, contact and generator impedances.

[3] Supply voltage higher than $V_{bb(AZ)}$ require an external current limit for the GND(150Ω resistor) and sense pin.

[4] R_I = internal resistance of the load dump test pulse generator.

[5] Current limit is a protection function. Operation in current limitation is considered as "outside" normal operating range. Protection functions are not designed for continuous repetitive operation.

[6] Device on 50mm*50mm*1.5mm epoxy PCB FR4 with 6 cm2 (one layer, 70µm thick) copper area for V_{bb} connection. PCB is vertical without blown air.

Electrical Characteristics

Parameter and Conditions, each of the two channels at T_j = -40...+150 °C, V_{bb} = 9...16 V, unless otherwise specified	Symbol	Values			Unit
		min.	typ.	max.	
Thermal Resistance					
junction - case each channe	R_{thJC}	-	-	1.8	K/W
junction - ambient[1)] one channel active:	R_{thJA}	-	40	-	K/W
all channels active:		-	33	-	

Load Switching Capabilities and Characteristics

On-state resistance (V_{bb} to OUT), (see page 13)	R_{ON}				mΩ
T_j = 25 °C, I_L = 5 A, each channel:		-	21	25	
T_j = 150 °C, each channel:		-	42	50	
T_j = 25 °C, two parallel channels:		-	11	13	
Nominal load current[1)]	$I_{L(nom)}$				A
T_a = 85°C, $T_j \leq$ 150°C , one channel active:		5.5	6	-	
two channels active, per channel:		4.1	4.5	-	
Nominal load current; ISO Norm	$I_{L(ISO)}$				
T_C = 85 °C, V_{ON} = 0.5 V, one channel active:		13	15	-	
two channels active, per channel:		13	15	-	
Output voltage drop limitation at small load currents I_L = 0.5 A	$V_{ON(NL)}$	-	40	-	mV
Output current while GND disconnected[2)] (see diagram page 12) V_{IN} = 0 V	$I_{L(GNDhigh)}$	-	-	2	mA

[1]Device on 50mm*50mm*1.5mm epoxy PCB FR4 with 6 cm2 (one layer, 70µm thick) copper area for V_{bb} connection. PCB is vertical without blown air.

[2]not subject to production test, specified by design

Electrical Characteristics

Parameter and Conditions, each of the two channels at T_j = -40...+150 °C, V_{bb} = 9...16 V, unless otherwise specified	Symbol	Values			Unit
		min.	typ.	max.	
Load Switching Capabilities and Characteristics					
Turn-on time[1)] to 90% V_{OUT} R_L = 12 Ω, V_{bb} = 12 V	t_{on}	-	90	200	µs
Turn-off time[1)] to 10% V_{OUT} R_L = 12 Ω, V_{bb} = 12 V	t_{off}	-	100	220	
Slew rate on[1)] 10 to 30% V_{OUT}, R_L = 12 Ω, V_{bb} = 12 V	dV/dt_{on}	0.1	0.25	0.45	V/µs
Slew rate off[1)] 70 to 40% V_{OUT}, R_L = 12 Ω, V_{bb} = 12 V	$-dV/dt_{off}$	0.09	0.25	0.4	

Operating Parameters

Operating voltage[2)]	$V_{bb(on)}$	4.5	-	28	V
Overvoltage protection[3)] I_{bb} = 40 mA	$V_{bb(AZ)}$	41	47	52	
Standby current[4)] (see diagram on page 13) T_j = -40...+25 °C, V_{IN} = 0 V T_j = 150 °C	$I_{bb(off)}$	 - -	 5 -	 7.5 20	µA

[1]See timing diagram on page 14.

[2]18V...28V for 100 hours

[3]Supply voltages higher than $V_{bb(AZ)}$ require an external current limit for the status pin and GND pin (e.g. 150Ω). See also $V_{Out(CL)}$ in table of protection functions and circuit diagram on page 11.

[4]Measured with load; for the whole device; all channels off.

Electrical Characteristics

Parameter and Conditions, each of the two channels at T_j = -40...+150 °C, V_{bb} = 9...16 V, unless otherwise specified	Symbol	Values min.	Values typ.	Values max.	Unit
Operating Parameters					
Off-State output current (included in $I_{bb(off)}$) V_{IN} = 0 V, each channel	$I_{L(off)}$	-	1.5	8	µA
Operating current[1] V_{IN} = 5 V, per active channel	I_{GND}	-	1.6	4	mA
Protection Functions[2]					
Current limit, (see timing diagrams, page 15)	$I_{L(LIM)}$				A
Low level; if potential at CLA = high		7	11	14	
High level; if potential at CLA = low		40	50	60	
Current limit adjustment threshold voltage	$V_{CLA(T-)}$	2.0	-	-	V
	$V_{CLA(T+)}$	-	-	4.0	
Repetitive short circuit current limit $T_j = T_{jt}$ (see timing diagrams on page 15)	$I_{L(SCr)}$				A
High level one active channel:		-	40	-	
two active channels[3]:		-	40	-	
Low level one active channel:		-	7	-	
two active channels[3]:		-	7	-	
Initial short circuit shutdown time low level:	$t_{off(SC)}$	-	4	-	ms
$T_{j,start}$ = 25°C ; V_{bb} = 13,5 V high level:		-	0.8	-	
Output clamp (inductive load switch off)[4] I_L = 40 mA	$V_{Out(CL)}$	-	-15	-	V
Thermal overload trip temperature	T_{jt}	150	170	-	°C
Thermal hysteresis	ΔT_{jt}	-	10	-	K

[1] Add I_{IS}, if $I_{IS} > 0$

[2] Integrated protection functions are designed to prevent IC destruction under fault conditions described in the data sheet. Fault conditions are considered as "outside" normal operating range. Protection functions are not designed for continuous repetitive operation.

[3] At the beginning of the short circuit the double current is possible for a short time.

[4] If channels are connected in parallel, output clamp is usually accomplished by the channel with the lowest $V_{Out(CL)}$.

Electrical Characteristics

Parameter and Conditions, each of the two channels at T_j = -40...+150 °C, V_{bb} = 9...16 V, unless otherwise specified	Symbol	Values min.	Values typ.	Values max.	Unit
Diagnostic Characteristics					
Open load detection voltage	$V_{OUT(OL)}$	2	3.2	4.4	V
Internal output pull down[1)] V_{OUT} = 13.5 V	$R_{OUT(PD)}$	11	23	35	kΩ
Current sense ratio, static on-condition $k_{ILIS} = I_L:I_{IS}$	k_{ILIS}				
I_L = 0.5 A		4640	5800	6960	
I_L = 3 A		4900	5400	5900	
I_L = 6 A		4900	5350	5800	
Sense signal in case of fault-conditions[2)] in off-state	V_{fault}	5	6.2	7.5	V
Current saturation of sense fault signal	I_{fault}	4	-	-	mA
Sense signal delay after thermal shutdown[3)]	$t_{delay(fault)}$	-	-	1.2	ms
Current sense output voltage limitation I_{IS} = 0 , I_L = 5 A	$V_{IS(lim)}$	5.4	6.5	7.3	V
Current sense leakage/offset current V_{IN} = 5 V, I_L = 0 , V_{IS} = 0	$I_{IS(LH)}$	-	-	5	µA
Current sense settling time to I_{IS} static ±10% after positive input slope[4)] , I_L = 0 to 5A	$t_{son(IS)}$	-	-	400	µs
Current sense settling time to I_{IS} static ±10% after change of load current[4)], I_L = 2.5 to 5A	$t_{slc(IS)}$	-	-	300	

[1]In case of floating output, the status doesn´t show open load.

[2]Fault condition means output voltage exceeds open load detection voltage $V_{OUT(OL)}$

[3]In the case of thermal shutdown the V_{fault} signal remains for $t_{delay(fault)}$ longer than the restart of the switch (see diagram on page 16). Not subject to production test, specified by design.

[4]not subject to production test, specified by design

Electrical Characteristics

Parameter at T_j = -40...+150 °C, V_{bb} = 9...16 V, unless otherwise specified	**Symbol**	**Values**			**Unit**
		min.	**typ.**	**max.**	
Diagnostic Characteristics					
Status invalid after negative input slope	$t_{d(SToff)}$	-	-	1.2	ms
Status invalid after positive input slope with open load	$t_{d(STOL)}$	-	-	20	µs

Input Feedback[1)]

Input resistance (see circuit page 11)	R_I	2.0	3.5	5.5	kΩ
Input turn-on threshold voltage	$V_{IN(T+)}$	-	-	2.4	V
Input turn-off threshold voltage	$V_{IN(T-)}$	1.0	-	-	
Input threshold hysteresis	$\Delta V_{IN(T)}$	-	0.5	-	
Off state input current V_{IN} = 0.4 V	$I_{IN(off)}$	3	-	40	µA
On state input current V_{IN} = 5 V	$I_{IN(on)}$	20	50	90	

Reverse Battery[2)]

Reverse battery voltage	$-V_{bb}$	-	-	27	V
Drain-source diode voltage (V_{OUT} > V_{bb}) T_j = 150 °C, I_{bb} = -10 mA	$-V_{ON}$	-	330	-	mV

[1] If ground resistors R_{GND} are used, add the voltage drop across these resistor.

[2] Requires a 150Ω resistor in GND connection. The reverse load current through the intrinsic drain-source diode has to be limited by the connected load. Power dissipation is higher compared to normal operating conditions due to the voltage drop across the drain-source diode. The temperature protection is not active during reverse current operation! Input and status currents have to be limited. (see max. ratings page 4)

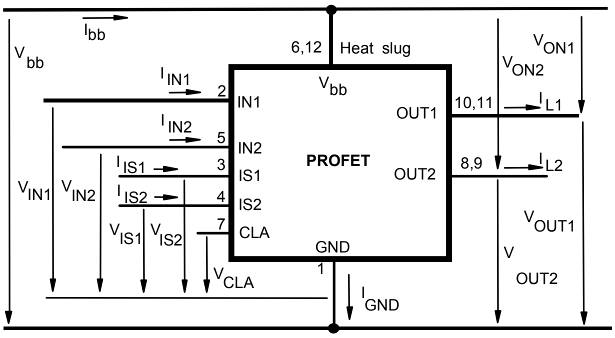
Truth Table - for each of the two channels

	Input level	Output level	Diagnostic output
Normal Operation	L H	L V_{bb}	Z[1)] $I_{IS} = I_L$ / kilis
Current Limitation[2)]	H	V_{bb}	V_{fault}
Short circuit to GND	L H	L L	Z[1)] V_{fault}
Overtemperature	L H	L L	Z[1)] V_{fault}
Short circuit to V_{bb}	L H	V_{bb} V_{bb}	V_{fault} $< I_{IS} = I_L$ / kilis[3)]
Open load	L H	$>V_{out(OL)}$ V_{bb}	V_{fault} Z[1)]

L = " Low" Level Z = high impedance, potential depends on external circuit
H = "High" Level V_{fault} = 5V typ., constant voltage independent of external sense resistor.

Parallel switching of channels is possible by connecting the inputs and outputs parallel. The current sense ouputs have to be connected with a single sense resistor.

Terms



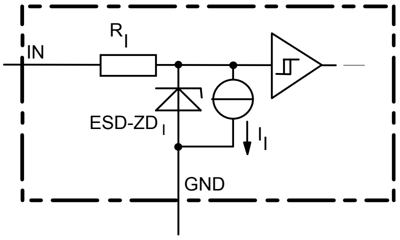
Leadframe / heat slug (V_{bb}) is connected to pin 6,12.

[1]L-potential by using a sense resistor

[2]Current limitation is only possible while the device is switched on.

[3]Low ohmic short to V_{bb} may reduce the output current I_L and therefore also the sense current I_{IS}.

Input circuit (ESD protection), IN1 or IN2



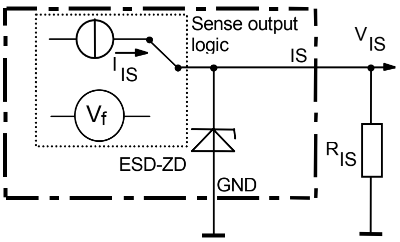
The use of ESD zener diodes as voltage clamp at DC conditions is not recommended.

Sense-Status output, IS1 or IS2

ON-State: Normal operation: $I_S = I_L / k_{ILIS}$

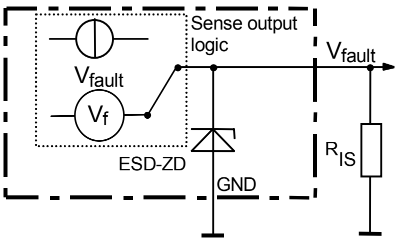
$V_{IS} = I_S * R_{IS}$; R_{IS} = 1kΩ nominal

R_{IS} > 500Ω



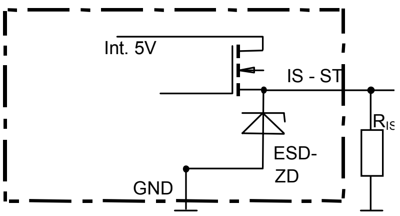
ESD zener diode: V_{ESD} = 6,1 V typ., max. 14 mA ;

ON-State: Fault condition so as thermal shut down or current limitation



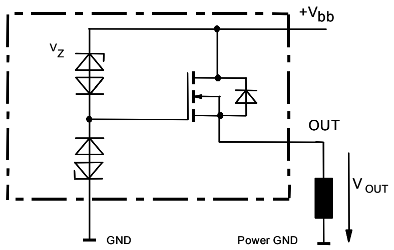
V_{fault} = 6 V typ ; $V_{fault} < V_{ESD}$ under all conditions

OFF-State diagnostic condition:
Open Load, if V_{OUT} > 3 V typ.; IN low



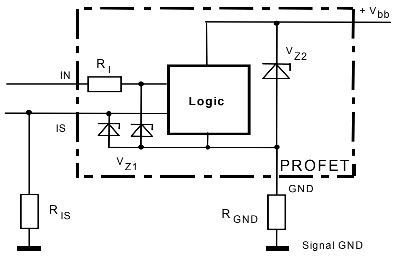
ESD-Zener diode: 6,1V typ., max. 5mA; $R_{ST(ON)}$ < 375Ω at 1,6mA.. The use of ESD zener diodes as voltage clamp at DC conditions is not recommended.

Inductive and overvoltage output clamp, OUT1 or OUT2



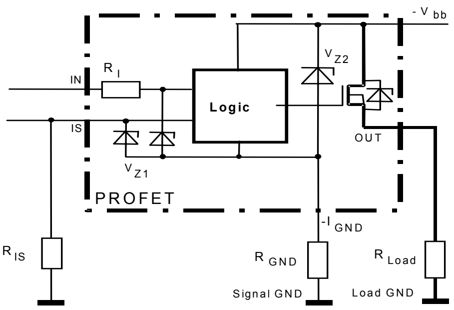
V_{Out} clamped to $V_{Out(CL)}$ = -15 V typ.

Overvolt. Protection of logic part
OUT1 or OUT2



V_{Z1} = 6,1V typ., V_{Z2} = 47V typ., R_{GND} = 150Ω ,
R_{IS} = 1kΩ , R_I = 3,5kΩ typ.

Reverse battery protection
OUT1 or OUT2

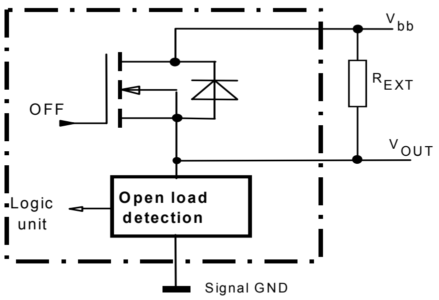


V_{Z1} = 6,1V typ., V_{Z2} = 47V typ., R_{GND} = 150Ω
R_{IS} = 1kΩ, R_I = 3,5kΩ typ.

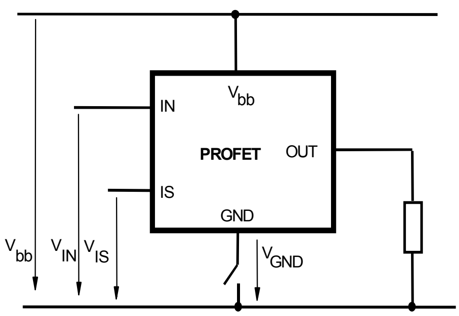
In case of reverse battery the load current has to be limited by the load. Protection functions are not active.

Open load detection, OUT1 or 2

Off-state diagnostic condition:
Open load, if V_{OUT} > 3 V typ.; IN = low

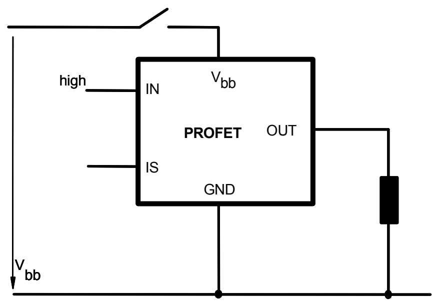


GND disconnect



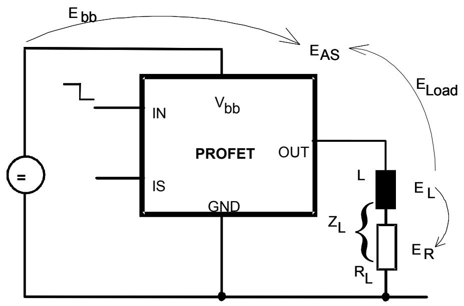
Any kind of load.

Vbb disconnect with energized inductive load



For inductive load currents up to the limits defined by Z_L each switch is protected against loss of V_{bb}.
(max. ratings and diagram on page 12)
Consider at your PCB layout that in the case of Vbb disconnection with energized inductive load all the load current flows through the GND connection.

Inductive load switch-off energy dissipation

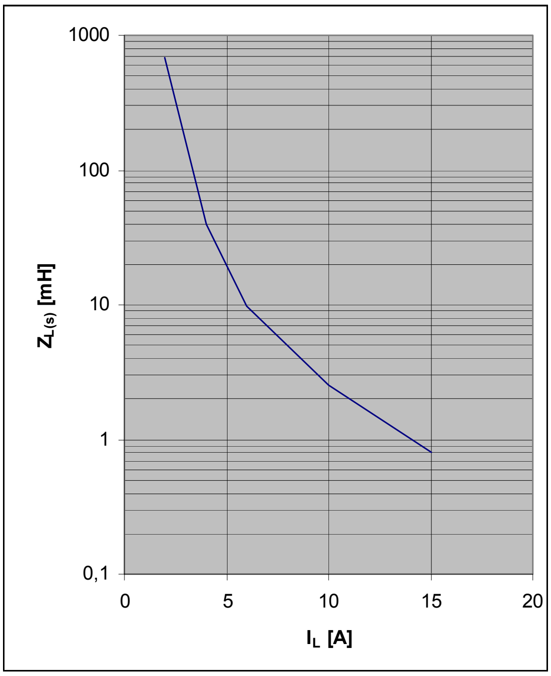


Energy stored in load inductance: $E_L = ½ * L * I_L^2$
While demagnetizing load inductance,
the enérgy dissipated in PROFET is
$E_{AS} = E_{bb} + E_L - E_R = V_{ON(CL)} * i_L(t)\, dt$,
with an approximate solution for $R_L > 0Ω$:

$$E_{AS} = \frac{I_L * L}{2 * R_L} * (V_{bb} + |V_{OUT(CL)}|) * \ln\left(1 + \frac{I_L * R_L}{|V_{OUT(CL)}|}\right)$$

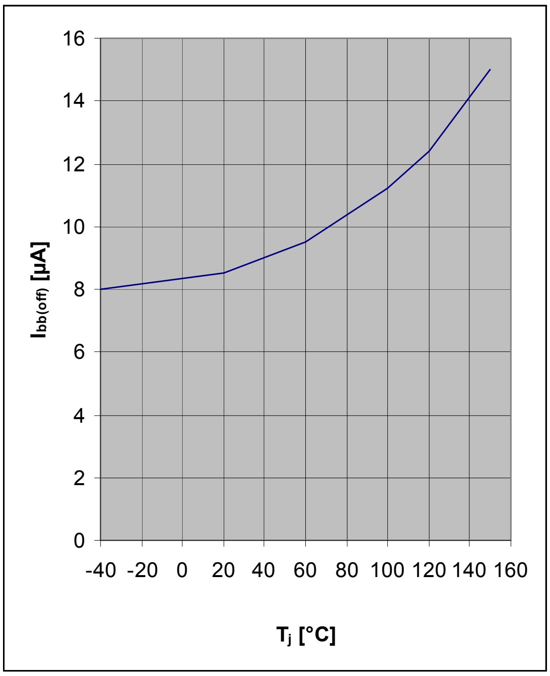
Maximum allowable load inductance for a single switch off (one channel)

L =f(IL); T_{jstart} = 150°C, V_{bb} = 12V, R_L = 0Ω



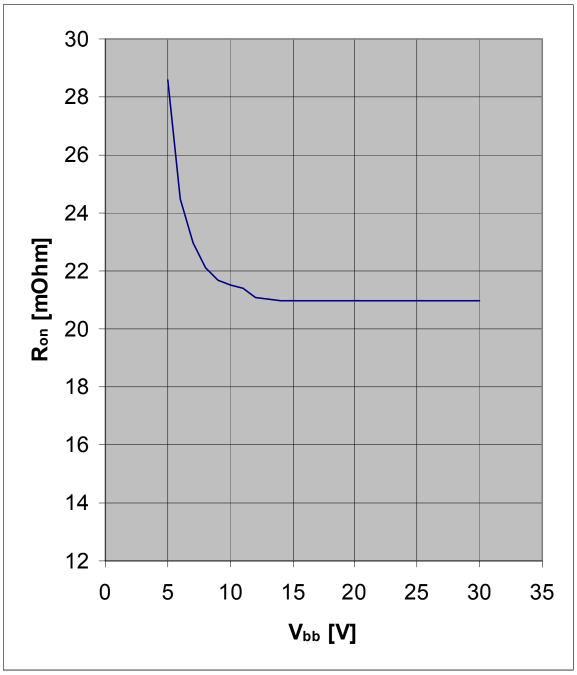
Typ. standby current

$\boldsymbol{I_{bb(off)}}$ **= f($\boldsymbol{T_j}$)** ; V_{bb} = 16 V ; $V_{IN1,2}$ = low



Typ. on-state resistance

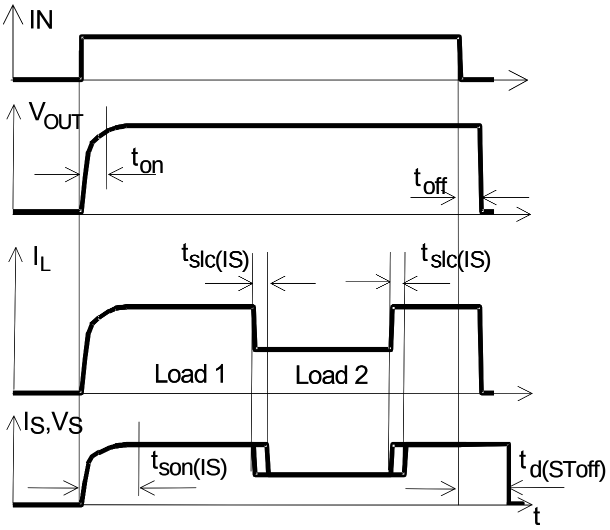
$\boldsymbol{R_{ON}}$ **= f($\boldsymbol{V_{bb}}$,$\boldsymbol{T_j}$)**; I_L = 5 A ; V_{in} = high



Timing diagrams

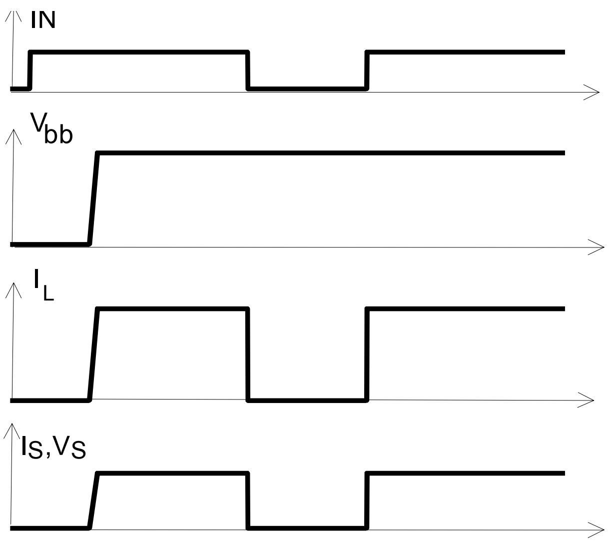
All channels are symmetric and consequently the diagrams are valid for channel 1 and channel 2.

Figure 1a: Switching a resistive load, change of load current in on-condition



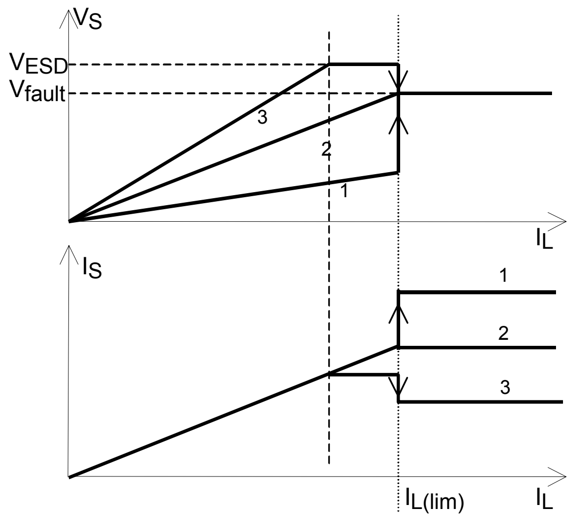
The sense signal is not valid during settling time after turn on or change of load current. tslc(IS) = 300 µs max.

Figure 1b: V_{bb} turn on



proper turn on under all conditions

Figure 1c: Behaviour of sense output: Sense current (I_S) and sense voltage (V_S) as function of load current dependent on the sense resistor.
Shown is V_S and I_S for three different sense resistors. Curve 1 refers to a low resistor, curve 2 to a medium-sized resistor and curve 3 to a big resistor. Note, that the sense resistor may not falls short of a minimum value of 500 Ω.



$I_S = I_L / k_{ILIS}$

$V_{IS} = I_S * R_{IS}$; $R_{IS} = 1\text{k}\Omega$ nominal

$R_{IS} > 500\Omega$

Figure 2a: Switching a lamp

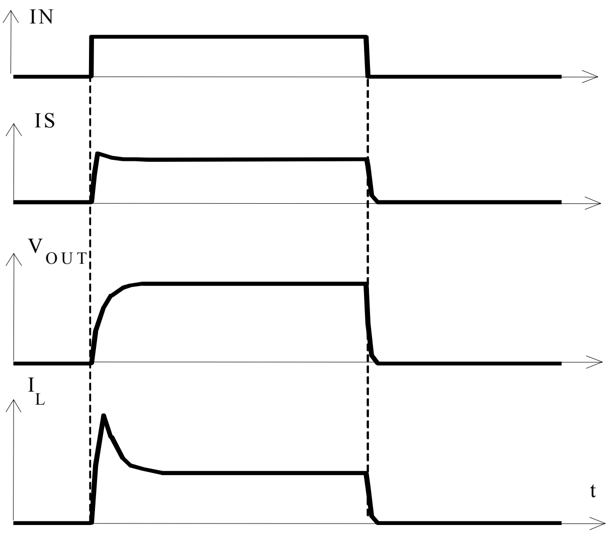


Figure 2b: Switching a lamp with current limit: The behaviour of I_S and V_S is shown for a resistor, which refers to curve 1 in figure 1c

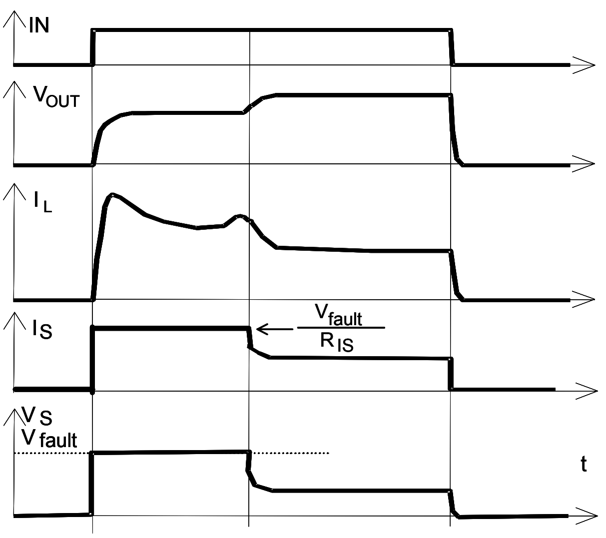
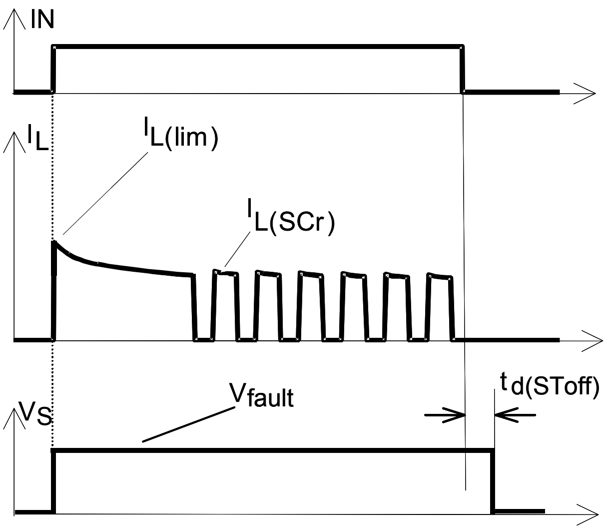


Figure 3a: Short circuit:
Shut down by overtemperature,
reset by cooling



Heating up may require several milliseconds, depending on external conditions.
$I_{L(lim)}$ = 50A typ. increases with decreasing temperature.

Figure 3b: Turn on into short circuit,
shut down by overtemperature, restart by cooling
(channel 1 and 2 switched parallel)

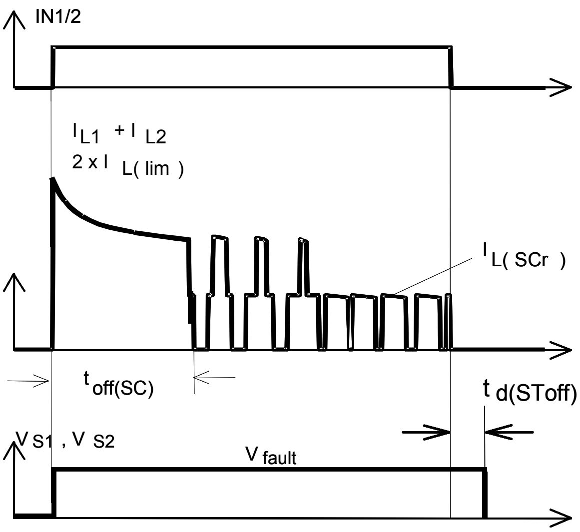


Figure 4a: Overtemperature
Reset if $T_j < T_{jt}$
The behaviour of I_S and V_S is shown for a resistor, which refers to curve 1 in figure 1c.

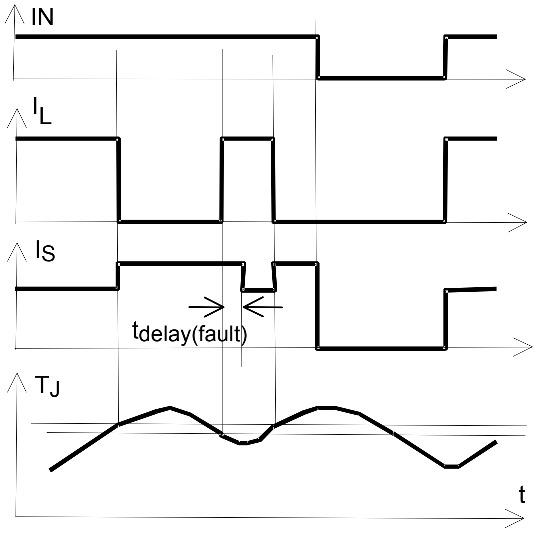
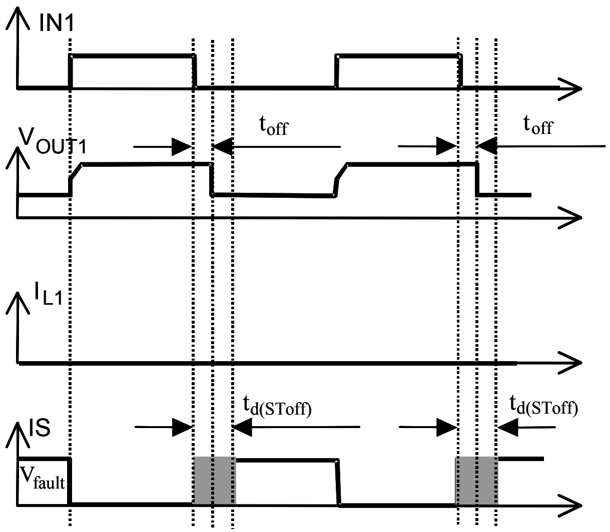
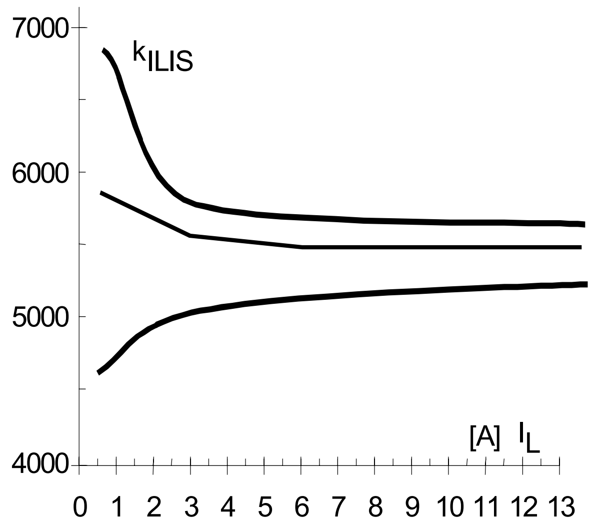


Figure 5a: Open-load: detection in OFF-state, turn on/off to open load.
Open load of channel 1; other channels normal opertaion.



t_{off} = 220µs max.; $t_{d(SToff)}$ = 1,2ms max.
with pull up resistor at output

Figure 6a: Current sense ratio[1]



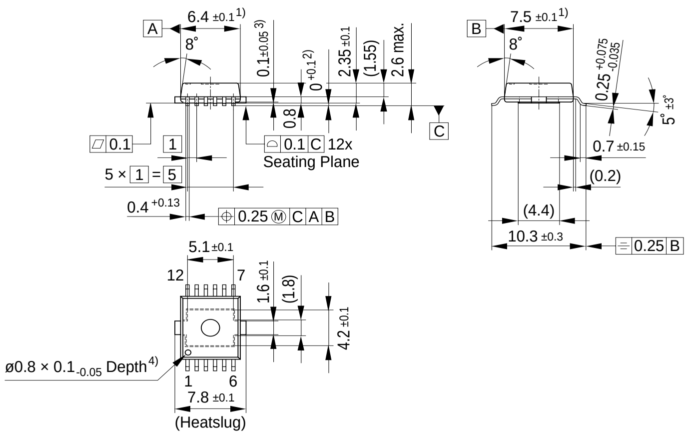
[1] This range for the current sense ratio refers to all devices. The accuracy of the k_{ILIS} can be raised by calibrating the value of k_{ILIS} for every single device.

Package and ordering code

all dimensions in mm

P-DSO-12-2

Sales Code	BTS 5240L
Ordering Code	Q67060-S6135



1) Does not include plastic or metal protrusion of 0.15 max. per side
2) Stand OFF
3) Stand OUT
4) Pin 1 Index Marking; Polish finish
All package corners max. R 0.25

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