

# XA-SK-ETH100 Slice Card Hardware Guide

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REV A

Publication Date: 2014/1/7  
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# 1 Slice Card Overview

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## IN THIS CHAPTER

- ▶ Pack Contents
  - ▶ Features
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## 1.1 Pack Contents

- ▶ One XA-SK-ETH100 Slice Card

## 1.2 Features

- ▶ One SMSC LAN8710A-EZK 10/100 Ethernet Phy providing 10/100 Ethernet in conjunction with xSOFTip ethernet Mac components
- ▶ SMI management interface
- ▶ Magnetics, data LED as standard
- ▶ Can be used in any Slicekit slot
- ▶ Use multiple ETH100 slices to create systems with multiple ethernet ports

## 2 XA-SK-ETH100 Functional Pins

This table shows the port mapping for each of the Slice Card Signal IO, and the Slicekit Slot connector pin it is located on.

Function	STAR	TRIANGLE	SQUARE	CIRCLE	PIN	Description
RX_DV	1C	1K	1C	1K	B10	Phy RX Data Valid
RX_CLK	1B	1J	1B	1J	A8	Phy RX CLK (input to XMOS)
TX_CLK	1G	1I	1G	1I	B15	Phy TX CLK (output from XMOS)
TX_EN	1F	1L	1F	1L	A15	TX Data Enable
RXD0	4A0	4E0	4A0	4E0	B6	Phy RX Data
RXD1	4A1	4E1	4A1	4E1	B7	Phy RX Data
RXD2	4A2	4E2	4A2	4E2	A6	Phy RX Data
RXD3	4A3	4E3	4A3	4E3	A7	Phy RX Data
TXD0	4B0	4F0	4B0	4F0	B9	Phy TX Data
TXD1	4B1	4F1	4B1	4F1	B11	Phy TX Data
TXD2	4B2	4F2	4B2	4F2	A9	Phy TX Data
TXD3	4B3	4F3	4B3	4F3	A11	Phy TX Data
MDIO	8B0	8D0	8B0	8D0	B12	Phy SMI interface data
MDC	8B1	8D1	8B1	8D1	B13	Phy SMI interface clock
INT_N	8B2	8D2	8B2	8D2	B17	Phy SMI interface interrupt
RXERR	8B3	8D3	8B3	8D3	B18	RXERR not used in software



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