



4 CHANNEL LOW CAPACITANCE TVS DIODE ARRAY

Features

- IEC 61000-4-2 (ESD): Air ±15kV, Contact ±8kV
- 4 Channels of ESD protection
- Low Channel Input Capacitance of 0.5pF Typical
- Typically Used at High Speed Ports such as USB 2.0, USB3.0, IEEE1394, Serial ATA, DVI, HDMI, PCI
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

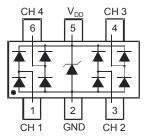
Mechanical Data

- Case: SOT26
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Copper leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208 (e3)
- Weight: 0.016 grams (approximate)

SOT26



Top View



Device Schematic

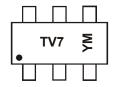
Ordering Information (Note 4)

| Product | Compliance | Marking | Reel size(inches) | Tape width(mm) | Quantity per reel |
|--------------|------------|---------|-------------------|----------------|-------------------|
| D5V0F4U6SO-7 | Standard | TV7 | 7 | 8 | 3,000/Tape & Reel |

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



TV7 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: A = 2013) M = Month (ex: 9 = September)

Date Code Key

| zato coucitoj | | | | | | | | | |
|---------------|------|------|------|------|------|------|------|--|--|
| Year | 2013 | 2014 | 2015 | 2016 | 2017 | 2018 | 2019 | | |
| Code | Α | В | С | D | E | F | G | | |

| Month | Jan | Feb | Mar | Apr | May | Jun | Jul | Aug | Sep | Oct | Nov | Dec |
|-------|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| Code | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 0 | Ν | D |



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Value | Unit | Conditions |
|------------------------------------|--------------------------|-------|------|------------------------|
| Peak Pulse Current | I _{PP} | 3.0 | Α | 8/20µs, Per Figure 3 |
| ESD Protection – Contact Discharge | V _{ESD_Contact} | ±8 | kV | Standard IEC 61000-4-2 |
| ESD Protection – Air Discharge | V _{ESD_Air} | ±15 | kV | Standard IEC 61000-4-2 |

Thermal Characteristics

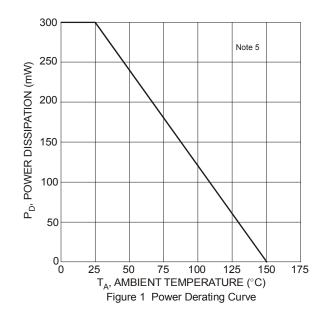
| Characteristic | Symbol | Value | Unit |
|--|-----------------------------------|-------------|------|
| Power Dissipation (Note 5) | P_{D} | 300 | mW |
| Thermal Resistance, Junction to Ambient (Note 5) | $R_{	heta JA}$ | 417 | °C/W |
| Operating and Storage Temperature Range | T _J , T _{STG} | -65 to +150 | °C |

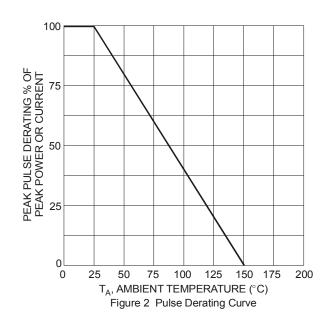
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Min | Тур | Max | Unit | Test Conditions |
|--|------------------|-----|-----|------|------|---------------------------------|
| Reverse Working Voltage | V_{RWM} | _ | _ | 5.5 | V | _ |
| Reverse Current (Note 6) | I _R | _ | _ | 200 | nA | V _R = 5.5V |
| Reverse Breakdown Voltage | V_{BR} | 6.0 | _ | _ | V | I _R = 1mA |
| Reverse Clamping Voltage, Positive Transients (Note 7) | V _{CL} | _ | 10 | 12 | V | $I_{PP} = 1A, t_p = 8/20 \mu s$ |
| Dynamic Resistance | R _{DYN} | _ | 1.0 | _ | Ω | $I_R = 1A$, $t_p = 8/20 \mu s$ |
| Capacitance (Note 8) | | _ | 0.4 | 0.65 | pF | V _R = 2.5V, f = 1MHz |
| Capacitance (Note o) | Ст | _ | 0.5 | _ | pF | V _R = 0V, f = 1MHz |

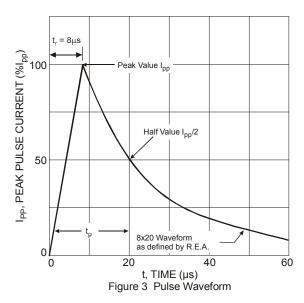
Notes:

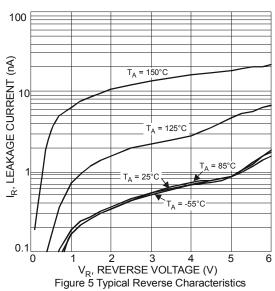
- 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes, Inc. suggested pad layout AP02001, which can be found on our website at http://www.diodes.com.
- 6. Short duration pulse test used to minimize self-heating effect.
- 7. Clamping voltage value is based on an $8x20\mu s$ peak pulse current (I_{PP}) waveform.
- 8. Measured from any CH to GND.
- 6. Wedsdred from all of the GND: 9. For information on the impact of Diodes' USB 2.0 compatible ESD protectors on signal integrity including eye diagram plots, please refer to AN77 at the following URL:http://www.diodes.com/destools/appnote_dnote.html.

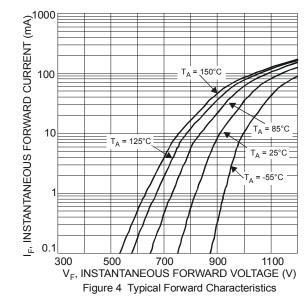


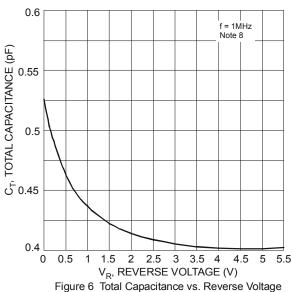






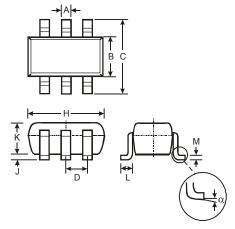






Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

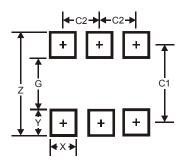


| | SOT26 | | | | | | | | | |
|-------|---------------|---------|------|--|--|--|--|--|--|--|
| Dim | Min | Min Max | | | | | | | | |
| Α | 0.35 | 0.50 | 0.38 | | | | | | | |
| В | 1.50 | 1.70 | 1.60 | | | | | | | |
| С | 2.70 | 3.00 | 2.80 | | | | | | | |
| D | _ | _ | 0.95 | | | | | | | |
| Н | H 2.90 | | 3.00 | | | | | | | |
| J | 0.013 | 0.10 | 0.05 | | | | | | | |
| K | 1.00 | 1.30 | 1.10 | | | | | | | |
| L | 0.35 | 0.55 | 0.40 | | | | | | | |
| M | 0.10 | 0.20 | 0.15 | | | | | | | |
| α | 0° | 8° | _ | | | | | | | |
| All D | imensi | ons in | mm | | | | | | | |



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



| Dimensions | Value (in mm) | | | | |
|------------|---------------|--|--|--|--|
| Z | 3.20 | | | | |
| G | 1.60 | | | | |
| X | 0.55 | | | | |
| Υ | 0.80 | | | | |
| C1 | 2.40 | | | | |
| C2 | 0.95 | | | | |

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