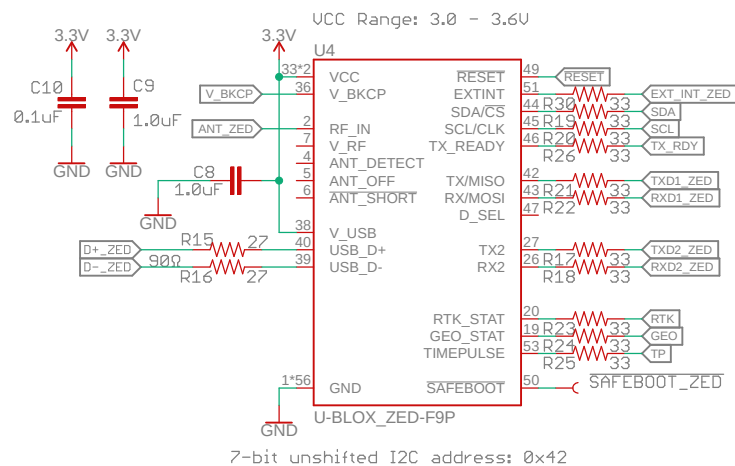
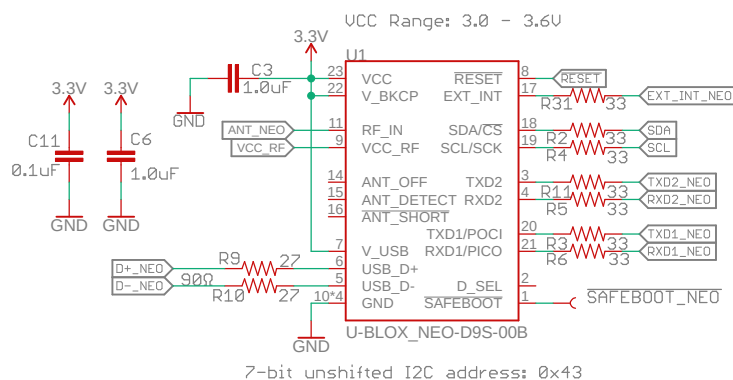


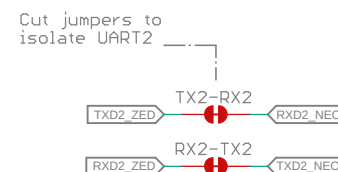
u-blox ZED-F9P



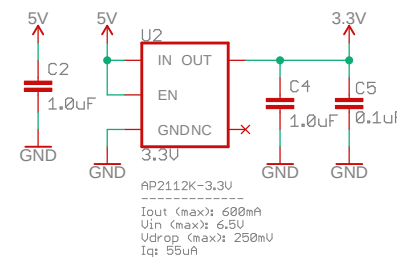
u-blox NEO-D9S



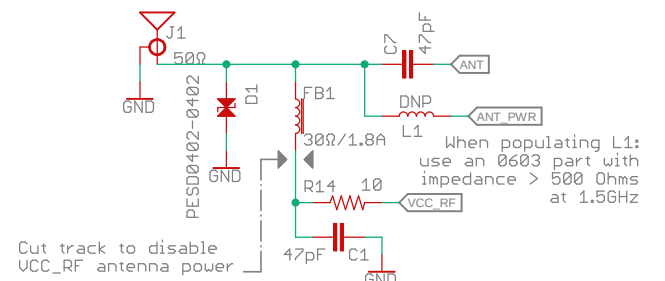
UART2 Link for PMP Corrections ZED-F9P <-> NEO-D9S



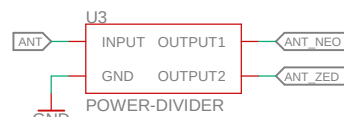
Voltage Regulation



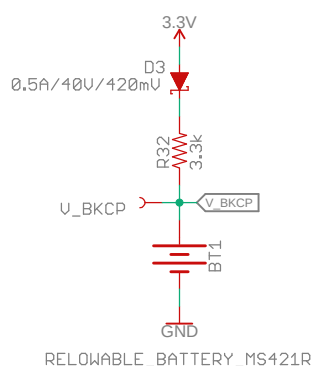
Antenna



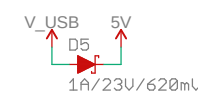
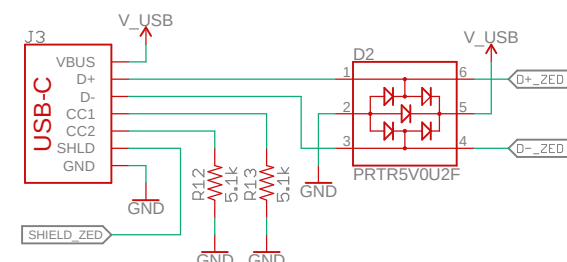
Antenna Divider



Backup Battery

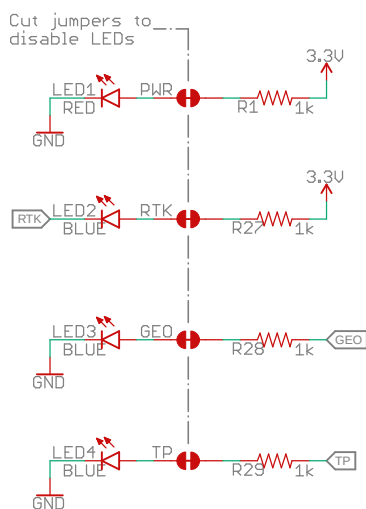


USB

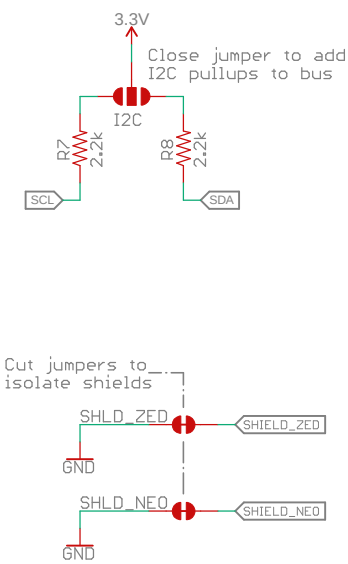


RF Track Impedance: Coplanar Waveguide with Ground Calculations
<https://chemandy.com/calculators/coplanar-waveguide-with-ground-calculator.htm>
Ground is on layer 2. Prepreg thickness: 0.2mm. Er = 4.6
12.5 mil track with 5 mil gap = 50 Ohms

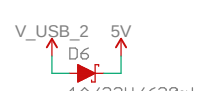
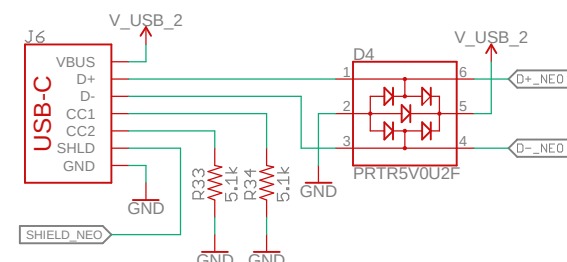
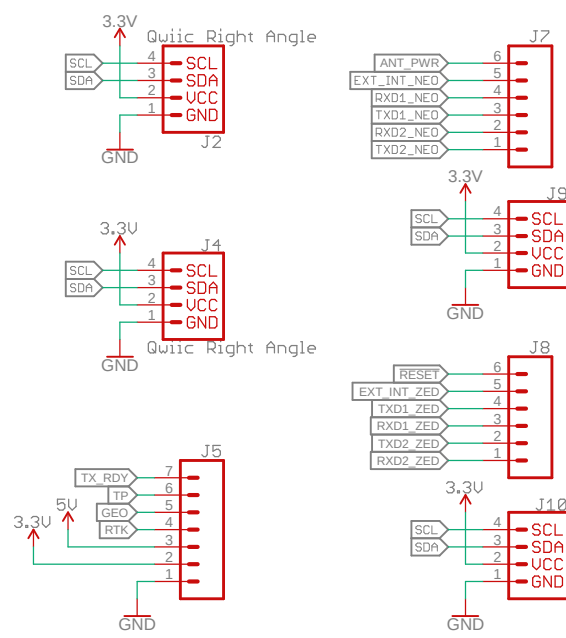
LEDs



Jumpers



Headers



USB Track Impedance: Differential Pair
<https://saturnpcb.com/saturn-pcb-toolkit/>
Prepreg thickness: 0.2mm (7.87 mil). Er = 4.6
10.5 mil track with 9.5 mil gap (20 mil center to center) = 90 Ohms



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TITLE: ZED-F9P_NEO-D9S_Combo		
Design by: Paul Clark		REV: x02
Date: 20/07/2022 15:10		Sheet: 1/1