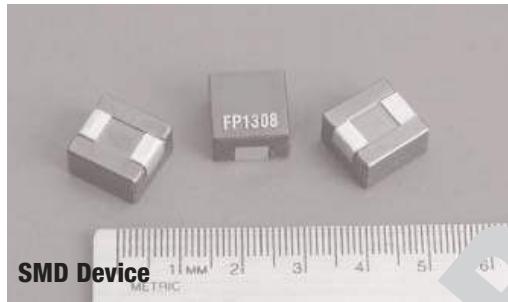


FP1308

High frequency, high current power inductors



Product features

- 13.7 x 12.9 x 8.0mm surface mount package
- High current handling capability in a small footprint
- Ferrite core material
- Inductance range from 0.110 μ H to 0.440 μ H
- Current range from 32 to 120A
- Frequency range up to 2MHz
- Halogen free, lead-free, RoHS compliant

Applications

- Voltage regulator modules (VRMs) for servers and microprocessors
- Multi-phase buck converters
- High frequency, high current switching power supplies

Environmental

- Storage temperature range (component): -40 °C to +125 °C
- Operating temperature range: -40 °C to +125 °C (ambient plus self-temperature rise)
- Solder reflow temperature: I-S-020 (latest revision) compliant



Product Specifications

Part Number ⁵	Rated Inductance (μ H)	OCL ¹ $\pm 10\%$ (μ H)	I _{rms} ² (Amps)	I _{sat} ³ (Amps)	DCR (mΩ) @ 25°C Typical	DCR (mΩ) @ 25°C Max	K-factor ⁴
FP1308-R11-R	0.110	0.110	68	120	0.20	0.24	21.330
FP1308-R21-R	0.210	0.210	68	72	0.20	0.24	21.333
FP1308-R26-R	0.260	0.260	68	60	0.20	0.24	21.335
FP1308-R32-R	0.320	0.320	68	45	0.20	0.24	21.340
FP1308-R44-R	0.440	0.440	68	32	0.20	0.24	21.366

1 Open Circuit Inductance (OCL) Test Parameters: 100kHz, 1.0V_{rms}, 0.0Adc

2 I_{rms}: DC current for an approximate temperature rise of 40°C without core loss. Derating is necessary for AC currents. PCB pad layout, trace thickness and width, air-flow and proximity of other heat generating components will affect the temperature rise. It is recommended the part temperature not exceed 125°C under worst case operating conditions verified in the end application.

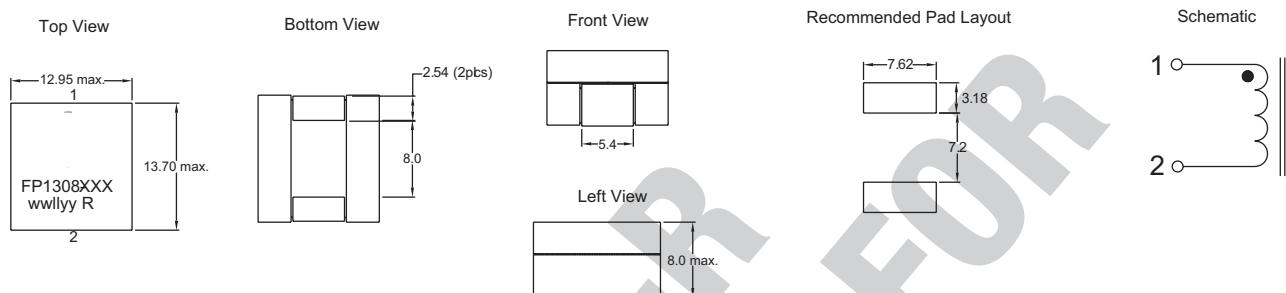
3 I_{sat}: Peak current for approximately 20% rolloff at +25°C.

4 K-factor: Used to determine B_{p-p} for core loss (see graph). B_{p-p} = K * L * ΔI. B_{p-p} (mT): (Gauss), K: (K-factor from table), L: (inductance in μ H), ΔI (peak-to-peak ripple current in amps).

5 Part Number Definition: FP1308-xxx-R

- FP1308 = Product code and size
- xxx= Inductance value in μ H, R = decimal point. If no "R" is present, then third character = # of zeros.
- ".R" suffix = RoHS compliant

Dimensions - mm



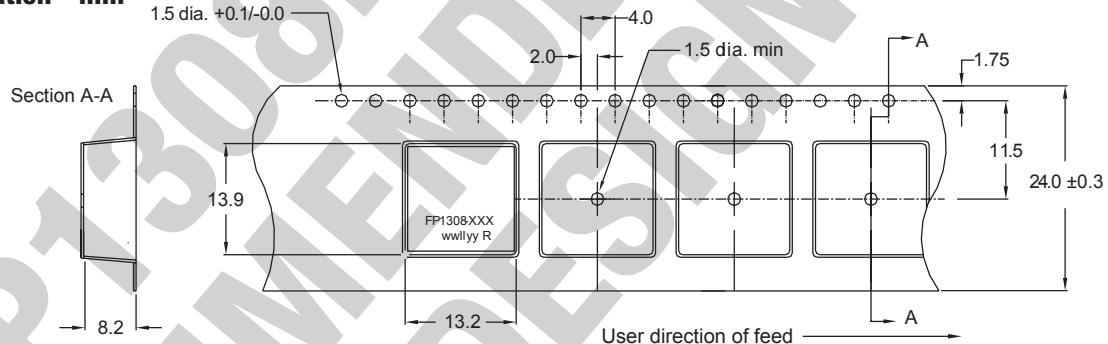
Part Marking: FP1308

xxx = Inductance value in μ H. (R = Decimal point). If no "R" is present, then last character is # Of zeros

wwllyy = Date code

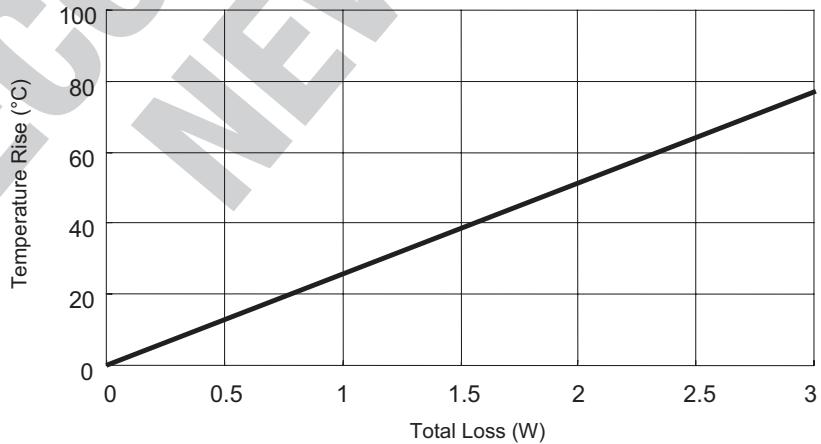
R = Revision level

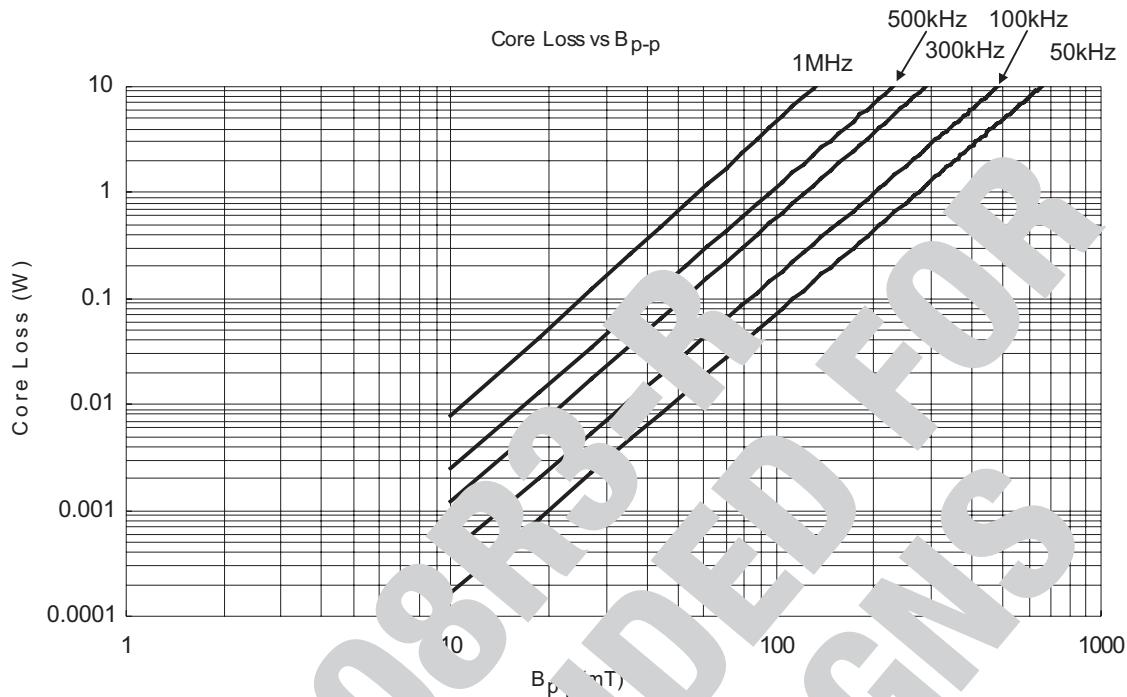
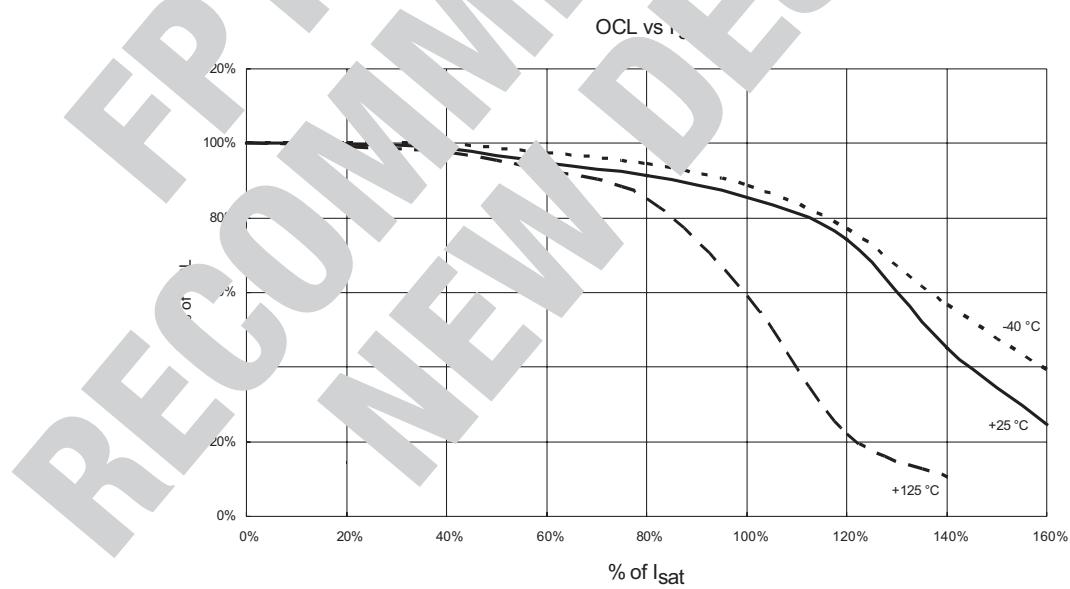
Packaging Information - mm



Supplied in tape-and-reel packaging, 400 parts per reel, 13" diameter reel.

Temperature Rise vs. Total Loss



Core Loss**Inductance Characteristic**

Solder Reflow Profile

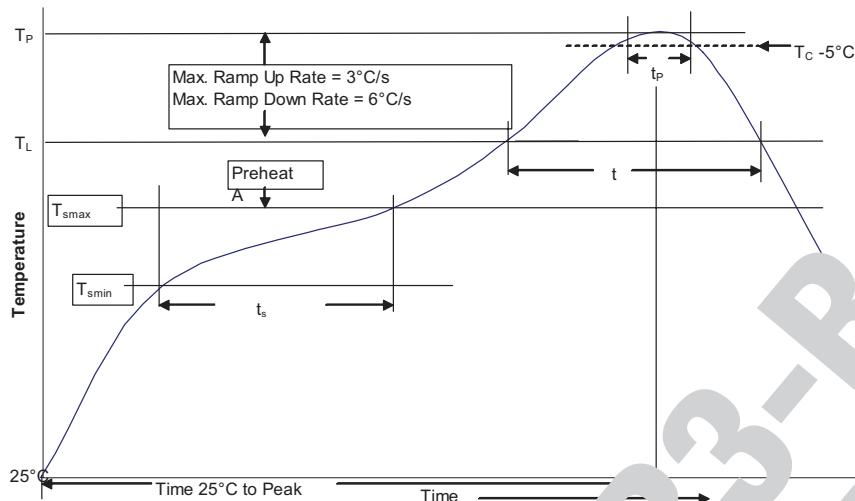


Table 1 - Standard SnPb Solder (T_c)

Package Thickness	Volume mm ³	Volume mm ³
<2.5mm	235°C	0°C
≥2.5mm	220°C	

Table 2 - Lead Free Solder (T_c)

Package Thickness	Volume mm ³	Volume mm ³	Volume mm ³
<1.6mm	350	350 - 2000	>2000
1.6 - 2.5mm	260°C	260°C	260°C
≥ 5mm	245°C	250°C	245°C

Reference JDEC J-STD-020

Profile Feature

Profile Feature	Standard SnPb Solder	Lead Free Solder
Preheat and Soak	• Temperature min. (T_{Smin})	100°C
	• Temperature max. (T_{Smax})	150°C
	• Time (T_{Smin} to T_{Smax}) (t _s)	50°C
Average ramp up rate T_{Smax} to T_p	60-120 Seconds	200°C
Liquidous temperature (T_L)	3°C	217°C
Time at liquidous (t_L)	60-150 Seconds	60-150 Seconds
Peak package body temperature (T_p)*	Triple 1	Table 2
Time (t_p)** within 5 °C of the specified classification temperature (T_c)	Seconds**	30 Seconds**
Average ramp-down rate (T_p to T_{Smax})	6°C/ Second Max.	6°C/ Second Max.
Time 25°C to Peak Temperature	8 Minutes Max.	8 Minutes Max.

* Tolerance for peak package body temperature (T_p) is defined as a supplier minimum and a user maximum.

** Tolerance for time at peak package temperature (t_p) is defined as a supplier minimum and a user maximum.

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