Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type

## HN4K03JU

High Speed Switching Applications Analog Switch Applications

- High input impedance
- Low gate threshold voltage:  $V_{th} = 0.5$  to 1.5V
- Excellent switching times
- Small package

# Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 Common)

Characteristics	Symbol	Rating	Unit
Drain-Source voltage	$V_{DS}$	20	V
Gate-Source voltage	$V_{GSS}$	10	V
DC Drain current	ID 〈	100	mA
Drain power dissipation	P <sub>D</sub> *	200	mW
Channel temperature	T <sub>ch</sub>	150	°C/
Storage temperature range	T <sub>stg</sub>	-55 to 150	°C

2.1±0.1 1.25±0.1 1.01-60 2.01-60 2.01-60 2.01-60 2.01-60 2.01-60 3. GATE 1 4. DRAIN 2 2. SOURCE 5. DRAIN 1 3. GATE 2

JEDEC —

JEITA —

TOSHIBA 2-2L1B

Weight: 6.2 mg (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

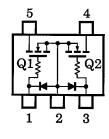
\*: Total rating

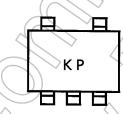
### Electrical Characteristics (Ta = 25°C) (Q1, Q2 Common)

Characteristics		Symbol	Test Condition	Min.	Тур.	Max.	Unit
Gate leakage current		I <sub>GSS</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 0	_	_	1	μΑ
Drain-Source brea	kdown voltage	V <sub>(BR) DSS</sub>	I <sub>D</sub> = 100μA, V <sub>GS</sub> = 0	20	_	_	V
Drain cut-off curre	nt	I <sub>DSS</sub>	V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0	/_	_	1	μΑ
Gate threshold vol	tage	V <sub>th</sub>	V <sub>DS</sub> = 3V, I <sub>D</sub> = 0.1mA	0.5	_	1.5	V
Forward transfer a	dmittance	Y <sub>fs</sub>	V <sub>DS</sub> = 3V, I <sub>D</sub> = 10mA	25	) 50	_	mS
Drain-Source ON r	resistance	R <sub>DS</sub> (ON)	I <sub>D</sub> = 10mA, V <sub>GS</sub> = 2.5V	) <u> </u>	8	12	Ω
Input capacitance		C <sub>iss</sub>	V <sub>DS</sub> = 3V, V <sub>GS</sub> = 0, f = 1MH <sub>Z</sub>	$\mathcal{D}$	8.5	_	pF
Reverse transfer c	apacitance	C <sub>rss</sub>	V <sub>DS</sub> = 3V, V <sub>GS</sub> = 0, f = 1MH <sub>z</sub>		3.3	_	pF
Output capacitance		Coss	V <sub>DS</sub> = 3V, V <sub>GS</sub> = 0, f = 1MH <sub>Z</sub>	_	9.3	_	pF
Switching time	Turn-on time	t <sub>on</sub>	V <sub>DD</sub> = 3V, I <sub>D</sub> = 10mA, V <sub>GS</sub> = 0 to 2.5V	_	0.16	_	μs
	Turn-off time	t <sub>off</sub>	V <sub>DD</sub> = 3V, I <sub>D</sub> = 10mA, V <sub>GS</sub> = 0 to 2.5V		0.15		

### **Equivalent Circuit (top view)**

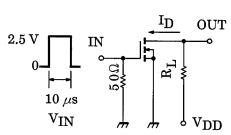




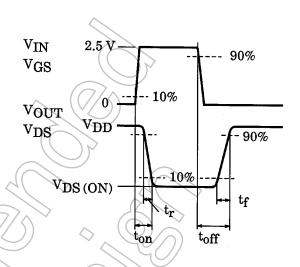


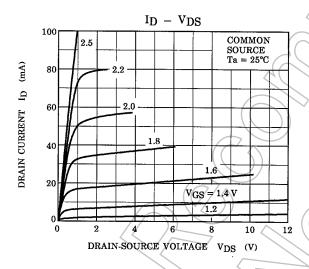
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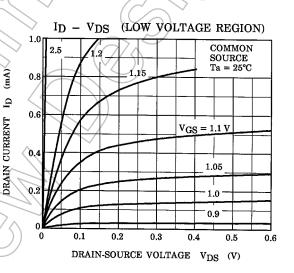
### (Q1, Q2 Common) Switching Time Test Circuit

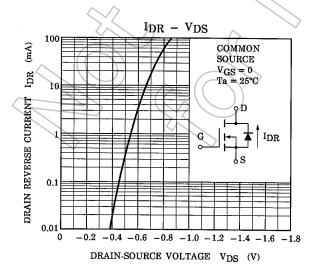


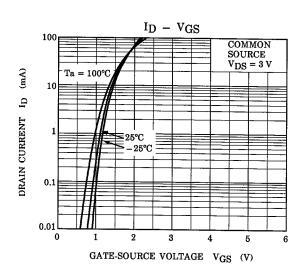
$$\begin{split} &V_{DD}=3~V\\ &D.U.\leqq1\%\\ &V_{IN}:t_r,\,t_f<5~ns\\ &(Z_{out}=50~\Omega)\\ &COMMON~SOURCE\\ &Ta=25^{\circ}C \end{split}$$



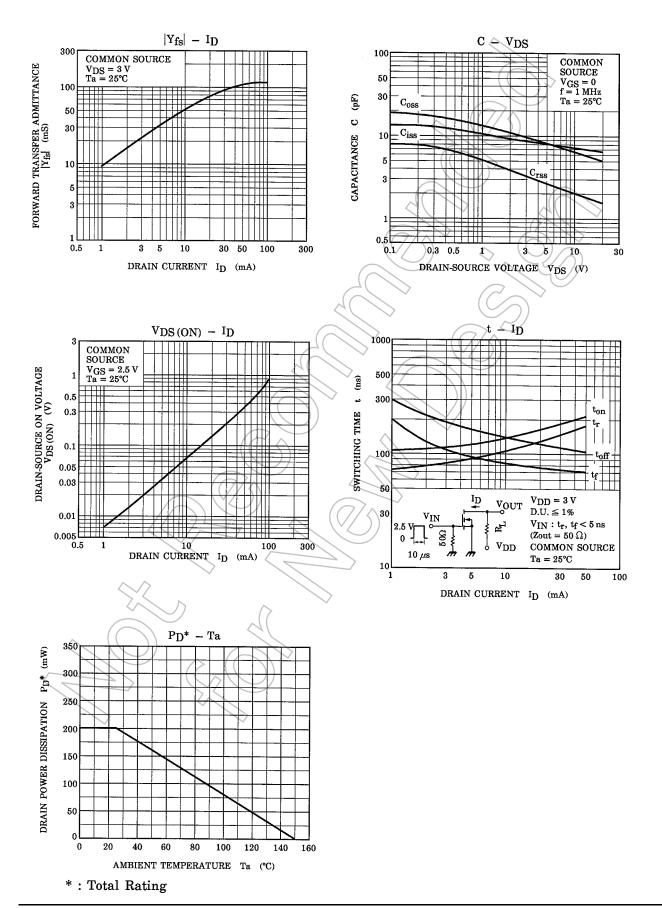








#### (Q1, Q2 Common)



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