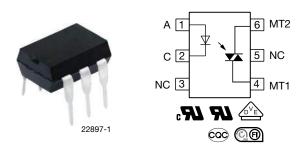


## K3010P, K3012P www.vishay.com

Vishay Semiconductors

# Optocoupler, Phototriac Output, Non-Zero Crossing, 250 VDRM



#### **FEATURES**

- 250 V blocking voltage
- · Wide range of trigger current
- 100 mA<sub>RMS</sub> on-state current
- Wide temperature range -55 °C to +100 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912







#### LINKS TO ADDITIONAL RESOURCES













#### **DESCRIPTION**

The K301xP series consists of a phototriac optically coupled to a gallium arsenide infrared-emitting diode in a 6-lead plastic dual inline package.

The non-zero crossing functionality enables full wave control. Featuring galvanic and electrical noise isolation, the output is able to directly switch AC loads or drive medium to high power TRIACs.

#### **APPLICATIONS**

- Power TRIAC driver
- · Isolated AC load switch
- Air condition
- Heaters
- White goods
- · Industrial controls
- Office equipment

#### **AGENCY APPROVALS**

- <u>UL</u>
- cUL
- DIN EN 60747-5-5 (VDE 0884-5)
- CQC: GB4943-1-2011
- CQC: GB8898-2011
- FIMKO

ORDERING INFORMATION					
K 3 PART NUMBE	0 1 X P  R TRIGGER CURRENT IFT	7.62 mm			
AGENCY CERTIFIED / PACKAGE	TRIGGER CURRENT, I <sub>FT</sub>				
VDE, cUL, CQC, FIMKO	5 mA 15 mA				
DIP-6	K3012P K3010P				

### Note

Additional options may be possible, please contact sales office



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<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
INPUT					
Reverse voltage		V <sub>R</sub>	5	V	
Forward current		I <sub>F</sub>	80	mA	
Forward surge current	t <sub>p</sub> ≤ 10 μs	I <sub>FSM</sub>	3	Α	
Power dissipation		P <sub>diss</sub>	100	mW	
Junction temperature		Tj	125	°C	
OUTPUT					
Off state output terminal voltage		$V_{DRM}$	250	V	
On state RMS current		I <sub>TRM</sub>	100	mA	
Peak surge current, non-repetitive	$t_p \le 10 \text{ ms}$	I <sub>TMS</sub>	1.5	Α	
Power dissipation		P <sub>diss</sub>	300	mW	
Junction temperature		Tj	125	°C	
COUPLER					
Total power dissipation		P <sub>tot</sub>	350	mW	
Storage temperature range		T <sub>stg</sub>	-55 to +150	°C	
Ambient temperature range		T <sub>amb</sub>	-55 to +100	°C	
Soldering temperature	2 mm from case, $t \le 10 \text{ s}$	T <sub>sld</sub>	260	°C	

#### Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT							
Forward voltage	$I_F = 50 \text{ mA}$		$V_{F}$	-	1.25	1.6	V
Junction capacitance	V <sub>R</sub> = 0, f = 1 MHz		C <sub>j</sub>	-	50	-	pF
OUTPUT							
Forward peak off-state voltage (repetitive)	I <sub>RDM</sub> = 100 nA		V <sub>DRM</sub> <sup>(1)</sup>	250	-	-	V
Peak on-state voltage	I <sub>TM</sub> = 100 mA		$V_{TM}$	-	1.5	3	V
Critical rate of rise of off-state voltage	I <sub>FT</sub> = 0, I <sub>FT</sub> = 30 mA		dV/dt <sub>cr</sub>	-	10	-	V/µs
			dV/dt <sub>crq</sub>	0.1	0.2	-	V/µs
COUPLER (2)							
Collector emitter trigger current	$V_{S} = 3 \text{ V}, R_{L} = 150 \Omega$	K3010P	I <sub>FT</sub>	-	8	15	mA
		K3012P	I <sub>FT</sub>	-	2	5	mA
Holding current	$I_F = 10 \text{ mA}, V_S \ge 3 \text{ V}$		I <sub>H</sub>	-	100	-	μΑ

#### Notes

- Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
  evaluation. Typical values are for information only and are not part of the testing requirements
- (1) Test voltage must be applied within dV/dt ratings
- (2) IFT is defined as a minimum trigger current

# K3010P, K3012P

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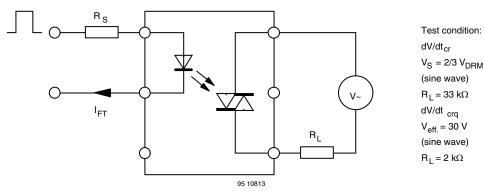
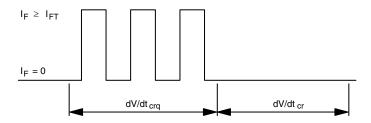


Fig. 1 - Test Circuit for dV/dt<sub>cr</sub> and dV/dt<sub>cr</sub>



dV/dt cr

Highest value of the "rate of rise of off-state voltage" which does not cause any switching from the off state to the on state

dV/dt crq

Highest value of the "rate of rise of communicating voltage" which does not switch on the device again, after the voltage has decreased to zero and the trigger current is switched from  $I_{\text{FT}}$  to zero

95 10814

Fig. 2

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Climatic classification	According to IEC 68 part 1		55 / 100 / 21	
Pollution degree	According to DIN VDE 0109		2	
Comparative tracking index	Insulation group Illa	CTI	175	
Maximum rated withstanding isolation voltage	According to UL1577, t = 1 min	V <sub>ISO</sub>	4420	V <sub>RMS</sub>
Tested withstanding isolation voltage According to UL1577, t = 1 s		$V_{ISO}$	5300	V <sub>RMS</sub>
Maximum transient isolation voltage	According to DIN EN 60747-5-5	V <sub>IOTM</sub>	8000	V <sub>peak</sub>
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5	V <sub>IORM</sub>	890	V <sub>peak</sub>
Isolation resistance	$T_{amb} = 25  ^{\circ}\text{C},  V_{IO} = 500  \text{V}$	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω
	$T_{amb} = 100  ^{\circ}C,  V_{IO} = 500  V$	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω
Output safety power		P <sub>SO</sub>	265	mW
Input safety current		I <sub>SI</sub>	130	mA
Input safety temperature		T <sub>S</sub>	150	°C
Creepage distance	DIP-6		≥ 7	mm
Clearance distance	DIP-6		≥ 7	mm
Insulation thickness		DTI	≥ 0.4	mm
Input to output test voltage, method A	$V_{IORM}$ x 1.6 = $V_{PR}$ , 100 % sample test with $t_M$ = 10 s, partial discharge < 5 pC	V <sub>PR</sub>	1424	V <sub>peak</sub>

According to DIN EN60747-5-5 (see figure 4). This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance with the safety ratings shall be ensured by means of suitable protective circuits



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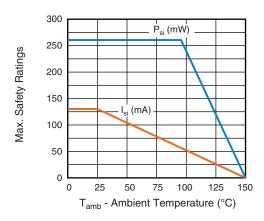


Fig. 3 - Safety Parameter Derating Diagram

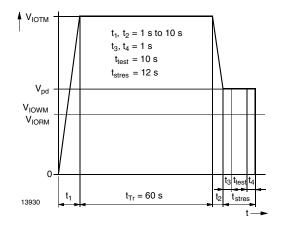


Fig. 4 - Test Pulse Diagram for Sample Test according to DIN EN60747-5-5 / DIN EN60747-; IEC 60747

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

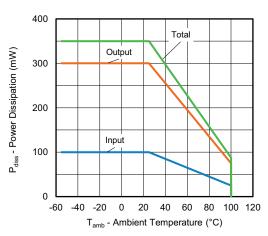


Fig. 5 - Total Power Dissipation vs. Ambient Temperature

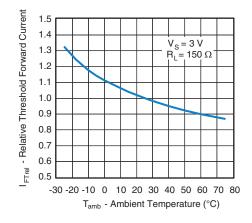


Fig. 7 - Relative Threshold Forward Current vs.
Ambient Temperature

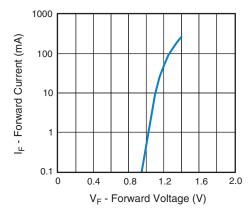


Fig. 6 - Forward Current vs. Forward Voltage

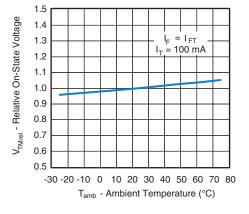


Fig. 8 - Relative On-State vs. Ambient Temperature



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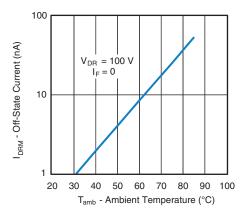


Fig. 9 - Off-State Current vs. Ambient Temperature

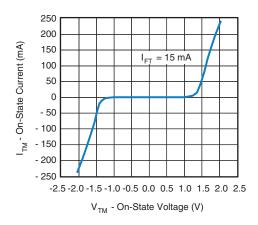
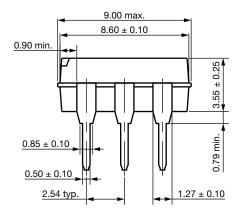
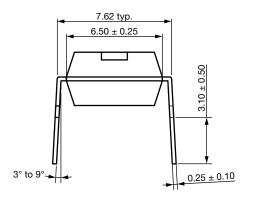


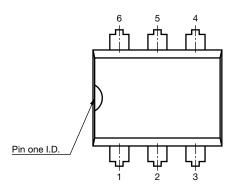
Fig. 10 - On-State Current vs. On-State Voltage

#### **PACKAGE DIMENSIONS** (in millimeters)

#### DIP-6







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#### **PACKAGE MARKING**



Fig. 11 - Example of K3010P

#### Notes

- "YWW" is the date code marking (Y = year code, WW = week code)
- The VDE logo is only marked on option1 parts

### **PACKING INFORMATION** (in millimeters)

#### **Tube**

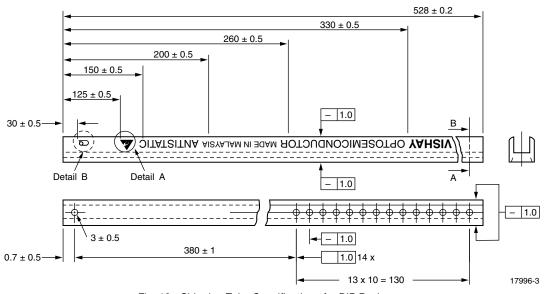


Fig. 12 - Shipping Tube Specifications for DIP Packages

DEVICES PER TUBES					
TYPE	UNITS/TUBE	TUBES/BOX	UNITS/BOX		
DIP-6	50	40	2000		

#### DIP-6

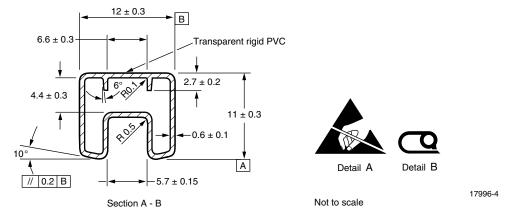


Fig. 13 - Tube Shipping Medium





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#### **SOLDER PROFILES**

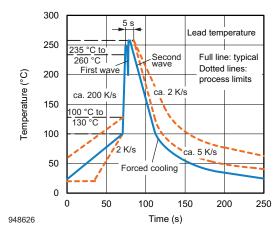


Fig. 14 - Wave Soldering Double Wave Profile According to J-STD-020 for DIP Devices

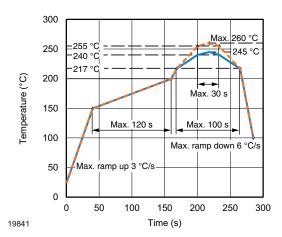


Fig. 15 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020 for SMD Devices

#### HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2 Floor life: unlimited

Conditions:  $T_{amb}$  < 30 °C, RH < 85 %

Moisture sensitivity level 1, according to J-STD-020



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