



PJC7002H

60V N-Channel Enhancement Mode MOSFET

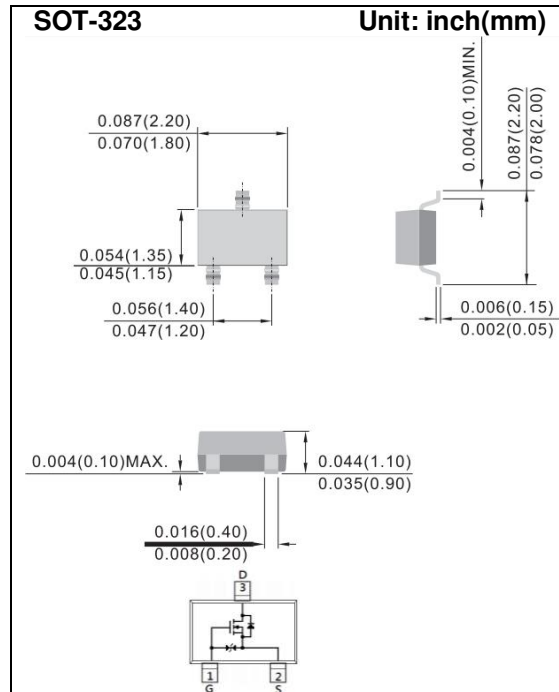
Voltage 60 V **Current** 250mA

Features

- RDS(ON) , VGS@10V, ID@300mA<5Ω
- RDS(ON) , VGS@5V, ID@50mA<7.5Ω
- Advanced Trench Process Technology
- ESD Protected
- Specially Designed for Relay driver, Speed line drive, etc.
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std. (Halogen Free)

Mechanical Data

- Case: SOT-323 Package
- Terminals: Solderable per MIL-STD-750, Method 2026



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	60	V
Gate-Source Voltage		V _{GS}	±20	V
Continuous Drain Current		I _D	250	mA
Pulsed Drain Current		I _{DM}	1000	mA
Power Dissipation	T _A =25°C	P _D	350	mW
	Derate above 25°C		2.8	mW/°C
Operating Junction and Storage Temperature Range		T _J , T _{STG}	-55~150	°C
Typical Thermal Resistance		R _{θJA}	357	°C/W
- Junction to Ambient (Note 3)				



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Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	60	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2.0	2.49	3.0	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=300mA$	-	2.0	5	Ω
		$V_{GS}=5V, I_D=50mA$	-	3.6	7.5	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=60V, V_{GS}=0V$	-	-	1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 0.5	μA
Dynamic (Note 4)						
Total Gate Charge	Q_g	$V_{DS}=10V, I_D=300mA,$ $V_{GS}=4.5V$ (Note 1,2)	-	1.3	-	nC
Gate-Source Charge	Q_{gs}		-	0.6	-	
Gate-Drain Charge	Q_{gd}		-	0.2	-	
Input Capacitance	C_{iss}	$V_{DS}=25V, V_{GS}=0V,$ $f=1.0MHz$	-	22	-	μF
Output Capacitance	C_{oss}		-	12	-	
Reverse Transfer Capacitance	C_{rss}		-	1.7	-	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=10V, I_D=300mA,$ $V_{GS}=10V,$ $R_G=10\Omega$ (Note 1,2)	-	2.9	-	ns
Turn-On Rise Time	t_r		-	1.8	-	
Turn-Off Delay Time	$t_{d(off)}$		-	5.6	-	
Turn-Off Fall Time	t_f		-	1.9	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_S	---	-	-	300	mA
Diode Forward Voltage	V_{SD}	$I_S=300mA, V_{GS}=0V$	-	0.92	1.5	V

NOTES:

1. Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$
2. Essentially independent of operating temperature typical characteristics.
3. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. mounted on a 1 inch square pad of copper
4. Guaranteed by design, not subject to production testing



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TYPICAL CHARACTERISTIC CURVES

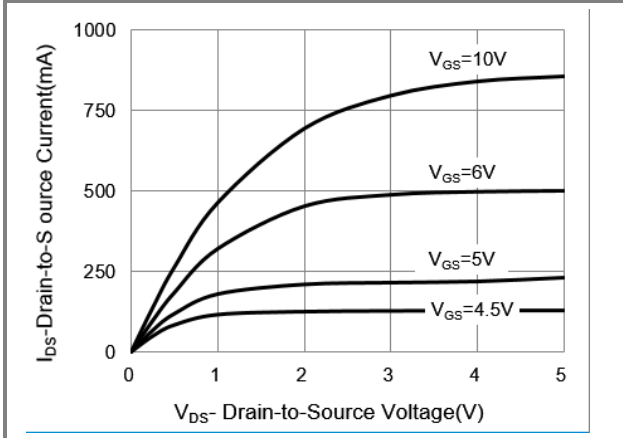


Fig.1 On-Region Characteristics

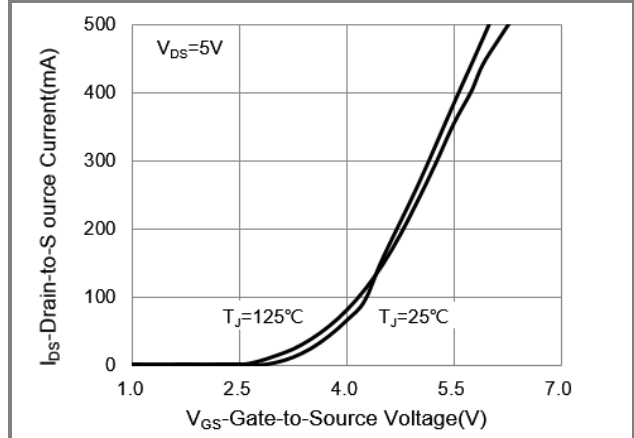


Fig.2 Transfer Characteristics

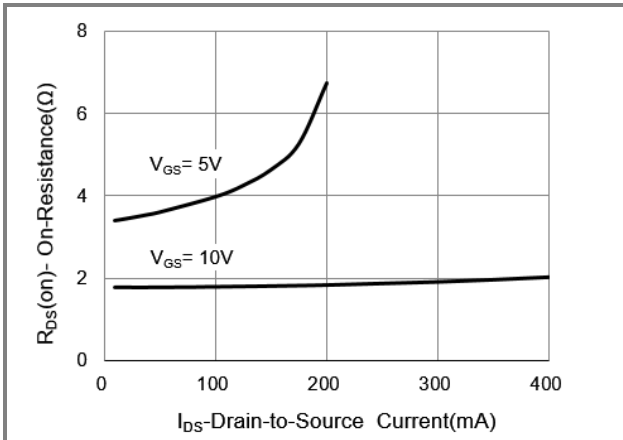


Fig.3 On-Resistance vs. Drain Current

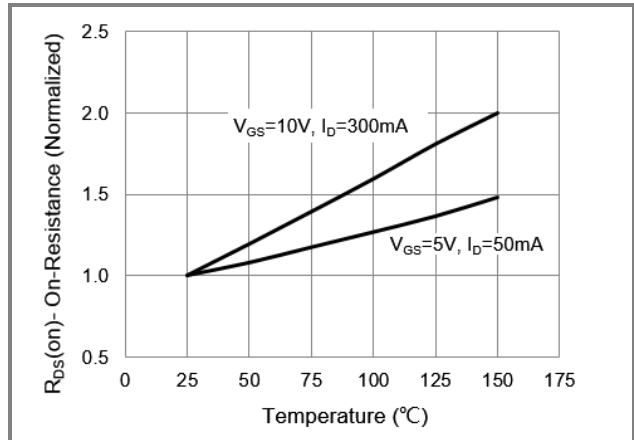


Fig.4 On-Resistance vs. Junction temperature

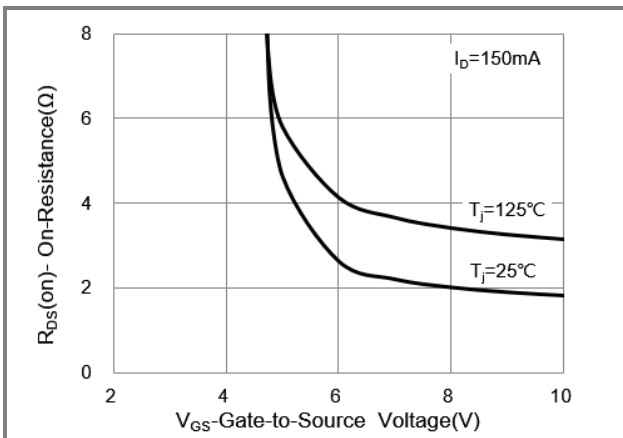


Fig.5 On-Resistance Variation with VGS.

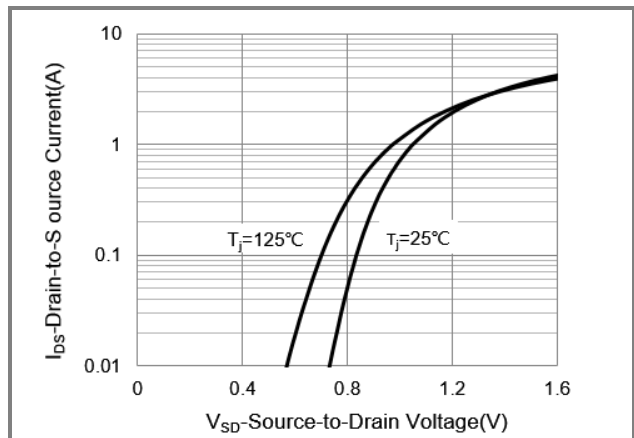


Fig.6 Body Diode Characteristics



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TYPICAL CHARACTERISTIC CURVES

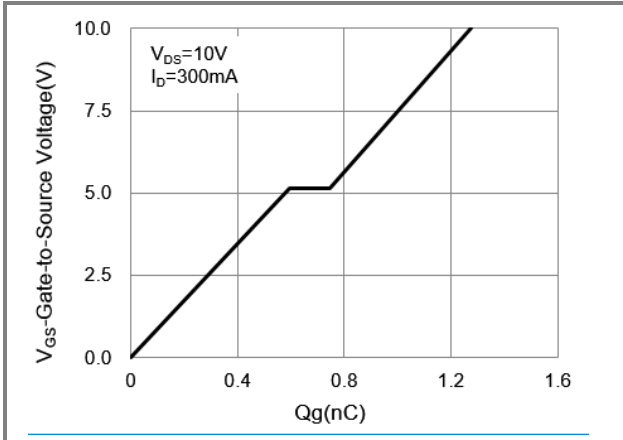


Fig.7 Gate-Charge Characteristics

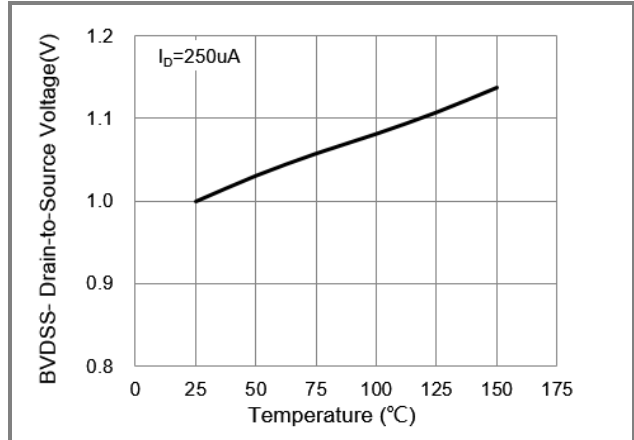


Fig.8 Breakdown Voltage Variation vs. Temperature

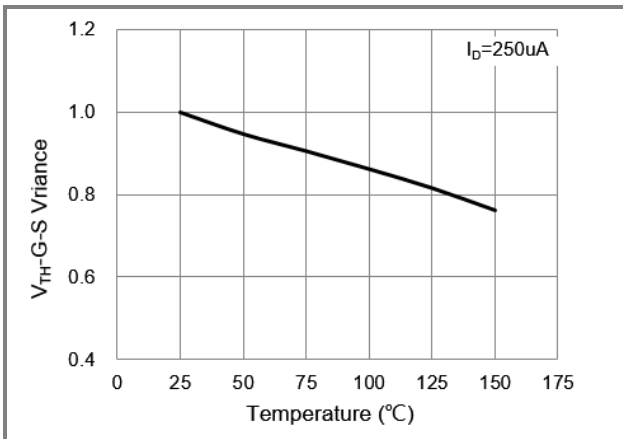


Fig.9 Threshold Voltage Variation with Temperature.

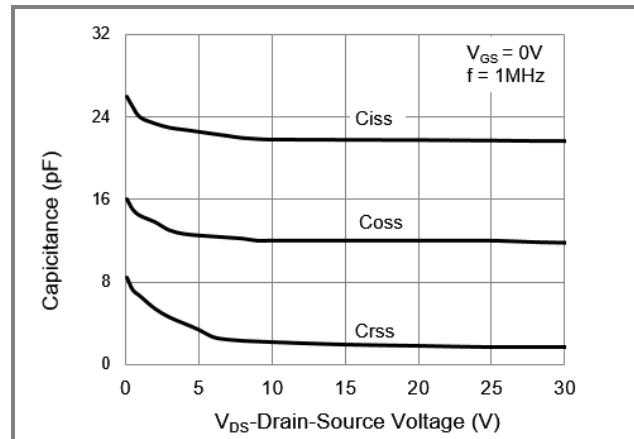


Fig.10 Capacitance vs. Drain-Source Voltage.

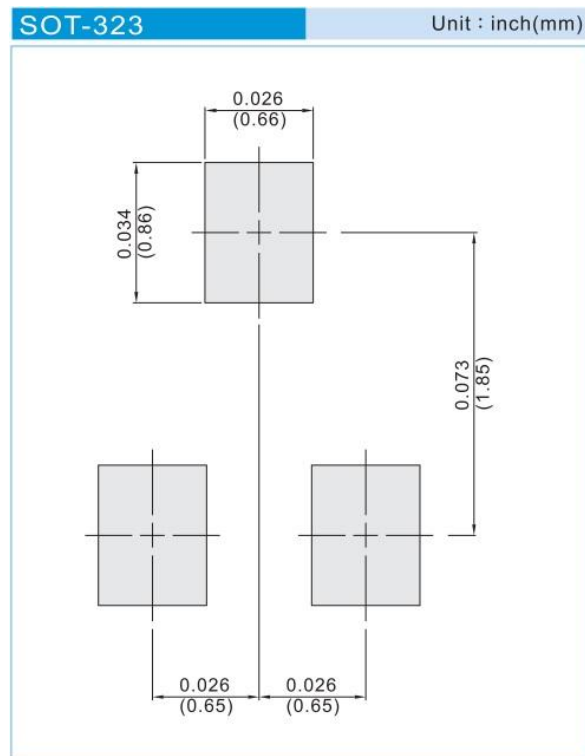


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PART NO PACKING CODE VERSION

PART NO PACKING CODE	Package Type	Packing Type	Marking	Version
PJC7002H_R1_00001	SOT-323	3K pcs / 7" reel	C2H	Halogen free
PJC7002H_R2_00001	SOT-323	12K pcs / 13" reel	C2H	Halogen free

MOUNTING PAD LAYOUT





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