

R8006KND3

Nch 800V 6A Power MOSFET

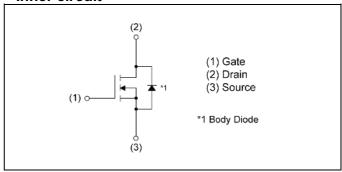
V _{DSS}	800V
R _{DS(on)} (Max.)	0.90Ω
I _D	±6A
P_D	83W

Package TO-252

Features

- 1) Low on-resistance
- 2) Fast switching
- 3) Parallel use is easy
- 4) Pb-free plating; RoHS compliant

•Inner circuit



Marking specification

Marking R8006KND3

Application

Switching applications

ullet Absolute maximum ratings (T_a = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit	
Drain - Source voltage		V_{DSS}	800	V
Continuous drain current		I _D *1	±6	Α
Pulsed drain current		I _{DP} *2	±18	Α
Coto Course valters	Static	V	±20	V
Gate - Source voltage	AC (f>1Hz)	V_{GSS}	±30	V
Avalanche current, single pulse		I _{AS}	1.2	А
Avalanche energy, single pulse		E _{AS} *3	76	mJ
Power dissipation (T _c = 25°C)	P _D	83	W	
Junction temperature	T _j	150	°C	
Operating junction and storage tempe	T _{stg}	-55 to +150	°C	

Thermal characteristics

Downwortow	Cymah al	Values			Linit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{th(j-c)} *4	-	-	1.5	°C/W
Thermal resistance, junction - ambient	R _{th(j-a)}	-	-	147	°C/W
Soldering temperature, wavesoldering for 10s	T _{sold}	-	-	265	°C

● Static characteristics (T_a = 25°C)

Parameter	Cymah al	Canditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V$, $I_D = 1mA$	800	-	-	V
Zero gate voltage drain current	I _{DSS}	V _{DS} = 800V, V _{GS} = 0V	1	-	100	μA
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±100	nA
Gate threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 4mA$	2.5	3.5	4.5	V
Static drain - source on - state resistance	R _{DS(on)} *5	V _{GS} = 10V, I _D = 3A	-	0.75	0.90	Ω

● Dynamic characteristics (T_a = 25°C)

Darramatar	Cymah al	Conditions	Values			Linit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Gate resistance	R_{G}	f = 1MHz, open drain	-	3	-	Ω
Input capacitance	C _{iss}	V _{GS} = 0V, VDS = 100V	-	650	-	
Output capacitance	C _{oss}	f = 1MHz	-	45	-	
Effective output capacitance energy related $C_{o(er)}^{*6}$ $V_{GS} = 0$		V _{GS} = 0V	-	9	-	pF
Effective output capacitance time related	C _{o(tr)} *7	V _{DS} = 0V to 400V	-	41	-	
Turn - on delay time	t _{d(on)} *5	V _{DD} ≈ 400V, V _{GS} = 10V	-	15	-	
Rise time	t _r *5	I _D = 3A	-	30	-	20
Turn - off delay time	$t_{d(off)}^{*5} \qquad R_L \cong 133\Omega$		-	40	-	ns
Fall time	t _f *5	$R_G = 10\Omega$	-	45	-	

● Gate charge characteristics (T_a = 25°C)

Darameter	Cumb al	Conditions	Values			Unit
Parameter	Symbol Conditions -		Min.	Тур.	Max.	Offic
Total gate charge	Q_g^{*5}	V _{DD} ≃ 400V	-	22	1	
Gate - Source charge	Q _{gs} *5	I _D = 6A	-	3	1	nC
Gate - Drain charge	Q _{gd} *5	V _{GS} = 10V	-	9	1	
Gate plateau voltage	V _(plateau)	V _{DD} ≈ 400V, I _D = 6A	-	4.0	1	V

●Body diode characteristics (Source-Drain) (T_a = 25°C)

Parameter	Cumbal	Conditions	Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Orill	
Source current	I _S *1	T - 25°C	-	-	6	Α	
Pulsed source current	l _{SP} *2	T _C = 25°C	-	-	18	Α	
Source-Drain voltage	V _{SD} *5	$V_{GS} = 0V$, $I_S = 6A$	-	-	1.5	V	
Reverse recovery time	t _{rr} *5		-	400	-	ns	
Reverse recovery charge	Q _{rr} *5	I _S = 6A di/dt = 100A/μs	-	5.0	-	μC	
Peak reverse recovery current	I _m *5		-	25	-	Α	

^{*1} Limited only by maximum channel temperature allowed.

^{*2} Pw ≤ 10µs, Duty cycle ≤ 1%

^{*3} L \doteqdot 100mH, V_{DD}=50V, R_G=25 Ω , STARTING T_i=25 $^{\circ}$ C

^{*4} T_C=25°C

^{*5} Pulsed

^{*6} Co(er) is a fixed capacitance that gives the same stored energy as Coss while V_{DS} is rising from 0 to 50% V_{DSS}.

 $^{^{*}}$ 6 Co(er) is a fixed capacitance that gives the same charging time as Coss while V_{DS} is rising from 0 to 50% V_{DSS} .

Fig.1 Power Dissipation Derating Curve

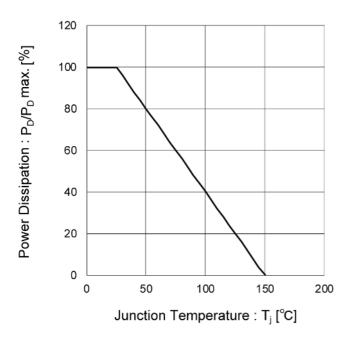


Fig.2 Drain Current Derating Curve

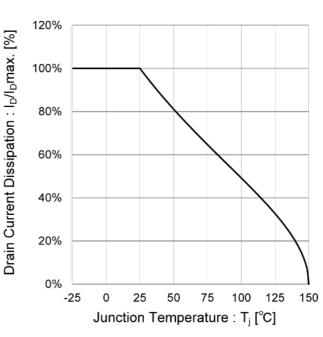


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

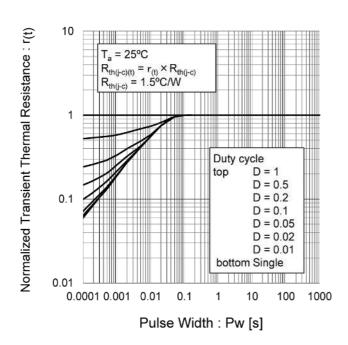
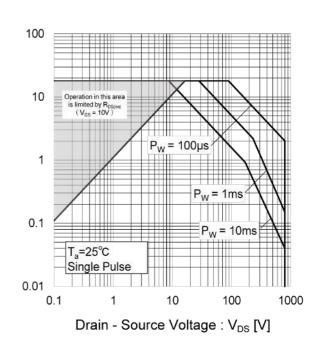


Fig.4 Maximum Safe Operating Area



Drain Current : I_D [A]

Fig.5 Avalanche Energy Derating Curve

120 Avalanche Energy: EAS / EAS max [%] 100 80 60 40 20 0 0 25 50 75 100 125 150 175 Junction Temperature : T_j [°C]

Fig.6 Normalized Breakdown Voltage vs. Junction Temperature

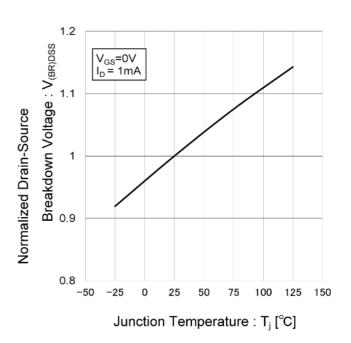
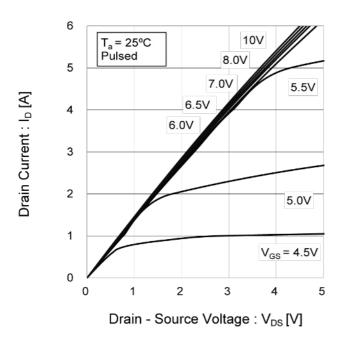
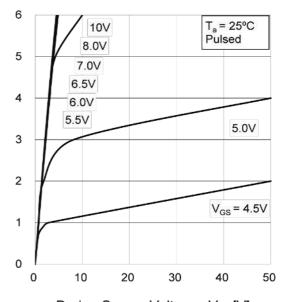


Fig.7 Output Characteristics(I))



Drain Current : I_D [A]

Fig.8 Output Characteristics(II)



Drain - Source Voltage : $V_{DS}[V]$

Fig.9 Gate Threshold Voltage vs. Drain Current

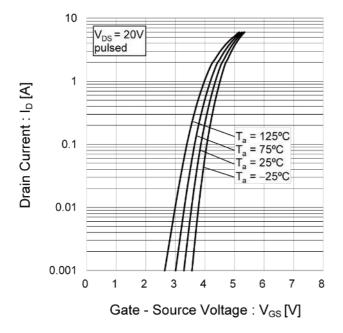


Fig.10 Normalized Gate Threshold Voltage vs. Junction Temperature

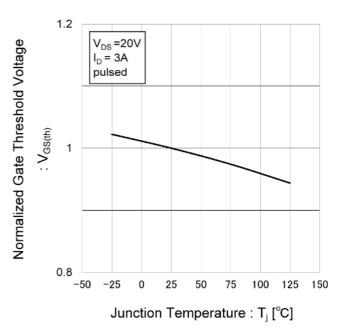


Fig.11 Static Drain - Source On - State Resistance vs. Drain Current

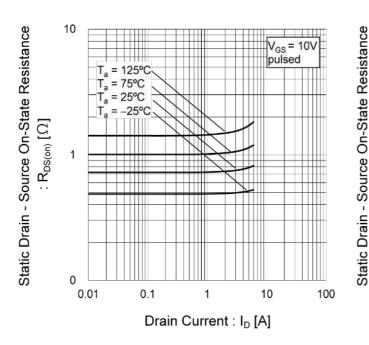


Fig.12 Static Drain - Source On - State Resistance vs. Gate - Source Voltage

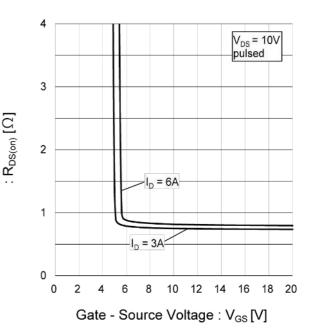


Fig.13 Normalized Static Drain - Source On - State Resistance vs. Junction Temperature

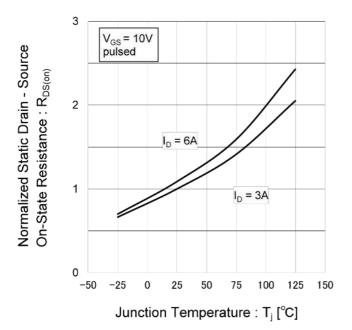
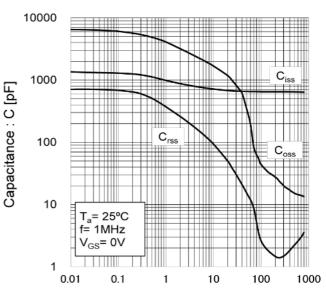


Fig.14 Capacitances



Drain - Source Voltage: VDS [V]

Fig.15 Switching Times

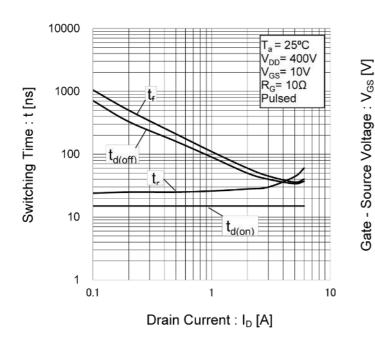


Fig.16 Gate Charge

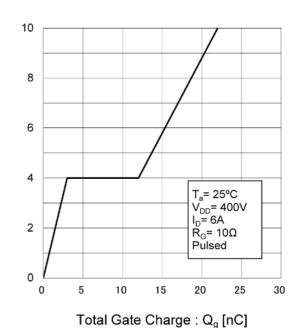


Fig.17 Source Current vs. Source - Drain Voltage

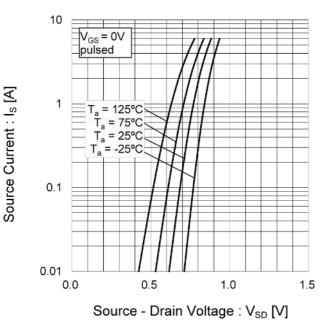
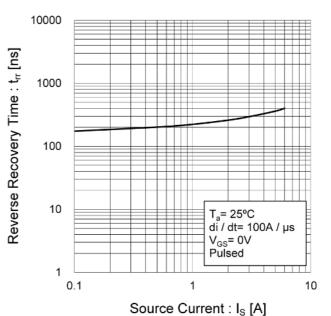


Fig.18 Reverse Recovery Time vs. Source Current





Measurement circuits

Fig.1-1 Switching time measurement circuit

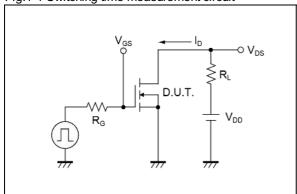


Fig.2-1 Gate charge measurement circuit

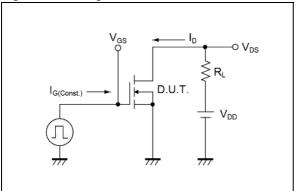


Fig.3-1 Avalanche measurement circuit

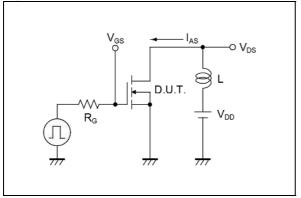


Fig.4-1 trr measurement Circuit

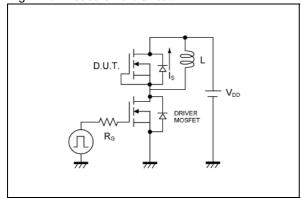


Fig.1-2 Switching waveforms

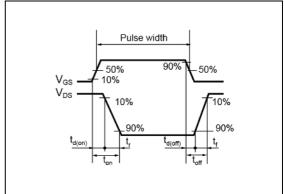


Fig.2-2 Gate charge waveform

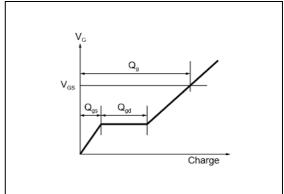


Fig.3-2 Avalanche waveform

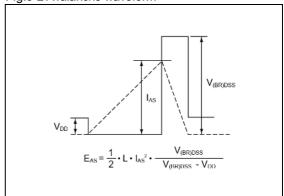
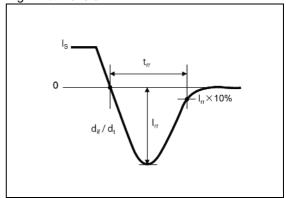
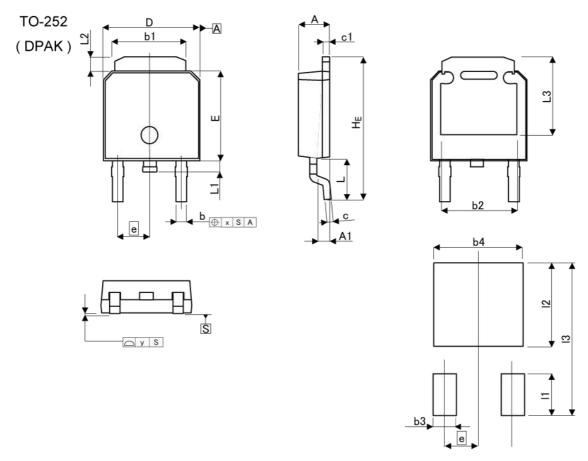


Fig.4-2 trr waveform



Dimensions



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM -	MILIME	ETERS	INCHES		
DIM [MIN	MAX	MIN	MAX	
Α	2.20	2.40	0.087	0.094	
A1	0.70	1.10	0.028	0.043	
b	0.60	0.90	0.024	0.035	
b1	5.20	5.50	0.205	0.217	
b2	4.	80	0.1	89	
С	0.40	0.60	0.016	0.024	
c1	0.40	0.60	0.016	0.024	
D	6.40	6.80	0.252	0.268	
е	2.	30	0.0	91	
E	6.00	6.40	0.236	0.252	
HE	9.40	10.40	0.370	0.409	
L	2.	90	0.1	14	
L1	0.60	1.00	0.024	0.039	
L2	0.70	1.30	0.028	0.051	
L3	5.	30	0.209		
х		0.25	(4)	0.010	
у		0.10	357	0.004	
DINA	MILIME	ETERS	INCI	HES	
DIM	MIN	MAX	MIN	MAX	
b3	#	1.15	7 4 8	0.045	
b4	-	5.55	N T .0.	0.219	
11	-	2.77	()	0.109	
12	-	5.50	357	0.217	
13		10.40	-	0.409	

Dimension in mm/inches

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CLASSIV	CLASSII	CLASSⅢ	CLASSⅢ

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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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