



30V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(on)}	Ι _D Τ _A = +25°C
-30V	1.5 Ω @ V _{GS} = -4.5V	-0.3 A
-30 V	$2.5 \Omega @ V_{GS} = -2.5V$	-0.2 A

Description

This MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$ and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Load Switch
- Portable Applications
- Power Management Functions

Features

- 0.4mm Ultra Low Profile Package for Thin Application
- 0.48mm² Package Footprint, 16 Times Smaller Than SOT23
- Low $V_{GS(th)}$, can be Driven Directly from a Battery
- Low R_{DS(ON)}
- ESD Protected
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

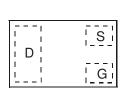
- Case: X2-DFN0806-3
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @4
- Weight: 0.00043 grams (Approximate)

ESD PROTECTED

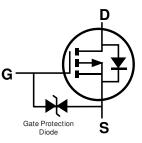


X2-DFN0806-3

Bottom View



Top View Package Pin Configuration



Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
DMP32D5LFA-7B	X2-DFN0806-3	10,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



R2= Product Type Marking Code

Top View Bar Denotes Gate and Source Side



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	30	V
Gate-Source Voltage			V _{GSS}	±8	V
Continuous Drain Current (Note 5) $V_{GS} = 4.5V$	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	ID	-0.3 -0.2	А
Pulsed Drain Current (Note 6)			I _{DM}	-1.5	А

Thermal Characteristics (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 5)	Steady State	PD	360	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{ extsf{ heta}JA}$	353	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

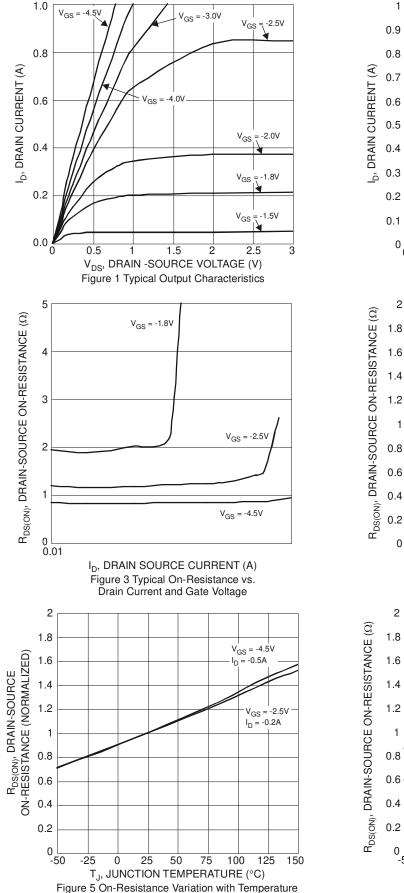
		1	1	1			
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV _{DSS}	-30	_	—	V	$V_{GS} = 0V, I_D = -250 \mu A$	
Zero Gate Voltage Drain Current	I _{DSS}		—	-100	nA	$V_{DS} = -24V, \ V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}		—	±10	μA	$V_{GS} = \pm 8V, V_{DS} = 0V$	
ON CHARACTERISTICS							
Gate Threshold Voltage	V _{GS(th)}	-0.4	—	-1.2	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$	
			0.9	1.5	Ω	$V_{GS} = -4.5V, I_D = -200mA$	
Static Drain-Source On-Resistance (Note 7)	R _{DS(on)}	_	1.2	2.5		$V_{GS} = -2.5V, I_D = -100mA$	
			2.5	7.5		$V_{GS} = -1.8V, I_D = -50mA$	
Diode Forward Voltage (Note 8)	V _{SD}	_	_	-1.0	V	$V_{GS} = 0V, I_{S} = -10mA$	
DYNAMIC CHARACTERISTICS (Note 8)	•						
Input Capacitance	Ciss	_	40.9	_	pF		
Output Capacitance	Coss	_	4.7	—	pF	[−] V _{DS} = -15V, V _{GS} = 0V, −f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	3.6	_	pF		
Total Gate Charge	Qg	_	0.7	_	nC		
Gate-Source Charge	Q _{gs}	_	0.1	_	nC	− V _{GS} = -4.5V, V _{DS} = -15V, −I _D = -200mA	
Gate-Drain Charge	Q _{gd}	_	0.2	_	nC	-1D = -20011A	
Turn-On Delay Time	t _{D(on)}	_	20	_	ns		
Turn-On Rise Time	tr	_	40	_	ns	V _{DS} = -15V, I _D = -200mA	
Turn-Off Delay Time	t _{D(off)}	_	144	_	ns	V_{GS} = -4.5V, R_G = 6 Ω	
Turn-Off Fall Time	t _f	_	58	_	ns	1	

5. Device mounted on FR-4 PCB, with minimum recommended pad layout. Notes:

Device mounted on minimum recommended pad layout test board, 10µs pulse duty cycle = 1%.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.



DMP32D5LFA



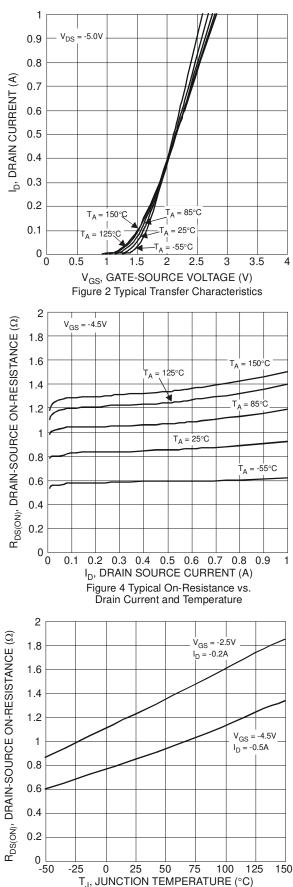
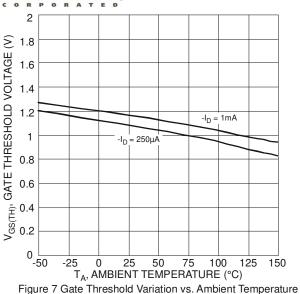
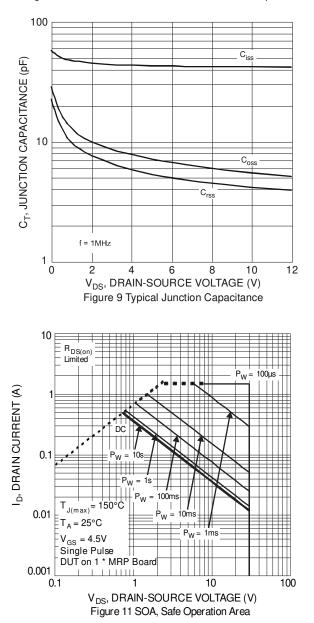


Figure 6 On-Resistance Variation with Temperature



DMP32D5LFA





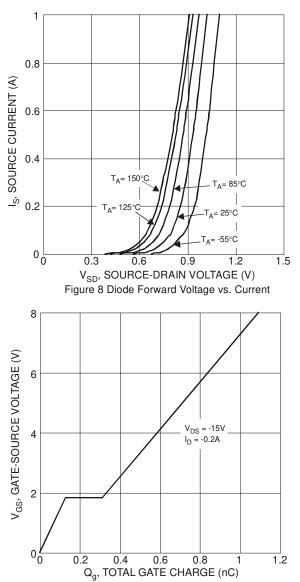
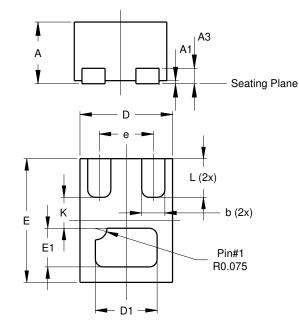


Figure 10 Gate-Charge Characteristics



Package Outline Dimensions

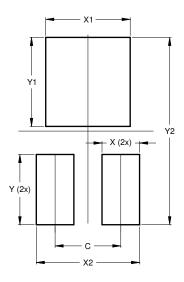
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



	X2-DFN0806-3					
Dim	Min	Max	Тур			
Α	0.375	0.40	0.39			
A1	0	0.05	0.02			
A3	-	-	0.10			
b	0.10	0.20	0.15			
D	0.55	0.65	0.60			
D1	0.35	0.45	0.40			
E	0.75	0.85	0.80			
E1	0.20	0.30	0.25			
е	-	-	0.35			
K	-	-	0.20			
L	0.20	0.30	0.25			
All C	All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)		
С	0.350		
Х	0.200		
X1	0.450		
X2	0.550		
Y	0.375		
Y1	0.475		
Y2	1.000		



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