

To our customers,

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## Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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## MOS FIELD EFFECT TRANSISTOR N0300P

### P-CHANNEL MOS FIELD EFFECT TRANSISTOR FOR SWITCHING

#### DESCRIPTION

The N0300P is a switching device which can be driven directly by a 4.5 V power source.

The device features a low on-state resistance and excellent switching characteristics, and is suitable for applications such as power switch of portable machine and so on.

#### FEATURES

- 4.5 V drive available
- Low on-state resistance  
 $R_{DS(on)1} = 72 \text{ m}\Omega \text{ MAX. (} V_{GS} = -10 \text{ V, } I_D = -2.0 \text{ A)}$   
 $R_{DS(on)2} = 105 \text{ m}\Omega \text{ MAX. (} V_{GS} = -4.5 \text{ V, } I_D = -2.0 \text{ A)}$
- Built-in gate protection diode

#### ORDERING INFORMATION

PART NUMBER	PACKAGE
N0300P-T1B-AT <sup>Note</sup>	SC-96 (Mini Mold Thin Type)

**Note** Pb-free (This product does not contain Pb in the external electrode and other parts.)

**Marking:** XZ

#### ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = 25°C)

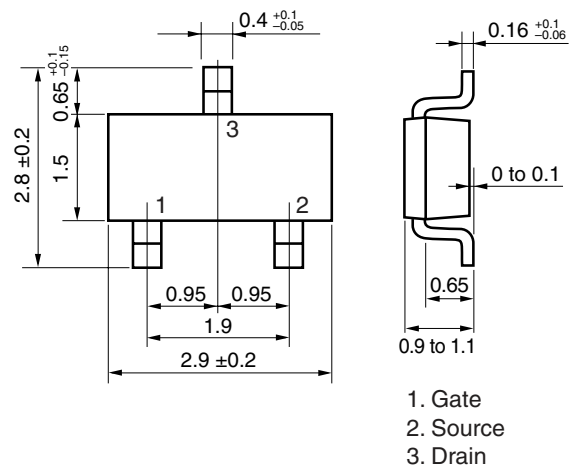
Drain to Source Voltage (V <sub>GS</sub> = 0 V)	V <sub>DSS</sub>	-30	V
Gate to Source Voltage (V <sub>DS</sub> = 0 V)	V <sub>GSS</sub>	±20	V
Drain Current (DC)	I <sub>D(DC)</sub>	±4.5	A
Drain Current (pulse) <sup>Note1</sup>	I <sub>D(pulse)</sub>	±18	A
Total Power Dissipation	P <sub>T1</sub>	0.2	W
Total Power Dissipation <sup>Note2</sup>	P <sub>T2</sub>	1.25	W
Channel Temperature	T <sub>ch</sub>	150	°C
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C

**Notes 1.** PW ≤ 10 μs, Duty Cycle ≤ 1%

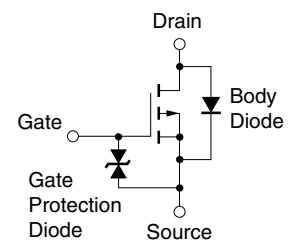
**2.** Mounted on FR-4 board of 50 mm x 50 mm x 1.6 mm, t ≤ 5 sec

**Remark** The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

#### PACKAGE DRAWING (Unit: mm)



#### EQUIVALENT CIRCUIT



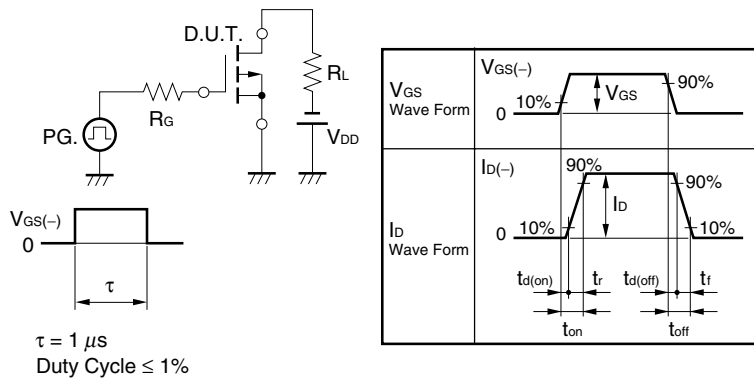
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**ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C)**

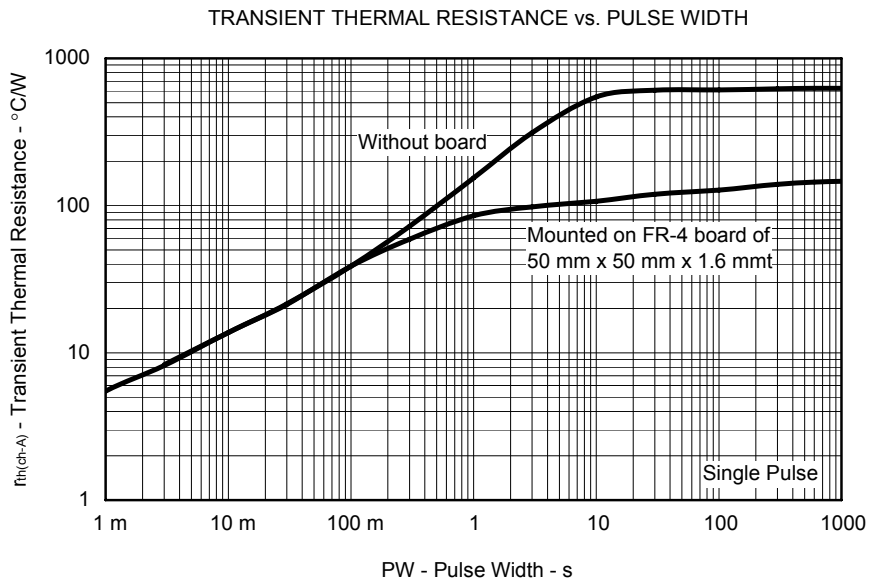
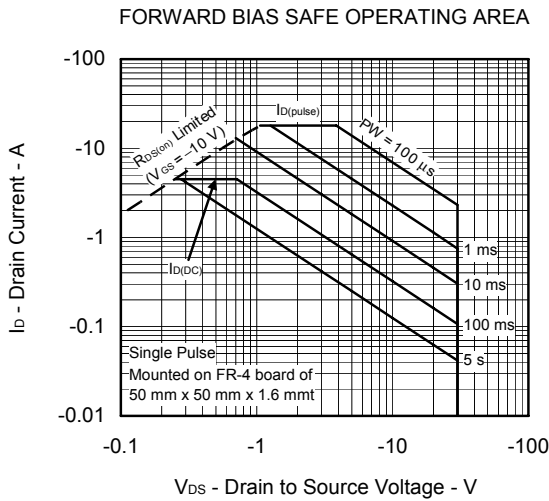
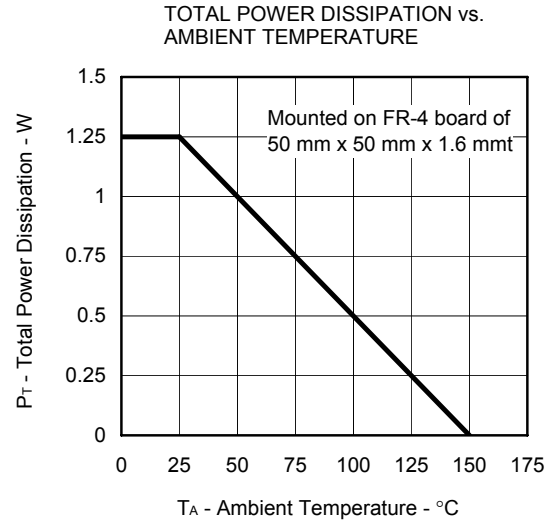
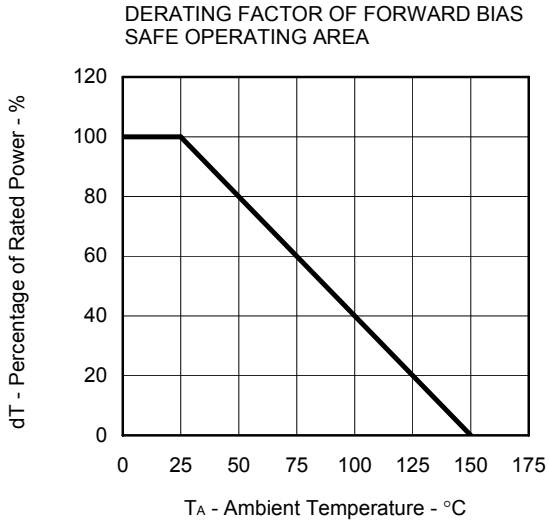
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = -30 V, V <sub>GS</sub> = 0 V			-1	μA
Gate Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V			±10	μA
Gate to Source Cut-off Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> = -10 V, I <sub>D</sub> = -1.0 mA	-1.0		-2.5	V
Forward Transfer Admittance <sup>Note</sup>	y <sub>fs</sub>	V <sub>DS</sub> = -10 V, I <sub>D</sub> = -1.5 A	1.0			S
Drain to Source On-state Resistance <sup>Note</sup>	R <sub>DS(on)1</sub>	V <sub>GS</sub> = -10 V, I <sub>D</sub> = -2.0 A		56	72	mΩ
	R <sub>DS(on)2</sub>	V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -2.0 A		75	105	mΩ
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = -10 V, V <sub>GS</sub> = 0 V,		345		pF
Output Capacitance	C <sub>oss</sub>	f = 1.0 MHz		78		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			65		pF
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = -15 V, I <sub>D</sub> = -2.0 A,		6.5		ns
Rise Time	t <sub>r</sub>	V <sub>GS</sub> = -10 V,		4.0		ns
Turn-off Delay Time	t <sub>d(off)</sub>	R <sub>G</sub> = 6 Ω		34		ns
Fall Time	t <sub>f</sub>			12		ns
Total Gate Charge	Q <sub>G</sub>	V <sub>DD</sub> = -24 V, V <sub>GS</sub> = -10.0 V, I <sub>D</sub> = -4.5 A		8.3		nC
Body Diode Forward Voltage <sup>Note</sup>	V <sub>F(S-D)</sub>	I <sub>F</sub> = 4.5 A, V <sub>GS</sub> = 0 V		0.95		V

**Note** Pulsed

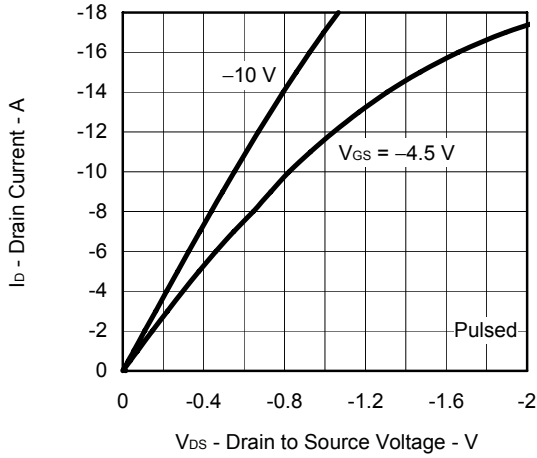
**TEST CIRCUIT SWITCHING TIME**



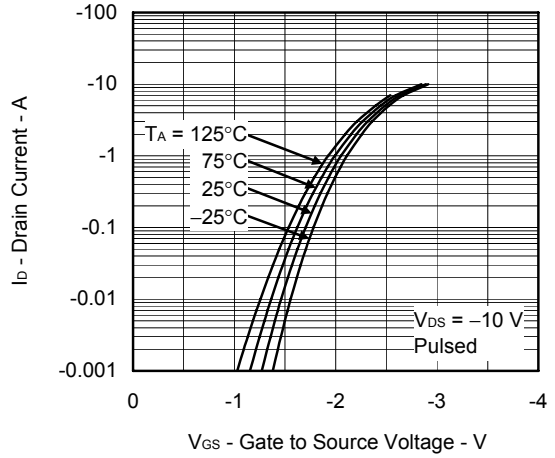
TYPICAL CHARACTERISTICS (TA = 25°C)



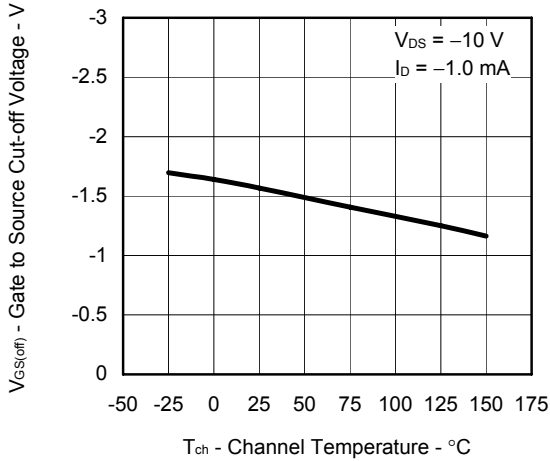
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



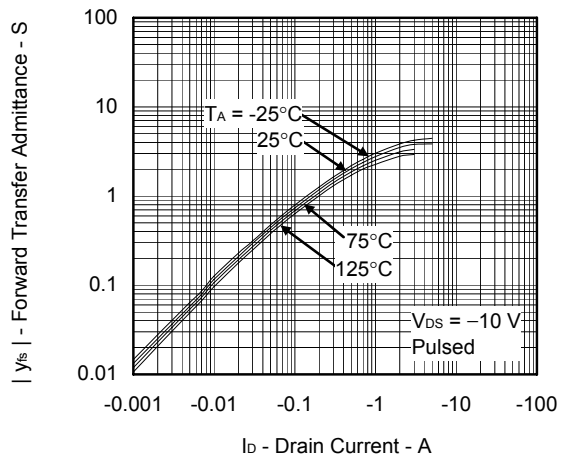
FORWARD TRANSFER CHARACTERISTICS



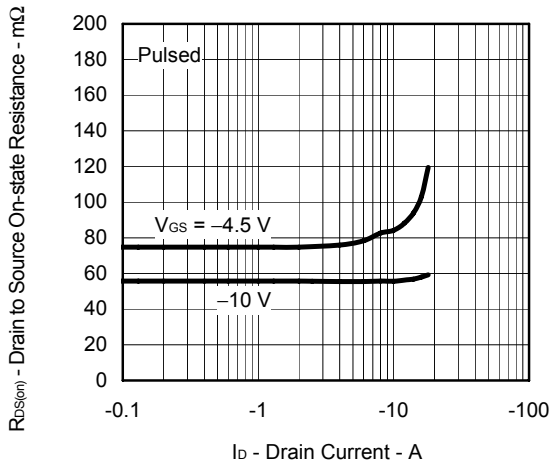
GATE TO SOURCE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE



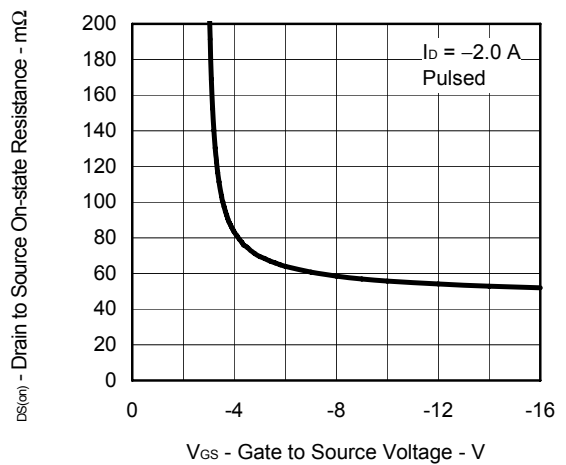
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



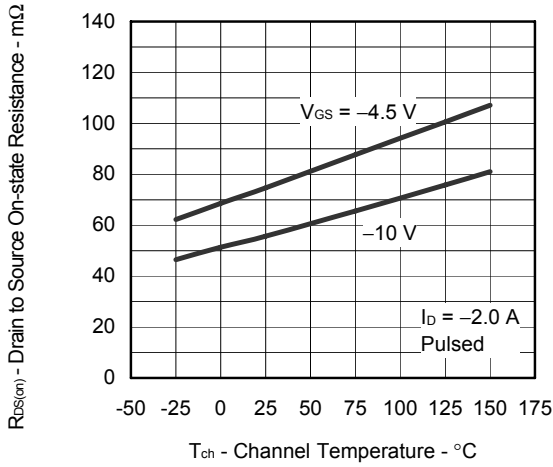
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



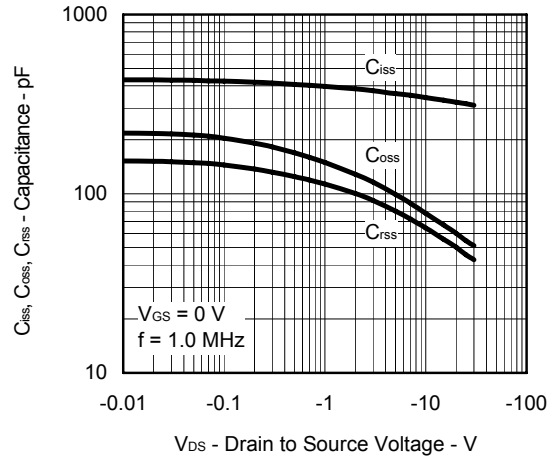
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



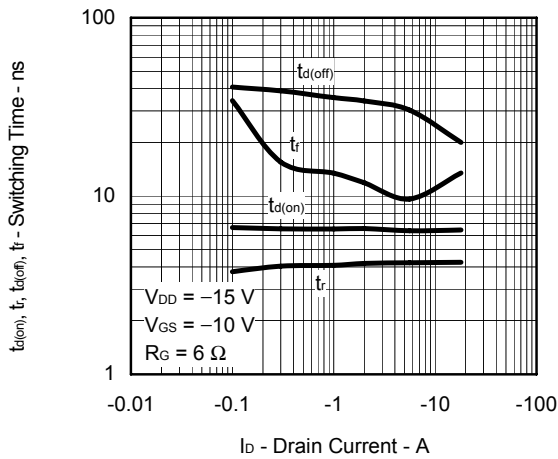
DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



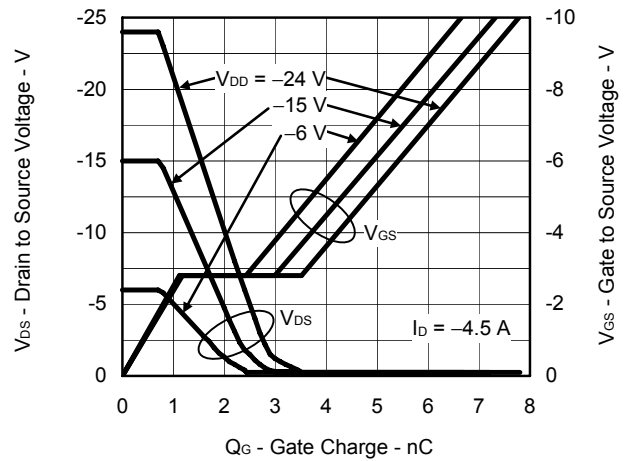
CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



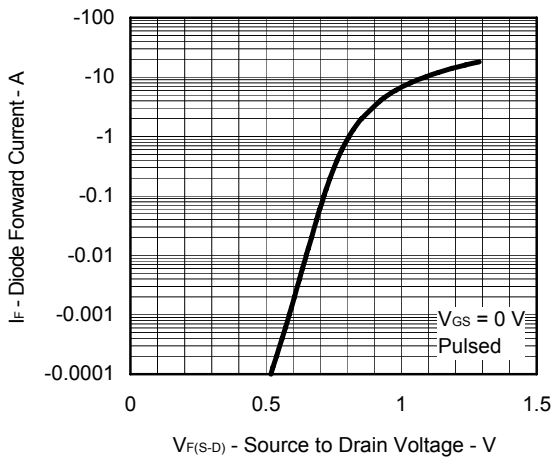
SWITCHING CHARACTERISTICS



DYNAMIC INPUT/OUTPUT CHARACTERISTICS



SOURCE TO DRAIN DIODE FORWARD VOLTAGE



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