

## OCXO Part No: OS560-2005-011

Issue 2; 6th May 2022

### Features

- Temperature stability  $\pm 10$ ppb
- Low phase noise
- Frequency 20MHz
- Low pre-aged options available
- The flexible nature of the design means that variations to suit almost any application can be developed to meet individual customer requirements



### Option B

- Temperature stability:  $\pm 10$ ppb over  $(-20$  to  $70)^{\circ}\text{C}$
- Output: Sinewave 7dBm nominal
- Voltage: 5.0V
- Warm up Current: 520mA
- Quiescent current: 220mA

### Phase Noise (typical)

- $F_0+10\text{Hz}$  -128 dBc/Hz
- $F_0+100\text{Hz}$  -145 dBc/Hz
- $F_0+1\text{KHz}$  -155 dBc/Hz
- $F_0+10\text{KHz}$  -160 dBc/Hz
- $F_0+100\text{KHz}$  -168 dBc/Hz

Values based on 10MHz unit

### Voltage / Load change

- $\pm 5\%$  supply voltage change:  $\pm 2$ ppb
- $\pm 10\%$  load change:  $\pm 10$ ppb

### Ageing

After 30 days continuous operation:

- Per day:  $\pm 0.1$ ppb max.
- Per year:  $\pm 50$ ppb max.
- Warm up time: 5 minutes to within 0.1 ppm

### Voltage Trim

- 0.5ppm minimum
- Trim impedance 50K $\Omega$

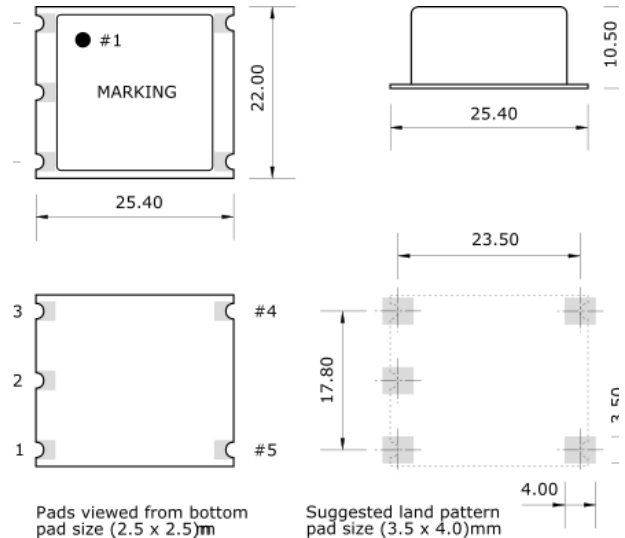
### Reference Options

- 3.0V or 4.5V

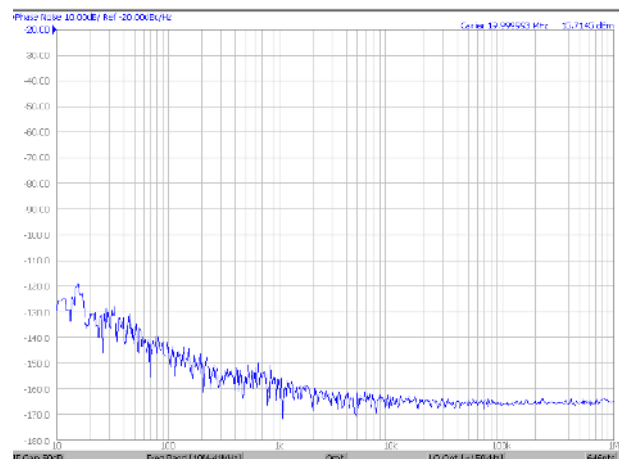
### Environmental

- Electrostatic-Sensitive Device (ESD)
- Storage Temperature Range:  $(-40$  to  $+125)^{\circ}\text{C}$
- Mechanical shock: MIL standard 202F, method 213, condition J

### Dimensions (mm)



### Phase Noise Plot



- Thermal shock: MIL standard 202F, method 107, condition A
- Vibration: MIL standard 202F, method 204, condition B
- Solderability: 5 seconds maximum at 230°C
- 3 seconds maximum at 350°C
- Unique customer part number and custom specification issued with each application

#### Compliance

- RoHS Status (2011/65/EU) - Compliant
- REACH Status - Compliant

#### Packaging

- Pack Style: Bulk

#### Ordering Information

- Unique customer part number and custom specification issued with each application
- OCXO Part No: OS560-2005-011
- Frequency: 20MHz
- Stability/Output/Voltage: Option B
- Supply voltage code: V2= +5Vd.c. supply
- Add suffix (R) for Vref output on pin #5

#### Test Circuit - Sinewave

