



100V INPUT, 5V 30mA REGULATOR TRANSISTOR

Description

The ZXTR2005ZQ monolithically integrates a transistor, Zener diode and resistor to function as a high voltage linear regulator. The device regulates with a 5V nominal output at 15mA. It is designed for use in high voltage applications where standard linear regulators cannot be used. This function is fully integrated into a SOT89 package, minimizing PCB area and reducing number of components when compared with a multi-chip discrete solution.

Applications

Supply Voltage Regulation in:

- Startup Switch in DC-DC Converters
- Networking
- Telecommunications
- Power-over-Ethernet (PoE)

Features

- Series Linear Regulator Using Emitter-Follower Stage
- Input Voltage = 10V to 100V (For Regulated Output Voltage)
- Output Voltage = 5V ± 10%
- 150kΩ Resistor to Limit Quiescent Current
- Fully Integrated Into a SOT89 Package
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The ZXTR2005ZQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

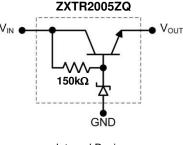
https://www.diodes.com/quality/product-definitions/

Mechanical Data

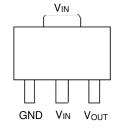
- Case: SOT89
- Case Material: Molded Plastic. "Green" Molding Compound.
 UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.052 grams (Approximate)



Top View



Internal Device Schematic



Top View

Pin Name	Pin Function
VIN	Input Supply
GND	Power Ground
V _{OUT}	Voltage Output

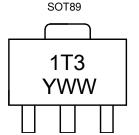
Ordering Information (Note 4)

- 5						
	Part Number	Package	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
	ZXTR2005ZQ-13	SOT89	1T3	13	12	2,500

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



1T3 = Product Type Marking Code YWW = Date Code Marking Y = Last Digit of Year (ex: 1 = 2021) WW = Week Code (01 to 53)



Absolute Maximum Ratings (Voltage relative to GND, @Ta = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Input Voltage	VIN	-0.3 to 100	V
Continuous Input & Output Current	IIN, IOUT	350	mA
Peak Pulsed Input & Output Current	Іім, Іом	2	Α
Maximum Voltage applied to Vout	Vout(max)	Smaller of V _{IN+} 5V or 11V	V

Maximum Current at V_{IN} = 48V (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Continuous Output Current	(Note 7)	Іоит	38	mA
Duland Output Current	(Note 8)	la	740	m A
Pulsed Output Current	(Note 9)	Іом	150	mA

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Power Dissipation	(Note 5)	D-	1.7	W
Power Dissipation	(Note 6)	PD	0.89] vv
Thermal Resistance, Junction to Ambient	(Note 5)	Do	59	
Thermal Resistance, Junction to Ambient	(Note 6)	Reja -	112	00044
Thermal Resistance, Junction to Lead	(Note 10)	ReJL	20	°C/W
Thermal Resistance, Junction to Case	(Note 10)	Rejc	15.7	
Recommended Operating Junction Temperature F	TJ	-40 to +125	°C	
Maximum Operating Junction and Storage Temper	T _J , T _{STG}	-65 to +150	°C	

ESD Ratings (Note 11)

Notes:

Characteristics	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge – Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge – Machine Model	ESD MM	400	V	С

5. For a device mounted with the exposed VIN pad on 50mm x 50mm 1oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still

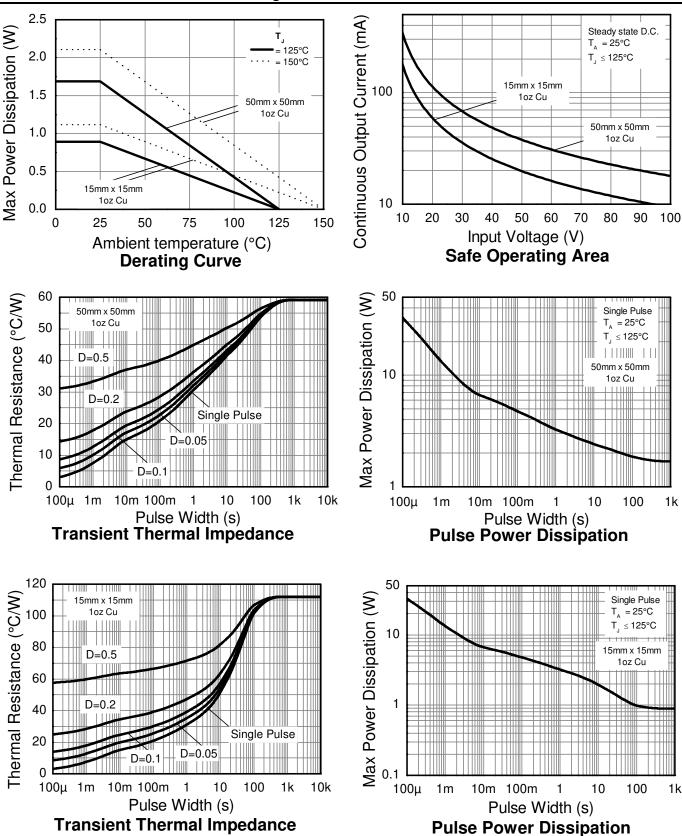
air conditions whilst operating in steady-state.

6. Same as note 5, except mounted on 15mm x 15mm 1oz copper.

- 7. Same as note 5, whilst operating at VIN = 48V. Refer to Safe Operating Area for other Input Voltages.
- 8. Same as note 5, except measured with a single pulse width = 100µs and VIN = 48V.
- 9. Same as note 5, except measured with a single pulse width = 10ms and V_{IN} = 48V.
- 10. ReuL = Thermal resistance from junction to solder-point (on the exposed VIN pad). ReuC = Thermal resistance from junction to the top of case.
- 11. Refer to JEDEC specification JESD22-A114 and JESD22-A115.



Thermal Characteristics and Derating Information





Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Output Voltage (Note 12)	Vout	4.5	5.0	5.5	V	V _{IN} = 48V, I _{OUT} = 15mA
Line Regulation (Notes 12 & 13)	ΔV out	1	195	300	mV	V _{IN} = 10V to 72V, I _{OUT} = 15mA
Temperature Coefficient	ΔVουτ/ΔΤ	l	7.0		mV/°C	T _J = -40°C to +125°C V _{IN} = 48V, I _{OUT} = 15mA
Load Regulation (Notes 12 & 14)	ΔV_{OUT}	_	-185 -205	-350 -400	mV	$I_{OUT} = 0.1$ to 30mA, $V_{IN} = 48V$ $I_{OUT} = 0.1$ to 100mA, $V_{IN} = 48V$
Minimum Value of Input Voltage Required to Maintain Line Regulation	VIN(MIN)	10	-	_	V	_
Quiescent Current	lα		260 550	500 900	μΑ	$V_{IN} = 48V, \ I_{OUT} = 10 \mu A$ $V_{IN} = 100V, \ I_{OUT} = 10 \mu A$
Power Supply Rejection Ratio	ΔVιη/ΔVουτ	_	45	_	dB	$C_{OUT} = 100nF$, $I_{OUT} = 15mA$, $V_{OUT} = 5V$, $V_{IN} = 10V$ to $100V$, $f = 100Hz$

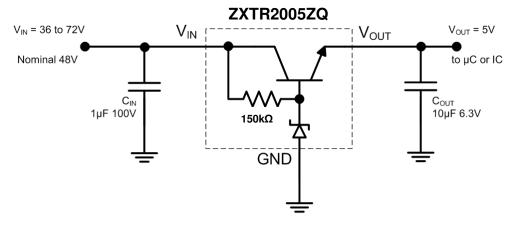
Notes: 12. Measured under pulsed conditions. Pulse width \leq 300 μ s. Duty cycle \leq 2%.

13. Line regulation $\Delta VOUT = VOUT(@VIN = 72V) - VOUT(@VIN = 10V)$

14. Load regulation $\Delta VOUT = VOUT(@ IOUT = 30mA) - VOUT(@ IOUT = 0.1mA)$

 $\Delta VOUT = VOUT(@ IOUT = 100mA) - VOUT(@ IOUT = 0.1mA)$

Typical Application Circuit



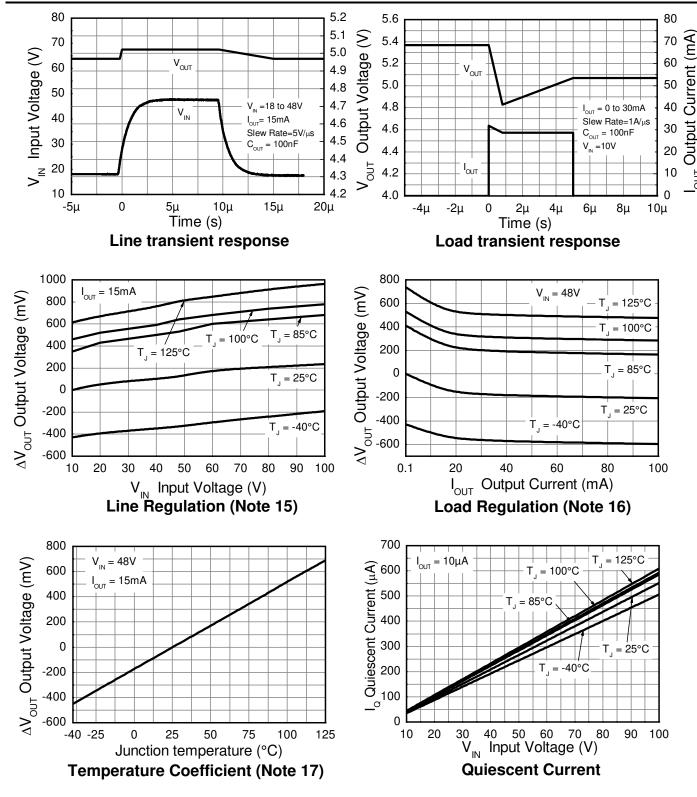
Example of a 5V regulated supply from a nominal 48V for powering a Controller IC.

Pin Functions

Pin Name Pin Function		Notes
Vin	Input Supply	Input voltage can vary from -0.3V to 100V with respect to GND; for VouT regulated then $10V \le V_{IN} \le 100V$. It is recommended to connect a $1\mu F$ capacitor to GND.
GND	Power Ground	This pin should be tied to the system ground.
Vоит	Voltage Output	Outputs a regulated 5V when $10V \le V_{IN} \le 100V$. When $V_{IN} < 10V$, then V_{OUT} maximum = $V_{IN} - 1.5V$. This pin can be pulled high to a maximum of +11V with respect to GND, or +5V with respect to V_{IN} , whichever is lower. It is recommended to connect a $10\mu F$ capacitor to GND and a minimum of $10\mu A$ to be drawn from V_{OUT} to maintain regulation.



Typical Electrical Characteristics (@TA = +25°C, unless otherwise specified.)



Notes: 15. Line regulation $\Delta VOUT = VOUT - VOUT$ (@ VIN = 10V, IOUT = 15mA, TJ = +25°C)

16. Load regulation Δ VOUT = VOUT - VOUT (@ VIN = 48V, IOUT = 0.1mA, TJ = +25°C)

17. Temperature Coefficient $\Delta V_{OUT} = V_{OUT} - V_{OUT}$ (@ $V_{IN} = 48V$, $I_{OUT} = 15mA$, $T_{J} = +25$ °C)



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

TOP VIEW TOP SIEW TOP SI

SOT89					
Dim	Min	Max	Тур		
Α	1.40	1.60	1.50		
В	0.50	0.62	0.56		
B1	0.42	0.54	0.48		
С	0.35	0.43	0.38		
D	4.40	4.60	4.50		
D1	1.62	1.83	1.733		
D2	1.61	1.81	1.71		
E	2.40	2.60	2.50		
E2	2.05	2.35	2.20		
е	ı	1	1.50		
Н	3.95	4.25	4.10		
H1	2.63	2.93	2.78		
L	0.90	1.20	1.05		
L1	0.327	0.527	0.427		
Z	0.20	0.40	0.30		
All Dimensions in mm					

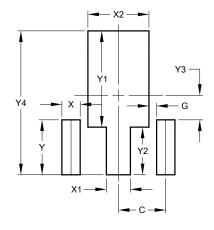
Suggested Pad Layout

 $Please \ see \ http://www.diodes.com/package-outlines.html \ for \ the \ latest \ version.$

SOT89

BOTTOM VIEW

SOT89



Dimensions	Value (in mm)
С	1.500
G	0.244
Х	0.580
X1	0.760
X2	1.933
Υ	1.730
Y1	3.030
Y2	1.500
Y3	0.770
Y4	4.530

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