

MC4558

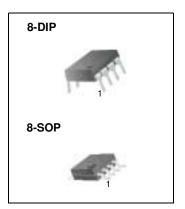
Dual Operational Amplifier

Features

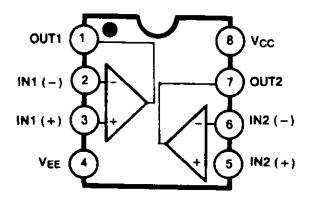
- No frequency compensation required.
- No latch up.
- Large common mode and differential voltage range.
- Parameter tracking over temperature range.
- Gain and phase match between amplifiers.
- Internally frequency compensated.
- Low noise input transistors.

Descriptions

The MC4558 series is a monolithic integrated circuit designed for dual operational amplifier.

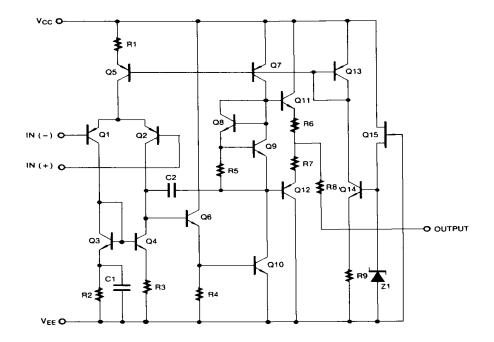


Internal Block Diagram



Schematic Diagram

(One Section Only)



Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Supply Voltage	Vcc	±22	V
Differential Input Voltage	V _I (DIFF)	30	V
Input Voltage	VI	±15	V
Power Dissipation	PD	400	mW
Operating Temperature Range MC4558C MC4558V	TOPR	0 ~ 70 -40 ~ 85	°C
Storage Temperature Range	TSTG	-65 ~ 150	°C

Electrical Characteristics

(VCC = 15V, VEE = - 15V, TA = 25 $^{\circ}$ C unless otherwise specified)

Dawamatan	Compleal	mbol Conditions		MC4558C/MC4558V			
Parameter	Symbol			Min	Тур	Max	Unit
Input Offset Voltage	anut Offact Valtage Via		Rs≤10KΩ		2	6	m\/
Input Offset Voltage	VIO		Note 1	-	-	7.5	- mV
Input Offset Current	lio			-	5	200	
			$T_A=T_A(MAX)$	-	ı	300	nA
			TA = TA(MIN)	-	ı	300	
				-	30	500	nA
Input Bias Current	IBIAS		$T_A=T_A(MAX)$	-	i	800	
			TA = TA(MIN)	-	i	800	
Large Signal	Gv	VO(P-P)= ±10\	V,RL≤2KΩ	20	200	-	V/mV
Voltage Gain	αv		Note 1	-	i	-	V/IIIV
Common Mode Input Voltage Range	VIO			±12	±13	-	- V
	VI(H)		Note 1	-	i	-	
Common Mode	CMRR	R _S ≤10KΩ		70	90	-	dB
Rejection Ratio	OWNT		Note 1	-	i	-	1 46
Supply Voltage	PSRR	R _S ≤10KΩ		76	90	-	dB
Rejection Ratio	1 31111		Note 1	76	90	-	GD.
Output Voltage Swing	VO(P.P)	RL≥10KΩ		±12	±14	-	V
		RL≥2KΩ		±10	±13	-	_ v
Supply Current (Both Amplifiers)				-	3.5	5.8	
	Icc		$T_A = T_A(MAX)$	-	i	5.0	mA
			$T_A = T_A(MIN)$	-	-	6.7	
Power Consumption (Both Amplifiers)	PC			-	70	170	
			TA = TA(MAX)	-	-	150	mW
			$T_a = T_A(MIN)$	-	-	200	
Slew Rate (Note2)	SR	V _I =10V, R _L ≥2KΩ C _I ≤100pF		1.2	-	-	V/μs
Rise Time (Note2)	TR	VI =20mV, RL≥2KΩ CI≤100pF		-	0.3	-	μs
Overshoot (Note2)	os	V _I =20mV, R _L ≥2KΩ C _I ≤100pF		-	15	-	%

Note:

 $^{1. \} MC4558C: T_{A(MIN)} \leq T_{A} \leq T_{A(MAX)} = 0 \leq T_{A} \leq 70 \ ^{\circ}C \ , \ MC4558V: T_{A(MIN)} \leq T_{A} \leq T_{A(MAX)} = -40 \leq T_{A} \leq +85 \ ^{\circ}C = -40 \leq T_{A} \leq +85 \$

^{2.} Guaranteed by design.

Typical Performance Characteristics

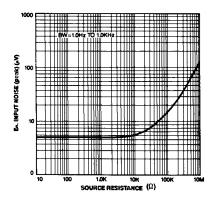


Figure 1. Burst Noise vs Source Resistance

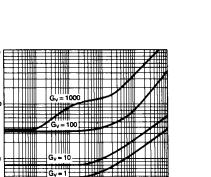


Figure 3. Output Noise vs Source Resistance

En. OUTPUT NOISE (RMS) (mV)

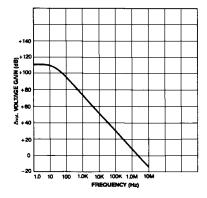


Figure 5. Open Loop Frequency Response

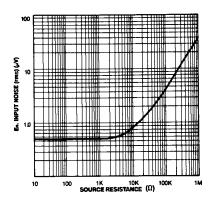


Figure 2. RMS Noise vs Source Resistance

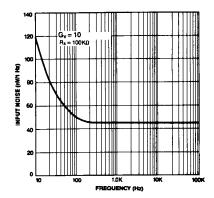


Figure 4. Spectral Noise Density

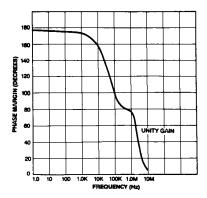


Figure 6. Phase Margin vs Frequency

Typical Performance Characteristics (continued)

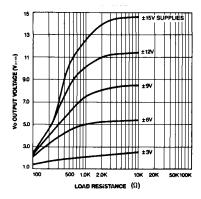


Figure 7. Positive Output Voltage Swing vs Load Resistance

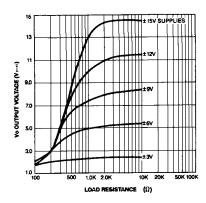


Figure 8. Negative Output Voltage Swing vs Load Resistance

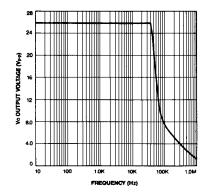
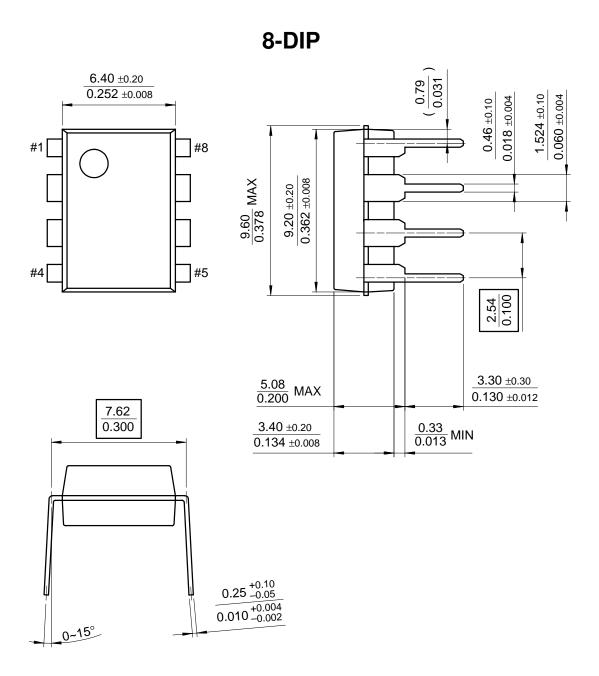


Figure 9. Power Bandwidth (Large Signal Output Swing vs Frequency)

Mechanical Dimensions

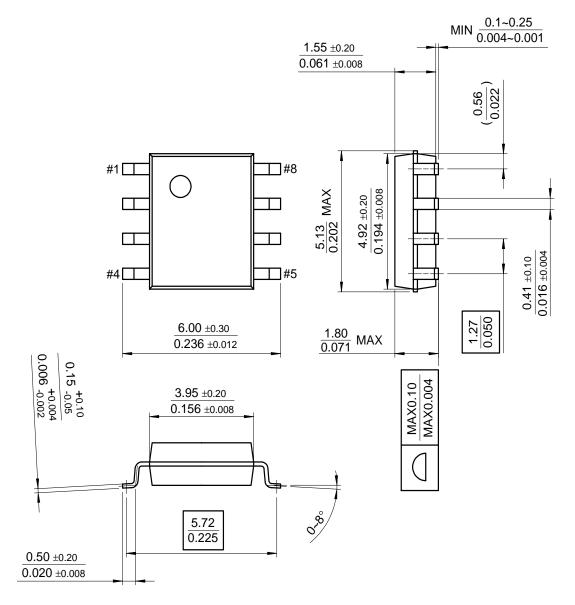
Package



Mechanical Dimensions (Continued)

Package

8-SOP



Ordering Information

Product Number	Package	Operating Temperature	
MC4558CP	8-DIP	0 ~ + 70°C	
MC4558CD	8-SOP	0 % + 70 C	
MC4558VP	8-DIP	-40 ∼ +85°C	
MC4558VD	8-SOP	-40 ·- +83 C	

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