



100V INPUT, 12V 50mA REGULATOR TRANSISTOR

Description

The ZXTR2012K monolithically integrates a transistor, zener diode and resistor to function as a high-voltage linear regulator. The device regulates with a 12V nominal output at 15mA. It's designed for use in high-voltage applications where standard linear regulators cannot be used. This function is fully integrated into a TO252 package, minimizing PCB area and reducing a number of components when compared with a multi-chip discrete solution.

Applications

Supply voltage regulation in:

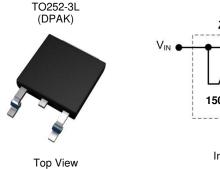
- Startup switch in DC-DC converters
- Networking
- Telecom
- Power over Ethernet (PoE)

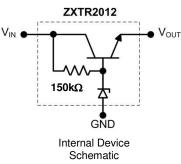
Features

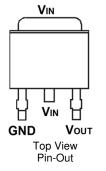
- Series Linear Regulator Using Emitter-Follower Stage
- Input Voltage = 15V to 100V (For regulated output voltage)
- Output Voltage = 12V ± 10%
- 150kΩ resistor to limit quiescent current
- Fully integrated into a TO252 package
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 for High Reliability

Mechanical Data

- Case: TO252-3L (DPAK)
- Case Material: Molded Plastic, "Green" Molding Compound.
- UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads.
- Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.34 grams (Approximate)







Pin Name	Pin Function
VIN	Input Supply
GND	Power Ground
Vout	Voltage Output

Ordering Information (Note 4)

Product	Package	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXTR2012K-13	TO252-3L	ZXTR 2012	13	16	2,500

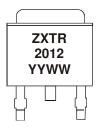
Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

 See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen and Antimony free, "Green" and Lead-Free.

3. Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



ZXTR 2012 = Product Type Marking Code YYWW = Date Code Marking YY = Last Digit of Year, (ex: 17 = 2017) WW = Week Code 01 - 52



Absolute Maximum Ratings (Voltage relative to GND, @T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Input Supply Voltage	V _{IN}	-0.3 to 100	V
Continuous Input & Output Current	IIN, IOUT	750	mA
Peak Pulsed Input & Output Current	I _{IM} , I _{OM}	2	A
Maximum Voltage applied to V _{OUT}	V _{OUT(max)}	Smaller of V _{IN} +12V or 18V	V

Maximum Current at V_{IN} = 48V (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Continuous Output Current	(Note 7)	lout	60	mA	
Ruland Output Current	(Note 8)		960	m (
Pulsed Output Current	(Note 9)	IOM	200	mA	

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Rower Dissinction	(Note 5)	р	2.3	w
Power Dissipation	(Note 6)	PD PD	1.1	vv
Thermal Desistance Junction to Ambient	(Note 5)	D	44	
Thermal Resistance, Junction to Ambient	(Note 6)	R _{0JA}	90	°C/W
Thermal Resistance, Junction to Lead	(Note 10)	R _{0JL}	8.4	C/VV
Thermal Resistance, Junction to Case (Note 10)		R _{0JC}	14.6	
Recommended Operating Junction Temperature Range		TJ	-40 to +125	O
Maximum Operating Junction and Storage Temperature Range		T_J , T_STG	-65 to +150	

ESD Ratings (Note 11)

Characteristics	Symbols	Value	Unit	JEDEC Class
Electrostatic Discharge – Human Body Model	ESD HBM	4000	V	ЗA
Electrostatic Discharge – Machine Model	ESD MM	400	V	С

Notes: 5. For a device mounted with the exposed V_{IN} pad on 50mm x 50mm 1oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in steady-state.
Same as note 5, except mounted on 15mm x 15mm 1oz copper.

7. Same as note 5, whilst operating at V_{IN} = 48V. Refer to Safe Operating Area for other Input Voltages.

8. Same as note 5, except measured with a single pulse width = 100μ s and V_{IN} = 48V.

9. Same as note 5, except measured with a single pulse width = 10ms and V_{IN} = 48V.

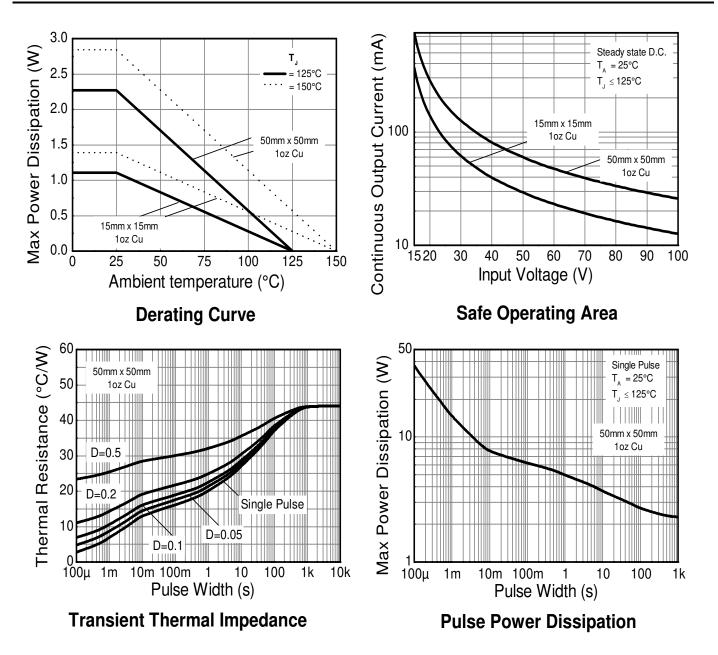
10. $R_{\theta JL}$ = Thermal resistance from junction to solder-point (on the exposed V_{IN} pad).

 $R_{\theta JC}$ = Thermal resistance from junction to the top of case.

11. Refer to JEDEC specification JESD22-A114 and JESD22-A115.



Thermal Characteristics and Derating Information





Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Output Voltage (Note 12)	V _{OUT}	10.8	12	13.2	V	$V_{IN} = 48V, I_{OUT} = 15mA$
Line Regulation (Notes 12 & 13)	ΔV_{OUT}	_	240	750	mV	$V_{IN} = 15$ to $72V$, $I_{OUT} = 15mA$
Temperature Coefficient	$\Delta V_{OUT} / \Delta T$		8.0		mV/°C	$T_J = -40^{\circ}C \text{ to } +125^{\circ}C$ $V_{IN} = 48V, I_{OUT} = 15mA$
Load Regulation (Notes 12 & 14)	ΔV_{OUT}	_	-450 -600	-600 -750	mV	$I_{OUT} = 0.1$ to 30mA, $V_{IN} = 48V$ $I_{OUT} = 0.1$ to 100mA, $V_{IN} = 48V$
Minimum Value of Input Voltage Required to Maintain Line Regulation	V _{IN(MIN)}	15	—		V	_
Quiescent Current	lq	_	240 590	400 900	μΑ	$\label{eq:VIN} \begin{split} V_{IN} &= 48V, \ I_{OUT} = 10 \mu A \\ V_{IN} &= 100V, \ I_{OUT} = 10 \mu A \end{split}$
Power Supply Rejection Ratio	$\Delta V_{\text{IN}} / \Delta V_{\text{OUT}}$	_	45	_	dB	C _{OUT} = 100nF, I _{OUT} = 15mA, V _{OUT} = 12V, V _{IN} =15 to 100V, f=100H

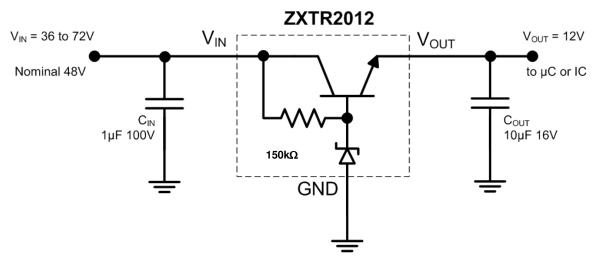
Notes: 12. Measured under pulsed conditions. Pulse width \leq 300µs. Duty cycle \leq 2%

 $\Delta V_{OUT} = V_{OUT} (@V_{IN} = 72V) - V_{OUT} (@V_{IN} = 15V)$ 13. Line regulation

14. Load regulation

 $\Delta V_{OUT} = V_{OUT}(@ |_{OUT} = 30mA) - V_{OUT}(@ |_{OUT} = 0.1mA)$ $\Delta V_{OUT} = V_{OUT}(@ |_{OUT} = 100mA) - V_{OUT}(@ |_{OUT} = 0.1mA)$

Typical Application Circuit

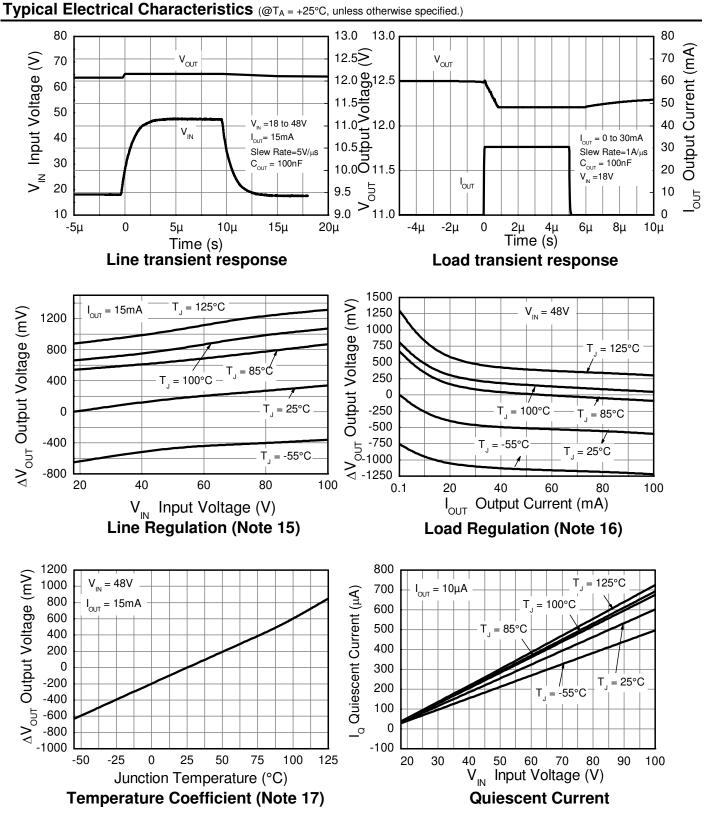


Example of an 12V regulated supply from a nominal 48V for powering a Controller IC.

Pin Functions

Pin Name	Pin Function	Notes	
V _{IN}	Input Supply	Input voltage can vary from -0.3V to 100V with respect to GND; for V _{OUT} regulated then $15V \le V_{IN} \le 100V$. It is recommended to connect a 1µF capacitor to GND.	
GND	Power Ground	This pin should be tied to the system ground.	
Vout	Voltage Output	Outputs a regulated 12V when $15V \le V_{IN} \le 100V$. When $V_{IN} < 15V$, then V_{OUT} maximum = $V_{IN} - 1.5V$. The pin can be pulled high to a maximum of +18V with respect to GND, or +12V with respect to V_{IN} , whichever is lower. It is recommended to connect a 10μ F capacitor to GND and a minimum of 10μ A to be drawn from V_{OUT} to maintain regulation.	



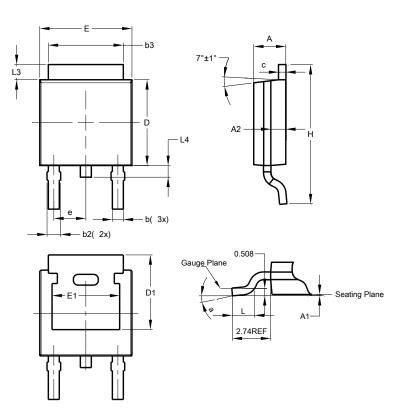


Notes: 15. Line regulation $\Delta V_{OUT} = V_{OUT} - V_{OUT}(@V_{IN} = 15V, I_{OUT} = 15\text{mA}, T_J = +25^{\circ}\text{C})$ 16. Load regulation $\Delta V_{OUT} = V_{OUT} - V_{OUT}(@V_{IN} = 48V, I_{OUT} = 0.1\text{mA}, T_J = +25^{\circ}\text{C})$ 17. Temperature Coefficient $\Delta V_{OUT} = V_{OUT} - V_{OUT}(@V_{IN} = 48V, I_{OUT} = 15\text{mA}, T_J = +25^{\circ}\text{C})$



Package Outline Dimensions

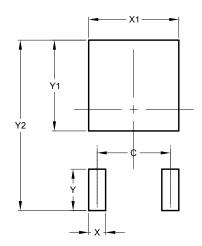
Please see http://www.diodes.com/package-outlines.html for the latest version.



TO252 (DPAK)						
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Dim	Min	Max	Тур			
Α	2.19	2.39	2.29			
A1	0.00	0.13	0.08			
A2	0.97	1.17	1.07			
b	0.64	0.88	0.783			
b2	0.76	1.14	0.95			
b3	5.21	5.46	5.33			
С	0.45	0.58	0.531			
D	6.00	6.20	6.10			
D1	5.21	-	-			
е	-	-	2.286			
Е	6.45	6.70	6.58			
E1	4.32	-	-			
Η	9.40	10.41	9.91			
L	1.40	1.78	1.59			
L3	0.88	1.27	1.08			
L4	0.64	1.02	0.83			
а	0°	10°	-			
All	Dimen	sions i	n mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	4.572
Х	1.060
X1	5.632
Y	2.600
Y1	5.700
Y2	10.700



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