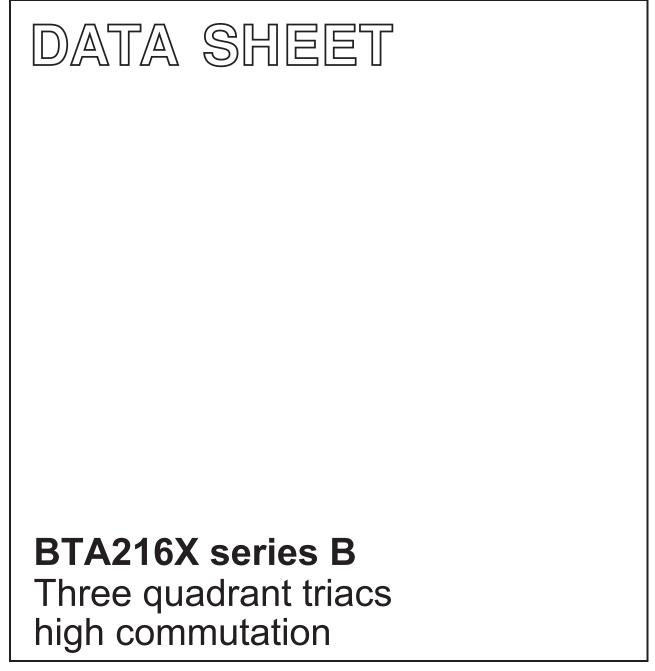
DISCRETE SEMICONDUCTORS



Product specification

September 2018



## **BTA216X series B**

## **GENERAL DESCRIPTION**

Glass passivated high commutation triacs in a full pack, plastic envelope intended for use in circuits where high static and dynamic dV/dt and high dl/dt can occur. These devices will commutate the full rated rms current at the maximum rated junction temperature, without the aid of a snubber.

#### **PINNING - SOT186A**

PIN	DESCRIPTION		
1	main terminal 1		
2	main terminal 2		
3	gate		
case	isolated		

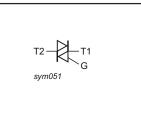
## QUICK REFERENCE DATA

SYMBOL	PARAMETER	MAX.	MAX.	MAX.	UNIT
V <sub>DRM</sub> I <sub>T(RMS)</sub> I <sub>TSM</sub>	BTA216X- Repetitive peak off-state voltages RMS on-state current Non-repetitive peak on-state current	<b>500B</b> 500 16 140	<b>600B</b> 600 16 140	<b>800B</b> 800 16 140	V A A

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**PIN CONFIGURATION** 

## SYMBOL



## LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.		MAX.		UNIT
V <sub>DRM</sub>	Repetitive peak off-state voltages		-	<b>-500</b> 500 <sup>1</sup>	<b>-600</b> 600 <sup>1</sup>	<b>-800</b> 800	v
I <sub>T(RMS)</sub>	RMS on-state current	full sine wave;	-		16		A
I <sub>TSM</sub>	Non-repetitive peak on-state current	$T_{hs} \le 38 \degree C$ full sine wave; $T_j = 25 \degree C$ prior to surge t = 20 ms	_		140		A
l <sup>2</sup> t	I <sup>2</sup> t for fusing	t = 16.7 ms t = 10 ms	-		150 98		A A <sup>2</sup> s
dl <sub>⊤</sub> /dt	Repetitive rate of rise of on-state current after triggering	$      I_{TM} = 20 \text{ A}; \  I_G = 0.2 \text{ A}; \\       dI_G / dt = 0.2 \text{ A} / \mu s $			100		A/μs
$\begin{matrix} I_{GM} \\ V_{GM} \\ P_{GM} \\ P_{G(AV)} \end{matrix}$	Peak gate current Peak gate voltage Peak gate power Average gate power	over any 20 ms	- - -		2 5 5 0.5		A V W W
T <sub>stg</sub> T <sub>j</sub>	Storage temperature Operating junction temperature	period	-40 -		150 125		°C °C

<sup>1</sup> Although not recommended, off-state voltages up to 800V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 15 A/ $\mu$ s.

## BTA216X series B

**ISOLATION LIMITING VALUE & CHARACTERISTIC** 

 $T_{hs} = 25$  °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V <sub>isol</sub>	R.M.S. isolation voltage from all three terminals to external heatsink	f = 50-60 Hz; sinusoidal waveform; R.H. $\leq 65\%$ ; clean and dustfree	-		2500	V
C <sub>isol</sub>	Capacitance from T2 to external heatsink	f = 1 MHz	-	10	-	pF

### THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
R <sub>th j-hs</sub> R <sub>th j-a</sub>	Thermal resistance junction to heatsink Thermal resistance junction to ambient	full or half cycle with heatsink compound without heatsink compound in free air	- -	- - 55	4.0 5.5 -	K/W K/W K/W

## STATIC CHARACTERISTICS

 $T_i = 25$  °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>GT</sub>	Gate trigger current <sup>2</sup>	$V_{\rm D} = 12 \text{ V}; I_{\rm T} = 0.1 \text{ A}$				
GI		T2+G+	2	18	50	mA
		T2+ G-	222	21	50	mA
		T2- G-	2	34	50	mA
I <sub>1</sub>	Latching current	$V_{\rm D} = 12 \text{ V}; I_{\rm GT} = 0.1 \text{ A}$				
-		T2+G+	-	31	60	mA
		T2+ G-	-	34	90	mA
		T2- G-	-	30	60	mA
I <sub>H</sub>	Holding current	$V_{\rm D} = 12 \text{ V}; I_{\rm GT} = 0.1 \text{ A}$	-	31	60	mA
İΫ <sub>T</sub>	On-state voltage	$I_{T} = 20 \text{ A}$	-	1.2	1.5	V
V <sub>GT</sub>	Gate trigger voltage	$\dot{V}_{\rm D} = 12 \text{ V}; I_{\rm T} = 0.1 \text{ A}$	-	0.7	1.5	V
		$V_{D} = 400 \text{ V}$ : $I_{T} = 0.1 \text{ A}$ : $T_{i} = 125 \text{ °C}$	0.25	0.4	-	V
I <sub>D</sub>	Off-state leakage current	$V_{D} = V_{DRM(max)}; T_{j} = 125 °C$	-	0.1	0.5	mA

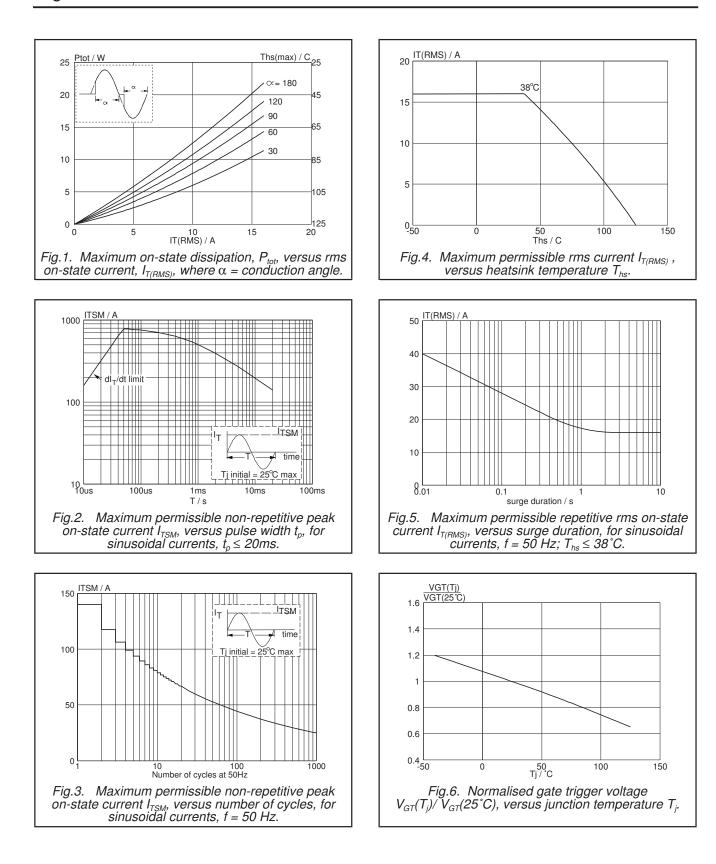
## **DYNAMIC CHARACTERISTICS**

 $T_i = 25$  °C unless otherwise stated

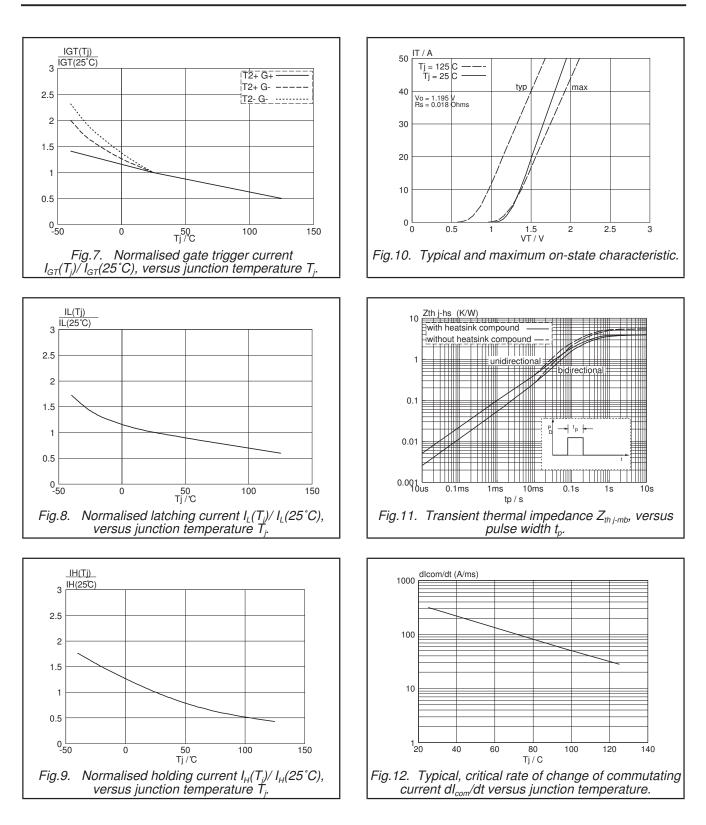
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
	Critical rate of rise of	$V_{DM} = 67\% V_{DRM(max)}; T_j = 125 \ ^{\circ}C;$	1000	4000	-	V/µs
dl <sub>com</sub> /dt	off-state voltage Critical rate of change of commutating current	exponential waveform; gate open circuit $V_{DM} = 400 \text{ V}; T_j = 125 ^{\circ}\text{C}; I_{T(RMS)} = 16 \text{ A};$ without snubber; gate open circuit	-	28	-	A/ms
t <sub>gt</sub>	Gate controlled turn-on time	$I_{TM} = 20 \text{ A}; V_D = V_{DRM(max)}; I_G = 0.1 \text{ A};$ $dI_G/dt = 5 \text{ A}/\mu \text{s}$	-	2	-	μs

**<sup>2</sup>** Device does not trigger in the T2-, G+ quadrant.

## BTA216X series B

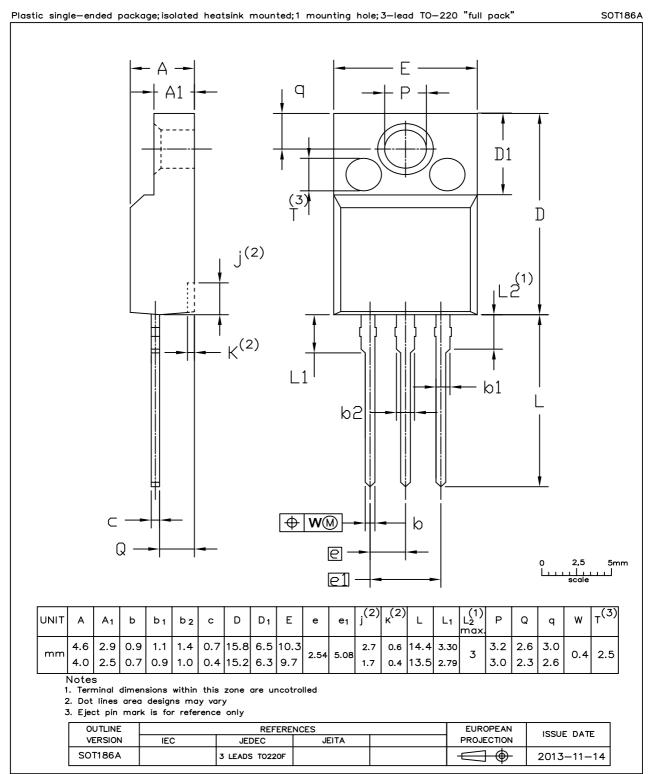


## BTA216X series B



## BTA216X series B

## **MECHANICAL DATA**



## Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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