

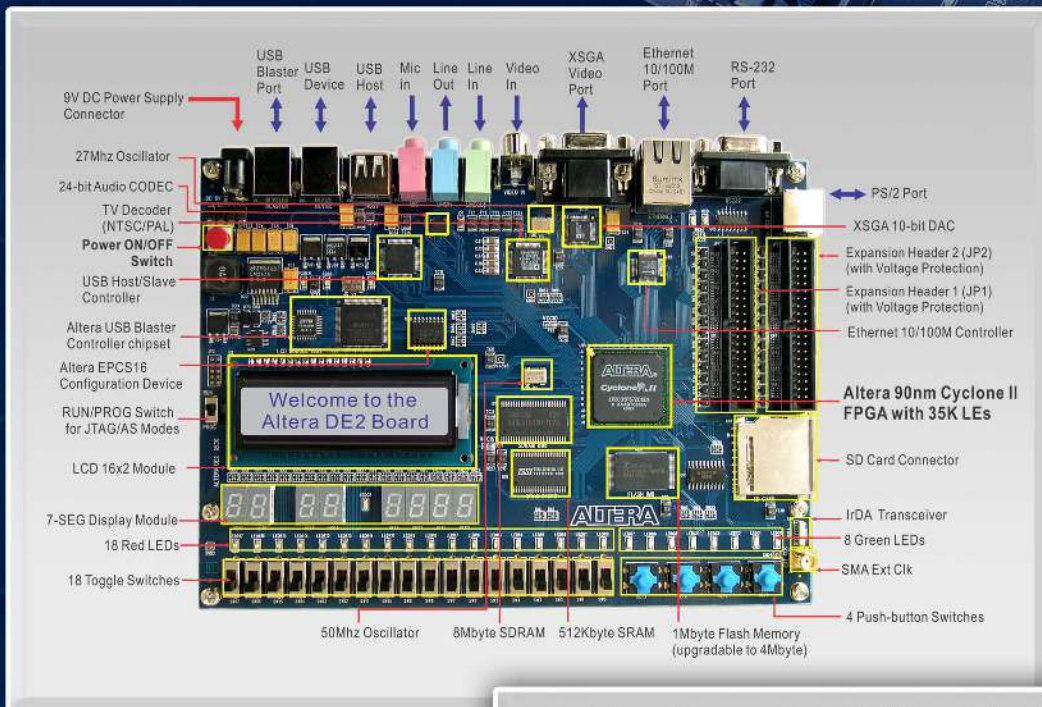
ALTERA DE2

Development and Education Board Learning through Innovation

Cyclone II →

Nios II →

USB
UNIVERSAL SERIAL BUS →



Manufactured by Terasic Exclusively for Altera

Development and Education Board

- Altera Cyclone II (2C35) FPGA with 35,000 LEs
- 16Mbit Serial Configuration Device for AS mode
- Built-in USB Blaster with Enhanced API Link IP
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- 24-bit CD-Quality Audio CODEC
- VGA DAC (10-bit DAC)
- USB Host and Device
- Ethernet 100/10Mbps
- SRAM, SDRAM, Flash, SD Card Connector
- RS232, IrDA, PS/2
- 16x2 LCD Panel

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