

#### Is Now Part of



# ON Semiconductor®

# To learn more about ON Semiconductor, please visit our website at <a href="https://www.onsemi.com">www.onsemi.com</a>

Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (\_), the underscore (\_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (\_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at <a href="www.onsemi.com">www.onsemi.com</a>. Please email any questions regarding the system integration to Fairchild <a href="guestions@onsemi.com">guestions@onsemi.com</a>.

ON Semiconductor and the ON Semiconductor logo are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any EDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officer



## FDMA520PZ

#### June 2014

# Single P-Channel PowerTrench<sup>®</sup> MOSFET -20V, -7.3A, $30m\Omega$

#### **Features**

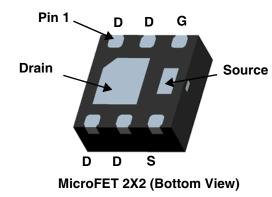
- Max  $r_{DS(on)} = 30m\Omega$  at  $V_{GS} = -4.5V$ ,  $I_D = -7.3A$
- Max  $r_{DS(on)}$  = 53m $\Omega$  at  $V_{GS}$  = -2.5V,  $I_D$  = -5.5A
- Low profile 0.8mm maximum in the new package MicroFET 2X2 mm
- HBM ESD protection level > 3kV typical (Note 3)
- Free from halogenated compounds and antimony oxides
- RoHS Compliant

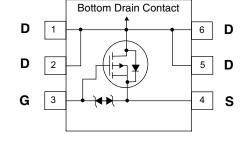


### **General Description**

This device is designed specifically for battery charge or load switching in cellular handset and other ultraportable applications. It features a MOSFET with low on-state resistance.

The MicroFET 2X2 package offers exceptional thermal performance for its physical size and is well suited to linear mode applications.





#### MOSFET Maximum Ratings T<sub>A</sub> = 25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V <sub>DS</sub>	Drain to Source Voltage		-20	V
$V_{GS}$	Gate to Source Voltage		±12	V
	Drain Current -Continuous	(Note 1a)	-7.3	_
ם'ם	-Pulsed		-24	Α
Б	Power Dissipation	(Note 1a)	2.4	10/
P <sub>D</sub>	Power Dissipation	(Note 1b)	0.9	W
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range		-55 to +150	°C

#### **Thermal Characteristics**

I	$R_{ heta JA}$	Thermal Resistance, Junction to Ambient	(Note 1a)	52	°C/W
F	$R_{ heta JA}$	Thermal Resistance, Junction to Ambient	(Note 1b)	145	C/VV

#### **Package Marking and Ordering Information**

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
520	FDMA520PZ	MicroFET 2X2	7"	8mm	3000 units

# **Electrical Characteristics** $T_J = 25^{\circ}C$ unless otherwise noted

Parameter

Off Characteristics							
BV <sub>DSS</sub>	Drain to Source Breakdown Voltage	$I_D = -250 \mu A, V_{GS} = 0V$	-20			V	
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = –250μA, referenced to 25°C		-8.4		mV/°C	
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS} = -16V, V_{GS} = 0V$			-1	μΑ	
I <sub>GSS</sub>	Gate to Source Leakage Current	$V_{GS} = \pm 12V, V_{DS} = 0V$			±10	μΑ	

**Test Conditions** 

Min

Тур

Max

Units

#### On Characteristics

Symbol

V <sub>GS(th)</sub>	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = -250 \mu A$	-0.6	-1.1	-1.5	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate to Source Threshold Voltage Temperature Coefficient	I <sub>D</sub> = -250μA, referenced to 25°C		3.5		mV/°C
		$V_{GS} = -4.5V$ , $I_D = -7.3A$		26	30	
r <sub>DS(on)</sub>	Static Drain to Source On Resistance	$V_{GS} = -2.5V$ , $I_D = -5.5A$		42	53	mΩ
. ,		$V_{GS} = -4.5V$ , $I_D = -7.3A$ , $T_J = 125$ °C		36	55	
9 <sub>FS</sub>	Forward Transconductance	$V_{DS} = -5V, I_{D} = -7.3A$		22		S

#### **Dynamic Characteristics**

C <sub>iss</sub>	Input Capacitance	V - 40V V - 0V	1235	1645	pF
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> = -10V, V <sub>GS</sub> = 0V, f = 1MHz	255	340	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	I - IIVII IZ	225	340	pF

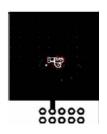
#### **Switching Characteristics**

t <sub>d(on)</sub>	Turn-On Delay Time		10	20	ns
t <sub>r</sub>	Rise Time	$V_{DD} = -10V, I_{D} = -7.3A$ $V_{GS} = -4.5V, R_{GEN} = 6\Omega$	29	47	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	V <sub>GS</sub> 4.5V, R <sub>GEN</sub> - 602	83	133	ns
t <sub>f</sub>	Fall Time		74	119	ns
$Q_g$	Total Gate Charge		14	20	nC
Q <sub>gs</sub>	Gate to Source Gate Charge	$V_{DD} = -5V, I_{D} = -7.3A$ $V_{GS} = -4.5V$	2.9		nC
$Q_{gd}$	Gate to Drain "Miller" Charge	GS4.5V	4.4		nC

#### **Drain-Source Diode Characteristics**

Is	Maximum Continuous Drain-Source Diode	Maximum Continuous Drain-Source Diode Forward Current			-2	Α
$V_{SD}$	Source to Drain Diode Forward Voltage V <sub>GS</sub> = 0V, I <sub>S</sub> = -2A			-0.8	-1.2	V
t <sub>rr</sub>	Reverse Recovery Time	1 - 7.24 di/dt = 1004/		30	45	ns
Q <sub>rr</sub>	Reverse Recovery Charge	- I <sub>F</sub> =–7.3A, di/dt = 100A/μs		22	33	nC

1: R<sub>0,JA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins.



a. 52°C/W when mounted on a 1 in<sup>2</sup> pad of 2 oz copper



b.145°C/W when mounted on a minimum pad of 2 oz copper

- 2: Pulse Test: Pulse Width < 300µs, Duty cycle < 2.0%.
  3: The diode connected between the gate and the source serves only as protection against ESD. No gate overvoltage rating is implied.

## Typical Characteristics T<sub>J</sub> = 25°C unless otherwise noted

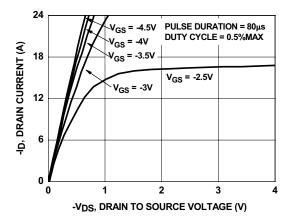


Figure 1. On-Region Characteristics

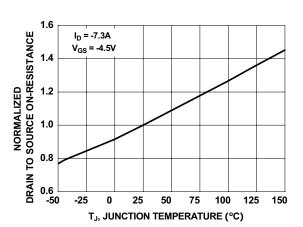


Figure 3. Normalized On-Resistance vs Junction Temperature

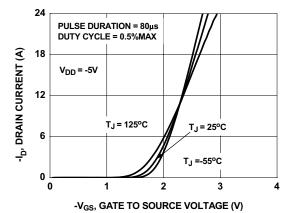


Figure 5. Transfer Characteristics

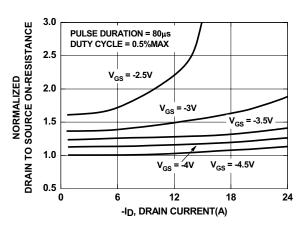


Figure 2. Normalized On-Resistance vs Drain Current and Gate Voltage

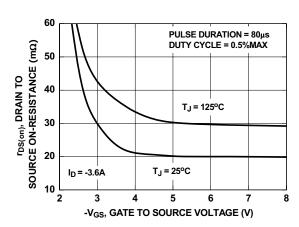


Figure 4. On-Resistance vs Gate to Source Voltage

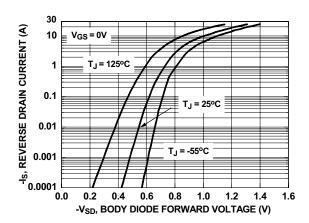


Figure 6. Source to Drain Diode Forward Voltage vs Source Current

# Typical Characteristics $T_J = 25^{\circ}C$ unless otherwise noted

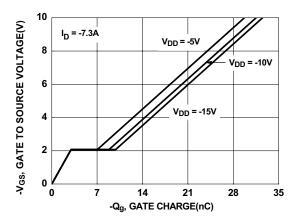


Figure 7. Gate Charge Characteristics

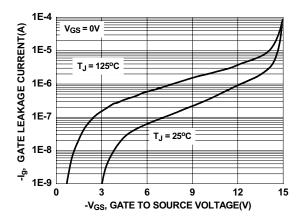


Figure 9. Gate Leakage Current vs Gate to Source Voltage

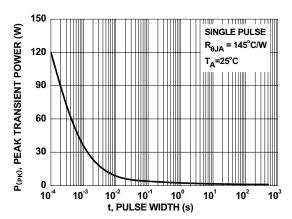


Figure 11. Single Pulse Maximum Power Dissipation

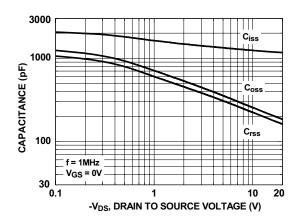


Figure 8. Capacitance vs Drain to Source Voltage

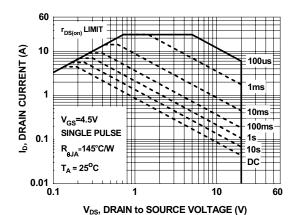


Figure 10. Forward Bias Safe Operating Area

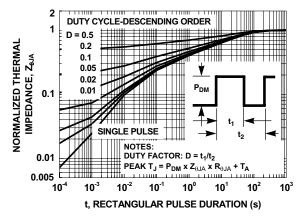
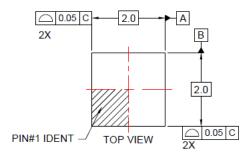
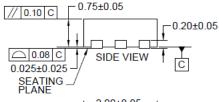
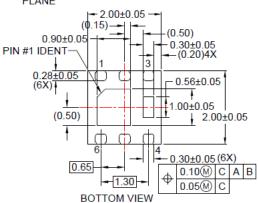


Figure 12. Transient Thermal Response Curve

#### **Dimensional Outline and Pad Layout**

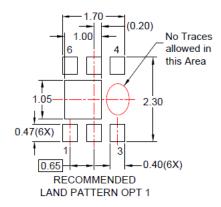


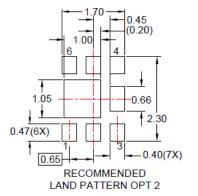




#### NOTES:

- A. PACKAGE DOES NOT FULLY CONFORM TO JEDEC MO-229 REGISTRATION
- B. DIMENSIONS ARE IN MILLIMETERS.
- C. DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 2009.
- D. LAND PATTERN RECOMMENDATION IS EXISTING INDUSTRY LAND PATTERN.
- E. DRAWING FILENAME: MKT-MLP06Lrev4.







Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings: http://www.fairchildsemi.com/package/packageDetails.html?id=PN\_MLDEB-C06





#### **TRADEMARKS**

The following includes registered and unregistered trademarks and service marks, owned by Fairchild Semiconductor and/or its global subsidiaries, and is not intended to be an exhaustive list of all such trademarks.

AccuPower<sup>TM</sup>
AX-CAP<sup>®</sup>\*
BitSiC<sup>TM</sup>
Build it Now<sup>TM</sup>
CorePLUS<sup>TM</sup>
CorePOWER<sup>TM</sup>
CROSSVOLT<sup>TM</sup>

CTL™
Current Transfer Logic™
DEUXPEED®
Dual Cool™
EcoSPARK®
EfficentMax™
ESBC™

Fairchild®
Fairchild Semiconductor®
FACT Quiet Series™
FACT®
FAST®
FastvCore™
FETBench™
FPS™

F-PFS™ FRFET<sup>®</sup>

Global Power Resource<sup>SM</sup> GreenBridge<sup>™</sup>

Green FPS™ Green FPS™ e-Series™

Gmax<sup>™</sup> GTO™ IntelliMAX<sup>™</sup> ISOPLANAR<sup>™</sup>

Marking Small Speakers Sound Louder and Better™

and Better™ MegaBuck™ MICROCOUPLER™

MicroFET™ MicroPak™ MicroPak2™ MillerDrive™ MotionMax™

MotionMax<sup>TM</sup>
mWSaver<sup>®</sup>
OptoHiT<sup>TM</sup>
OPTOLOGIC<sup>®</sup>
OPTOPLANAR<sup>®</sup>

® PowerTrench® PowerXS™

Programmable Active  $Droop^{TM}$   $OFFT^{®}$ 

QS<sup>TM</sup>
Quiet Series<sup>TM</sup>
RapidConfigure<sup>TM</sup>

Saving our world, 1mW/W/kW at a time™ SignalWise™ SmartMax™ SMART START™

Solutions for Your Success™

SPM<sup>®</sup>
STEALTH™
SuperFET®
SuperSOT™-3

SuperSOT™-6 SuperSOT™-8 SupreMOS® SyncFET™ Sync-Lock™ SYSTEM ®\*
GENERAL
TinyBoost®
TinyBuck®
TinyCalc™
TinyLogic®
TINYOPTO™
TinyPower™
TinyPWM™
TinyPWMI™
TinyPWIT™
TranSiC™
TriFault Detect™
TRUECURRENT®\*

SerDes\*
UHC<sup>®</sup>
Ultra FRFET™
UniFET™
VCX™
VisualMax™
VoltagePlus™
XS™
仙童™

μSerDes™

\*Trademarks of System General Corporation, used under license by Fairchild Semiconductor.

#### DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

#### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used here in:

- Life support devices or systems are devices or systems which, (a) are
  intended for surgical implant into the body or (b) support or sustain life,
  and (c) whose failure to perform when properly used in accordance with
  instructions for use provided in the labeling, can be reasonably
  expected to result in a significant injury of the user.
- A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

#### ANTI-COUNTERFEITING POLICY

Fairchild Semiconductor Corporation's Anti-Counterfeiting Policy. Fairchild's Anti-Counterfeiting Policy is also stated on our external website, www.Fairchildsemi.com, under Sales Support.

Counterfeiting of semiconductor parts is a growing problem in the industry. All manufactures of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed application, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers by either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handing and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address and warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors.

# PRODUCT STATUS DEFINITIONS Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete Not In Production		Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

Rev. 168

ON Semiconductor and in are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at <a href="www.onsemi.com/site/pdf/Patent-Marking.pdf">www.onsemi.com/site/pdf/Patent-Marking.pdf</a>. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hol

#### **PUBLICATION ORDERING INFORMATION**

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800-282-9855 Toll Free USA/Canada
Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910

Japan Customer Focus Center Phone: 81–3–5817–1050 ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative