AUTOMOTIV

RoHS

COMPLIANT

HALOGEN FREE

GREEN

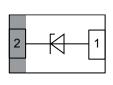
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Vishay Semiconductors

Ultra Low Capacitance Unidirectional Single Line ESD Protection Diode in DFN1006-2A





MARKING (example only)



Bar = cathode marking X = date code YY = type code (see table below)

LINKS TO ADDITIONAL RESOURCES





FEATURES

- Ultra compact DFN1006-2A package
- Low package height < 0.5 mm
- 1-line ESD protection
- AEC-Q101 qualified available
- Working range 5.5 V
- Low leakage current < 0.01 μA
- Ultra low load capacitance C_D = 0.7 pF typ.
- ESD immunity acc. IEC 61000-4-2
 ± 16 kV contact discharge
 - ± 16 kV air discharge
- e3 Sn

Tin plated exposed side wall of lead frame

- Soldering can be checked by standard vision inspection
- AOI = Automated Optical Inspection
- No X-ray necessary
- · Lead material: Cu
- PATENT(S): www.vishav.com/patents
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

ORDERING INFORMATION						
PART NUMBER (EXAMPLE)	ENVIRONMENTAL AND QUALITY CODE			PACKAGING CODE		
	AEC-Q101 QUALIFIED	Rohs-Compliant + Lead (Pb)-free terminations	TIN PLATED	10K PER 7" REEL (8 mm TAPE)	ORDERING CODE (EXAMPLE)	
		GREEN	PLATED	10K = MOQ		
VBUS05N1-HD1	-	G	3	-08	VBUS05N1-HD1-G3-08	
VBUS05N1-HD1	Н	G	3	-08	VBUS05N1-HD1HG3-08	

PACKAGE DATA						
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	SOLDERING CONDITIONS		
VBUS05N1-HD1	DFN1006-2A	5A	0.83 mg	Peak temperature max. 260 °C		

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	Acc. IEC 61000-4-5, 8/20 μs/single shot	I _{PPM}	4	Α		
Peak pulse power	Pin 1 to pin 2 Acc. IEC 61000-4-5; $t_p = 8/20 \mu s$; single shot	P _{PP}	60	W		
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V	± 16	kV		
ESD IIIIIIIIIIIII	Air discharge acc. IEC 61000-4-2; 10 pulses	V _{ESD}	± 16			
Operating temperature	Junction temperature	TJ	-55 to +150	°C		
Storage temperature		T _{stg}	-65 to +150	°C		

PATENT(S): www.vishay.com/patents

Rev. 1.4, 06-Mar-2023

This Vishay product is protected by one or more United States and international patents.

ESD PROTECTION FOR HIGH-SPEED SIGNAL OR DATA LINES

The VBUS05N1-HD1 is a Unidirectional ESD protection device which clamps positive and negative overvoltage transients to ground. Connected between the signal or data line and the ground the VBUS05N1-HD1 offers a high isolation (low leakage current, low capacitance) within the specified working range. Due to the short leads and small package size of the tiny DFN1006 package the line inductance is very low, so that fast transients like an ESD strike can be clamped with minimal over- or undershoots. Due to the very low capacitance the VBUS05N1-HD1 can be used for high speed data ports like HDMI, USB 3.0 or Thunderbolt.

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Protection paths	Number of lines which can be protected	N _{channel}	-	-	1	lines
Reverse stand-off voltage	Max. reverse working voltage	V_{RWM}	-	-	5.5	V
Reverse voltage	At I _R = 0.1 μA	V_R	5.5	-	-	V
Reverse current	At V _{RWM} = 5.5 V	I _R	-	-	0.1	μA
Reverse breakdown voltage	At I _R = 1 mA	V_{BR}	7.0	7.8	9.0	V
Payaraa alamping valtaga	At I _{PP} = 1 A	V _C	-	9.5	11	V
Reverse clamping voltage	At I _{PP} = I _{PPM} = 4 A	V _C	-	13	15	V
Canacitanas	At $V_R = 0 V$; $f = 1 MHz$	C_D	-	0.7	0.8	pF
Capacitance	At V _R = 3.3 V; f = 1 MHz	C _D	-	0.7	-	pF
Clamping voltage	Transmission Line Pulse (TLP); $t_p = 100 \text{ ns}$ $I_{TLP} = 8 \text{ A}$	V _{C-TLP}	-	15	-	V
Clamping voltage	Transmission Line Pulse (TLP); $t_p = 100 \text{ ns}$ $I_{TLP} = 16 \text{ A}$		-	22	-	
Dynamic resistance	Transmission Line Pulse (TLP); t _p = 100 ns	R _{DYN}	ı	0.8	-	Ω

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

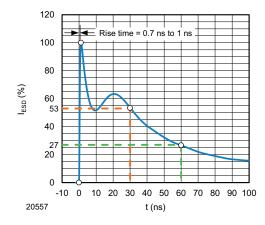


Fig. 1 - ESD Discharge Current Wave Form acc. IEC 61000-4-2 (330 Ω /150 pF)

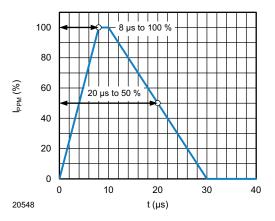
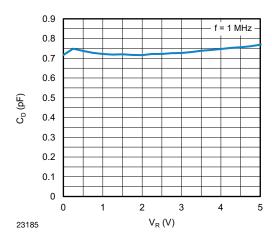


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5



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Fig. 3 - Typical Capacitance vs. Reverse Voltage

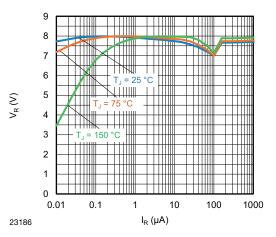


Fig. 4 - Typical Reverse Voltage vs. Reverse Current

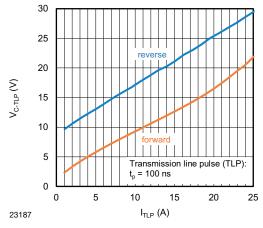


Fig. 5 - Typical Clamping Voltage vs. Peak Pulse Current

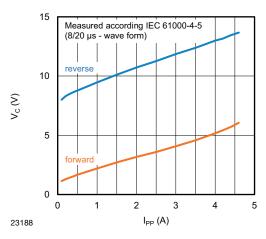


Fig. 6 - Typical Peak Clamping Voltage vs. Peak Pulse Current

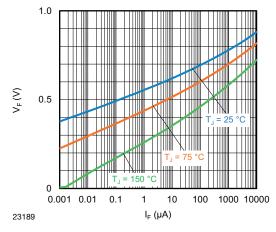
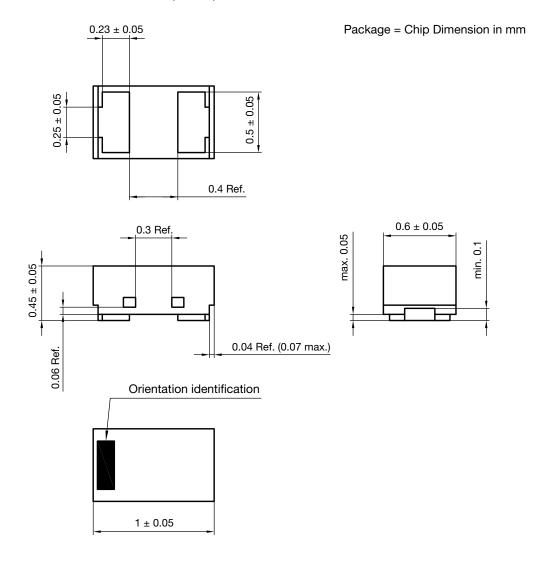
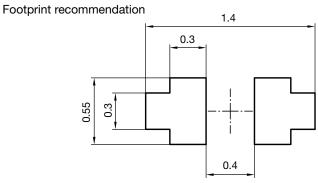


Fig. 7 - Typical Forward Voltage vs. Forward Current



PACKAGE DIMENSIONS in millimeters (inches): DFN1006-2A

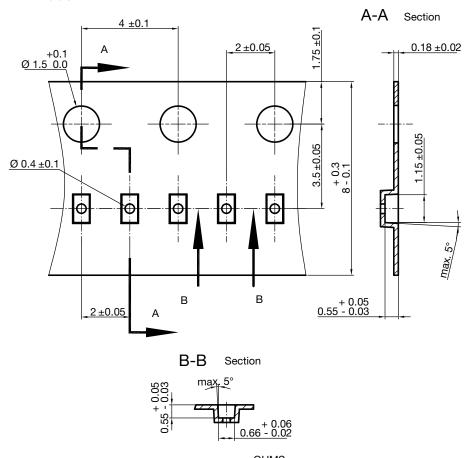




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CARRIER TAPE DFN1006-2A



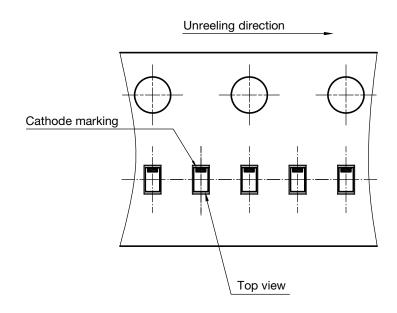
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S8-V-3906.04-064 (4)

created 28.10.2019

surface resistance: 10^5 - $10^{11} \frac{OHMS}{SQ}$ Cummulative tolerances of 10 sprocket holes is ± 0.2 mm

ORIENTATION IN CARRIER TAPE DFN1006-2A





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