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93772AFLFT

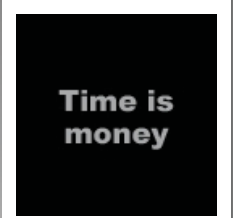
Category: Zero Delay Buffers

Generic Part: 93772

Market Group: PC CLOCK

Description: PC BUFFER

Product Description/Features:
 • Low skew, low jitter PLL clock driver
 • Max frequency supported = 266MHz (DDR 533)
 • I²C for functional and output control
 • Feedback pins for input to output synchronization
 • Spread Spectrum tolerant inputs
 • 3.3V toerant CLK_INT input



Parameters

Package	SSOP 28 (PYG28)
Voltage	3.3 V
Package	SSOP 28
Speed	NA
Temperature	C
Status	Active
Sample	No
Minimum Order Quantity	1000
Factory Order Increment	1000

Distributor Inventory

No Pricing information is available from our Distributors at this time.

Documents

Type	Title	Size	Revision Date
Misc	PC Clocks Contact Info	61 KB	05/29/2007

Package

Description	SSOP 5.3 MM
Class	PLASTIC
Moisture Sensitivity Level (MSL)	1
Category	Green
Moisture Exposure Floor Life	Unlimited @ <30°C/85% RH
Peak Reflow Temperature	260°C
Rebake Conditions	NA
Length	10.2
Mark	PYG
Width	5.3
Pitch	0.65
Thickness	1.73
Status	Active