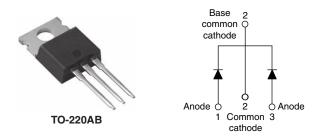


Vishay Semiconductors

Schottky Rectifier, 2 x 15 A



PRODUCT SUMMARY					
Package	TO-220AB				
I _{F(AV)}	2 x 15 A				
V _R	45 V				
V _F at I _F	See Electrical table				
I _{RM} max.	100 mA at 125 °C				
T _J max.	150 °C				
Diode variation	Common cathode				
E _{AS}	10 mJ				

FEATURES

- 150 °C T_J operation
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

DESCRIPTION

This center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	YMBOL CHARACTERISTICS VALUES					
I _{F(AV)}	Rectangular waveform (per device)	30	А			
V _{RRM}		35/45	V			
I _{FRM}	$T_{\rm C} = 123 \ ^{\circ}{\rm C}$ (per leg)	30	۸			
I _{FSM}	t _p = 5 μs sine	1020	А			
V _F	20 A _{pk} , T _J = 125 °C	0.6	V			
TJ	Range	- 65 to 150	°C			

VOLTAGE RATINGS						
PARAMETER SYMBOL VS-MBR3045CTPbF VS-MBR3045CT-N3 UNITS						
Maximum DC reverse voltage	V _R	45	45	V		
Maximum working peak reverse voltage V _{RWM} 45 45 V						

ABSOLUTE MAXIMUM RATINGS							
PARAMETER		SYMBOL	TEST COM	TEST CONDITIONS			
Maximum average	per leg		T _C = 123 °C, rated V _B		15		
forward current	per device	I _{F(AV)}	$T_{\rm C} = 123$ C, lated $V_{\rm R}$		30		
Peak repetitive forward curre	ent per leg	I _{FRM}	Rated V _R , square wave, 20	kHz, T _C = 123 °C	30		
Non-repetitive peak surge current		I _{ESM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	1020	A	
		1 OW	Surge applied at rated load conditions halfwave, single phase, 60 Hz		200		
Non-repetitive avalanche en	ergy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 2 A, L = 5 mH		10	mJ	
Repetitive avalanche current	per leg	I _{AR}	Current decaying linearly to Frequency limited by T _J ma	2	А		

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1



RoHS

COMPLIANT

HALOGEN

FREE



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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONE	VALUES	UNITS		
		30 A	T _J = 25 °C	0.76		
Maximum forward voltage drop	V _{FM} ⁽¹⁾	20 A	T _J = 125 °C	0.6	V	
		30 A	1j = 125 C	0.72		
Maximum instantaneous reverse current	I _{RM} ⁽¹⁾	T _J = 25 °C	Dated DC voltage	1	mA	
Maximum instantaneous reverse current		T _J = 125 °C	Rated DC voltage	100		
Threshold voltage	V _{F(TO)}	$T_{i} = T_{i}$ maximum		0.29	V	
Forward slope resistance	r _t	ij = ij maximum		13.6	mΩ	
Maximum junction capacitance	CT	$V_R = 5 V_{DC}$ (test signal range	800	pF		
Typical series inductance	L _S	Measured from top of termin	8.0	nH		
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs	

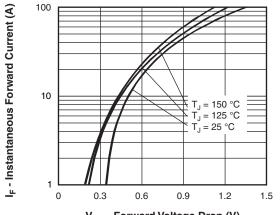
Note

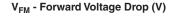
 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction temperature range	TJ		- 65 to 150	°C			
Maximum storage temperature range	T _{Stg}		- 65 to 175				
Maximum thermal resistance, junction to case per leg	R _{thJC}	DC operation	1.5				
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased Only for TO-220	0.50	°C/W			
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation For D ² PAK and TO-262	50				
Approximate weight			2	g			
Approximate weight			0.07	OZ.			
Mounting torque		Non-lubricated threads	6 (5)	kgf ⋅ cm			
Mounting torque maximum	1	Non-Iudricated threads	12 (10)	(lbf · in)			
Marking device		Case style TO-220AB	MBR3	045CT			



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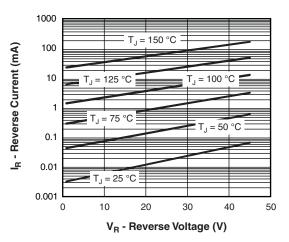


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

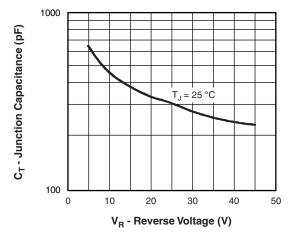


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

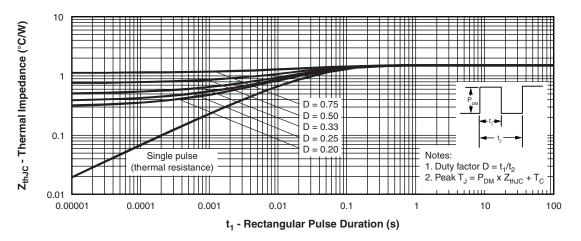
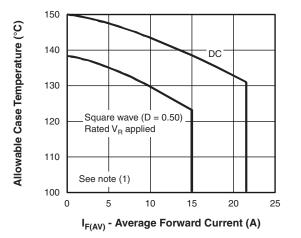


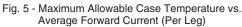
Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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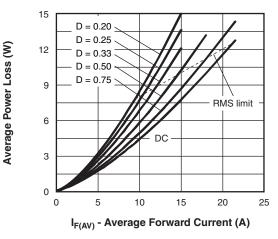


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

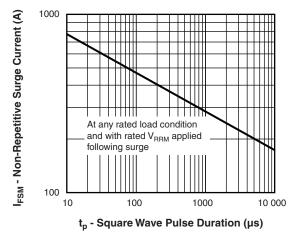


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
 - $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{Rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$



Vishay Semiconductors

ORDERING INFORMATION TABLE

Device cod

е	VS-	MBR	30	45	СТ	PbF
	1	2	3	4	5	6

- Vishay Semiconductors product 1
- 2 Schottky MBR series
- 3 4 5 6 Current rating (30 = 30 A)
 - _ Voltage ratings (045 = 45 V)
 - CT = Essential part number _
 - Environmental digit
 - PbF = Lead (Pb)-free and RoHS compliant
 - -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-MBR3045CTPbF	50	1000	Antistatic plastic tube				
VS-MBR3045CT-N3	50	1000	Antistatic plastic tube				

LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95222				
Part marking information	TO-220AB PbF	www.vishay.com/doc?95225		
Part marking information	TO-220AB -N3	www.vishay.com/doc?95028		



Vishay Semiconductors

3 x b

3 x b2

Detail B

(b, b2)

b1. b3 Section C - C and D - D

L1 (2)

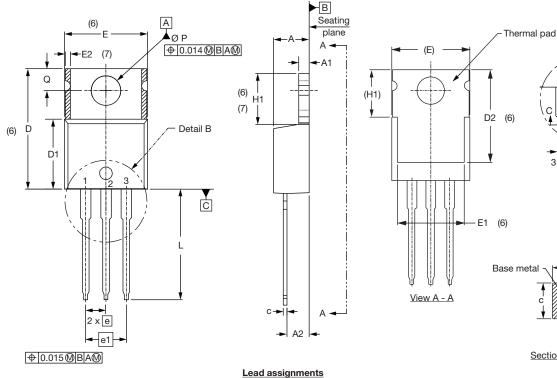
- Plating

c1 (4)

(4)

TO-220AB

DIMENSIONS in millimeters and inches



Lead tip

- **Diodes**
- 1. Anode/open 2. - Cathode 3. - Anode

SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
A	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

Notes

- ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
- ⁽²⁾ Lead dimension and finish uncontrolled in L1
- ⁽³⁾ Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed $0.127 \text{ mm} (0.005^{\circ})$ per side. These dimensions are measured at the outermost extremes of the plastic body
- $^{\left(4\right) }$ Dimension b1, b3 and c1 apply to base metal only
- ⁽⁵⁾ Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1

Conforms to JEDEC outline TO-220AB

SYMBOL	MILLIN	IETERS	INCHES		NOTES
STIVIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
E	10.11	10.51	0.398	0.414	3, 6
E1	6.86	8.89	0.270	0.350	6
E2	-	0.76	-	0.030	7
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	
θ	90° to 93°		90° t	o 93°	

(7) Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed

Outline conforms to JEDEC TO-220, except A2 (maximum) and (8) D2 (minimum) where dimensions are derived from the actual package outline

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