

RGWS80TS65D

650V 40A Field Stop Trench IGBT

V _{CES}	650V
I _{C (100°C)}	40A
V _{CE(sat) (Typ.)}	1.6V
P_D	202W

Outline TO-247GE (1) (2)(3)

Features

- 1) Low Collector Emitter Saturation Voltage
- 2) High Speed Switching
- 3) Low Switching Loss & Soft Switching
- 4) Built in Very Fast & Soft Recovery FRD
- 5) Pb free Lead Plating; RoHS Compliant

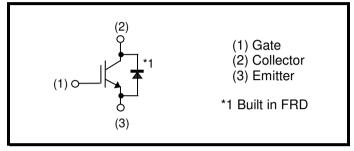
Application

PFC

Solar converters

Mid to high switching frequency converters

●Inner Circuit



Packaging Specifications

Tape Width (m	Packaging	Tube
	Reel Size (mm)	-
	Tape Width (mm)	-
	Basic Ordering Unit (pcs)	600
	Packing Code	C13
	Marking	RGWS80TS65D

● Absolute Maximum Ratings (at T_C = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		$V_{\sf CES}$	650	V
Gate - Emitter Voltage		V_{GES}	±30	V
Collector Current	T _C = 25°C	I _C	71	Α
Collector Gurrent	T _C = 100°C	I _C	43	Α
Pulsed Collector Current		I _{CP} *1	120	Α
Diode Forward Current	T _C = 25°C	I _F	23	Α
	T _C = 100°C	I _F	13	Α
Diode Pulsed Forward Current		I _{FP} *1	60	Α
Pawar Dissination	T _C = 25°C	P _D	202	W
Power Dissipation	T _C = 100°C	P_{D}	101	W
Operating Junction Temperature		T _j	-40 to +175	°C
Storage Temperature		T _{stg}	-55 to +175	°C

^{*1} Pulse width limited by T_{imax}.

●Thermal Resistance

Parameter	Symbol	Values			Unit
r arameter	Symbol	Min.	Тур.	Max.	Offic
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	-	0.74	°C/W
Thermal Resistance Diode Junction - Case	$R_{\theta(j-c)}$	-	ı	2.88	°C/W

●IGBT Electrical Characteristics (at T_j = 25°C unless otherwise specified)

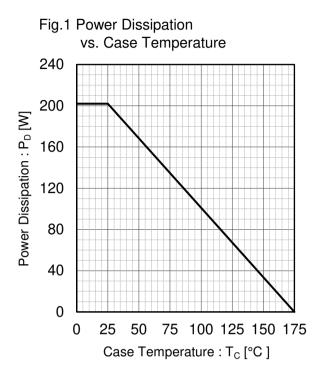
Parameter	Symbol	Conditions	Values			Unit
Farameter	Syllibol	Conditions	Min.	Тур.	Max.	Offic
Collector - Emitter Breakdown Voltage	BV _{CES}	$I_{C} = 10 \mu A, V_{GE} = 0 V$	650	-	-	V
Collector Cut - off Current	I _{CES}	$V_{CE} = 650V, V_{GE} = 0V$	-	-	10	μΑ
Gate - Emitter Leakage Current	I _{GES}	$V_{GE} = \pm 30V$, $V_{CE} = 0V$	ı	ı	±200	nA
Gate - Emitter Threshold Voltage	$V_{GE(th)}$	$V_{CE} = 5V, I_{C} = 20.0 \text{mA}$	5.0	6.0	7.0	V
Collector - Emitter Saturation Voltage	V _{CE(sat)}	$I_{C} = 40A, V_{GE} = 15V,$ $T_{j} = 25^{\circ}C$ $T_{j} = 175^{\circ}C$	-	1.6 2.0	2.0	V

●IGBT Electrical Characteristics (at T_j = 25°C unless otherwise specified)

Parameter	Symbol	Symbol Conditions -	Values			Unit
Parameter	Symbol		Min.	Тур.	Max.	Offic
Input Capacitance	C _{ies}	$V_{CE} = 30V$,	-	2530	-	
Output Capacitance	C _{oes}	$V_{GE} = 0V$,	-	65	-	рF
Reverse transfer Capacitance	C _{res}	f = 1MHz	-	46	-	
Total Gate Charge	Q_g	$V_{CE} = 400V$,	-	83	-	
Gate - Emitter Charge	Q_ge	$I_C = 40A$,	-	18	-	nC
Gate - Collector Charge	Q_{gc}	V _{GE} = 15V	-	31	-	
Turn - on Delay Time	t _{d(on)}		-	40	-	
Rise Time	t _r	$I_C = 40A, V_{CC} = 400V,$ $V_{GF} = 15V, R_G = 10\Omega,$	-	17	-	ns
Turn - off Delay Time	t _{d(off)}	$T_i = 25^{\circ}C$	-	114	-	
Fall Time	t _f	Inductive Load	-	40	-	
Turn - on Switching Loss	E _{on}	*E _{on} include diode reverse recovery	-	0.70	-	I
Turn - off Switching Loss	E _{off}		-	0.66	-	- mJ
Turn - on Delay Time	t _{d(on)}		-	38	-	
Rise Time	t _r	$I_C = 40A, V_{CC} = 400V,$	-	18	-	no
Turn - off Delay Time	t _{d(off)}	$V_{GE} = 15V, R_G = 10\Omega,$ $T_i = 175^{\circ}C$	-	127	-	ns
Fall Time	t _f	Inductive Load	-	74	-	
Turn - on Switching Loss	E _{on}	*E _{on} include diode reverse recovery	-	0.70	-	I
Turn - off Switching Loss	E _{off}		-	0.84	-	mJ
Reverse Bias Safe Operating Area	RBSOA	$I_C = 120A$, $V_{CC} = 520V$ $V_P = 650V$, $V_{GE} = 15V$ $R_G = 100\Omega$, $T_j = 175^{\circ}C$	FU	LL SQUA	RE	-

•FRD Electrical Characteristics (at $T_j = 25$ °C unless otherwise specified)

Parameter	Cymbol	Conditions	Values			Unit
	Symbol		Min.	Тур.	Max.	Offic
	$I_F = 10A$,	$I_F = 10A$,				
Diode Forward Voltage	V_{F}	$T_j = 25^{\circ}C$	-	1.45	1.9	V
		T _j = 175°C	-	1.4	-	
Diode Reverse Recovery Time	t _{rr}		-	88	-	ns
Diode Peak Reverse Recovery Current	I _{rr}	$I_F = 10A,$ $V_{CC} = 400V,$ $di_F/dt = 200A/\mu s,$ $T_j = 25^{\circ}C$	-	5.9	-	А
Diode Reverse Recovery Charge	Q _{rr}		-	0.28	-	μC
Diode Reverse Recovery Energy	E _{rr}		-	17.6	-	μJ
Diode Reverse Recovery Time	t _{rr}	$I_F = 10A,$ $V_{CC} = 400V,$ $di_F/dt = 200A/\mu s,$ $T_j = 175^{\circ}C$	-	105	-	ns
Diode Peak Reverse Recovery Current	l _{rr}		-	6.9	-	А
Diode Reverse Recovery Charge	Q _{rr}		-	0.42	-	μC
Diode Reverse Recovery Energy	E _{rr}		-	28.8	-	μJ



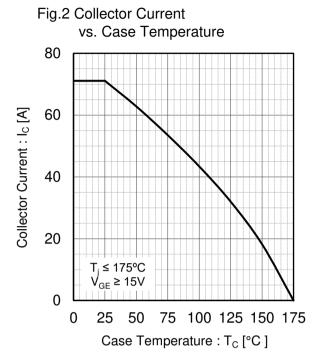


Fig.3 Forward Bias Safe Operating Area

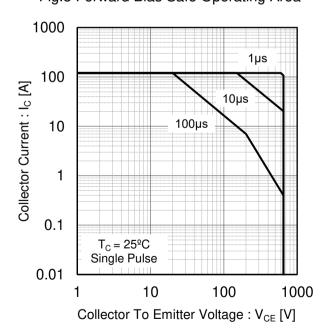


Fig.4 Reverse Bias Safe Operating Area

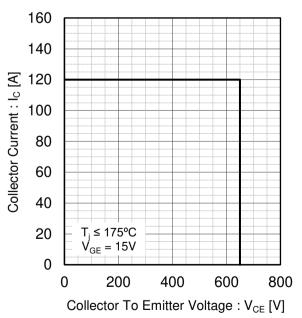


Fig.5 Typical Output Characteristics

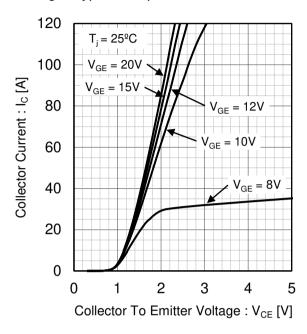


Fig.6 Typical Output Characteristics

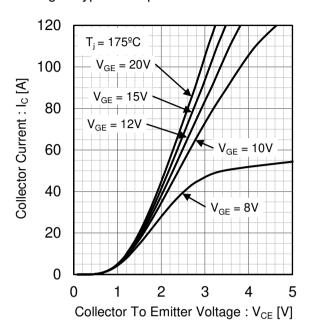


Fig.7 Typical Transfer Characteristics

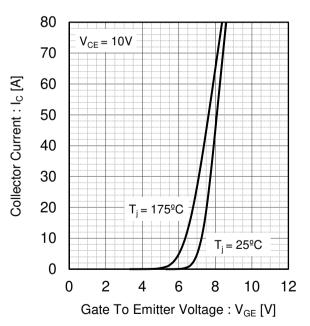
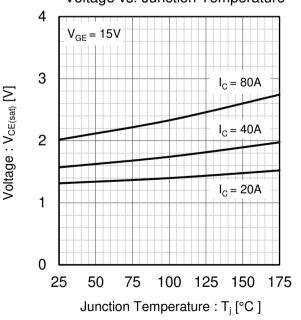


Fig.8 Typical Collector to Emitter Saturation Voltage vs. Junction Temperature



Collector To Emitter Saturation

Fig.9 Typical Collector to Emitter Saturation Voltage vs. Gate to Emitter Voltage

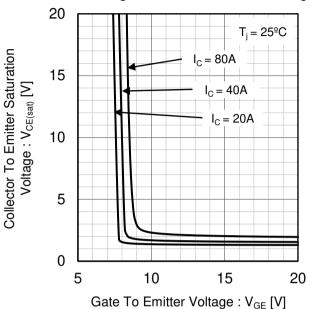


Fig.10 Typical Collector to Emitter Saturation Voltage vs. Gate to Emitter Voltage

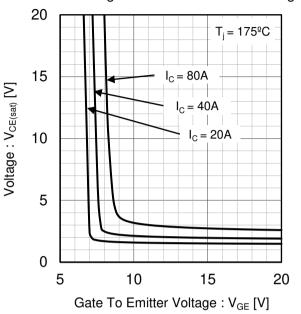


Fig.11 Typical Switching Time vs. Collector Current

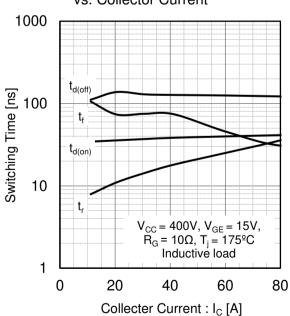
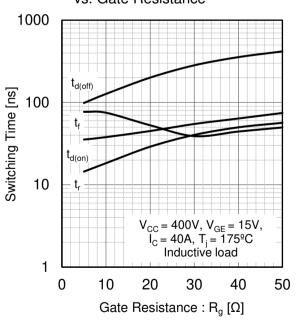


Fig.12 Typical Switching Time vs. Gate Resistance



Collector To Emitter Saturation

Fig.13 Typical Switching Energy Losses vs. Collector Current

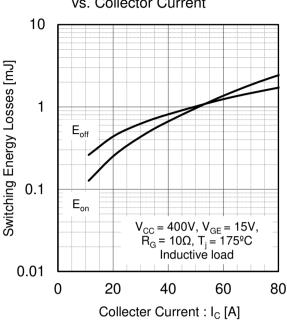


Fig.14 Typocal Switching Energy Losses vs. Gate Resistance

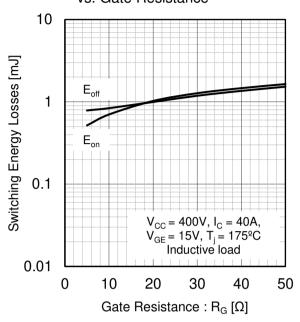


Fig.15 Typical Capacitance vs. Collector to Emitter Voltage

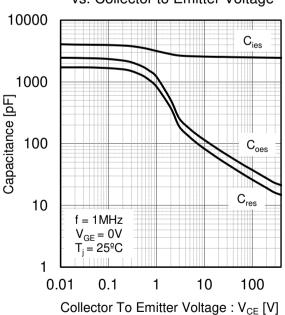


Fig.16 Typical Gate Charge

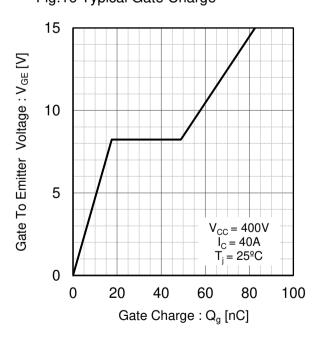
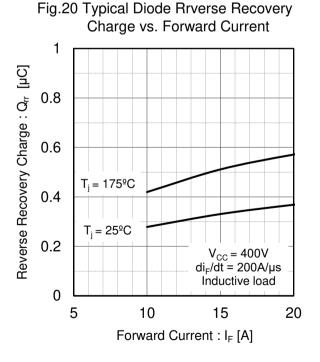


Fig.17 Typical Diode Forward Current vs. Forward Voltage 60 50 Forward Current : I_F [A] 40 30 T_i = 25ºC T_i = 175ºC 20 10 0 2 3 4 0 1 5 Forward Voltage: V_F [V]

vs. Forward Current 200 Reverse Recovery Time : t_{rr} [ns] 150 $T_i = 175^{\circ}C$ 100 $T_i = 25^{\circ}C$ 50 $V_{CC} = 400V$ di_F/dt = 200A/µs Inductive load 0 10 15 5 20 Forward Current : I_F [A]

Fig.18 Typical Diode Revese Recovery Time

Fig.19 Typical Diode Reverse Recovery Current vs. Forward Current 15 Reverse Recovery Current : I_{rr} [A] 10 T_i = 175ºC $T_i = 25^{\circ}C$ 5 $V_{CC} = 400V$ $di_F/dt = 200A/\mu s$ Inductive load 0 5 10 15 20 Forward Current : I_F [A]



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Fig.21 Typical IGBT Transient Thermal Impedance

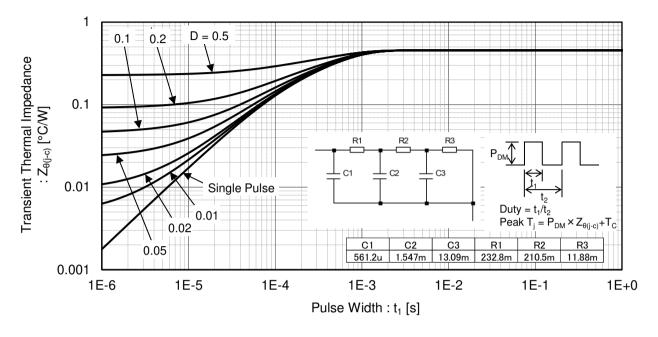
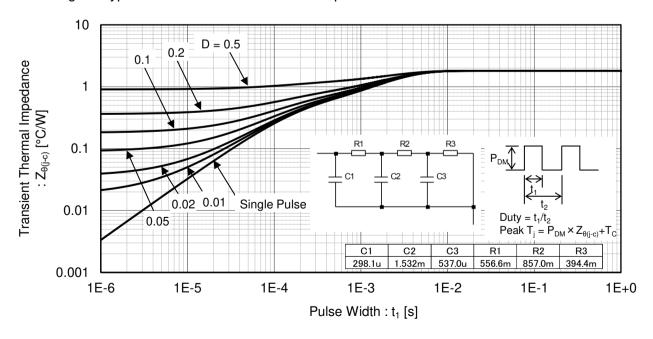


Fig.22 Typical Diode Transient Thermal Impedance



Inductive Load Switching Circuit and Waveform

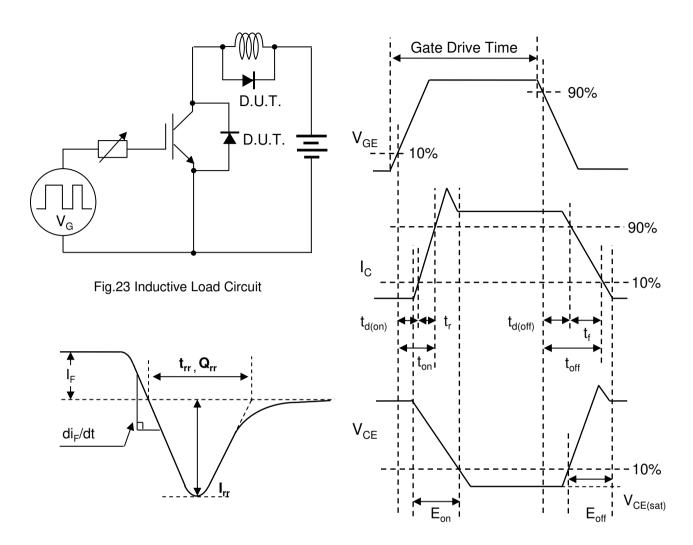


Fig.25 Diode Reverse Recovery Waveform

Fig.24 Inductive Load Waveform

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