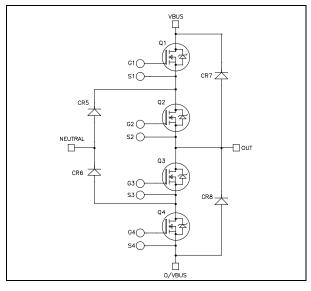
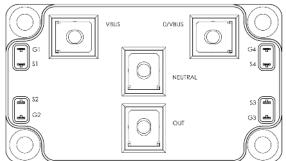


Three level inverter SiC MOSFET Power Module





SiC Power MOSFET:

 $V_{DSS} = 1200V ; R_{DSon} = 12m\Omega @ Tj = 25^{\circ}C$

Application

Uninterruptible Power Supplies

Features

- SiC Power MOSFET
 - Low $R_{DS(on)}$
 - High temperature performance

SiC Schottky Diode

- Zero reverse recovery
- Zero forward recovery
- Temperature Independent switching behavior
- Positive temperature coefficient on VF
- Kelvin emitter for easy drive
- Very low stray inductance
- M5 power connectors
- High level of integration
- AlN substrate for improved thermal performance

Benefits

- Stable temperature behavior
- Very rugged
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Easy paralleling due to positive TC of VCEsat
- Low profile
- **RoHS Compliant**

All ratings (a) $T_i = 25$ °C unless otherwise specified

Q1 to Q4 Absolute maximum ratings (per SiC MOSFET)

Symbol	Parameter	-	Max ratings	Unit
$V_{ m DSS}$	Drain - Source Voltage		1200	V
Ţ	Cantinuana David Comment	$T_c = 25$ °C	219	
I_D	Continuous Drain Current	$T_c = 80$ °C	164	A
I_{DM}	Pulsed Drain current			
V_{GS}	Gate - Source Voltage		-10/+25	V
R_{DSon}	Drain - Source ON Resistance		12	mΩ
P_D	Maximum Power Dissipation	$T_c = 25$ °C	925	W

These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com

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Q1 to Q4 Electrical Characteristics (per SiC MOSFET)

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit	
I_{DSS}	Zero Gate Voltage Drain Current	$V_{GS} = 0V ; V_{DS} = 120$			300	μΑ	
D	Dunin Common on Desistance	$V_{GS} = 20V$	$T_j = 25^{\circ}C$		8	12	
R _{DS(on)}	Drain – Source on Resistance	$I_{\rm D} = 150 A$	$T_{j} = 150^{\circ}C$		14	21	mΩ
V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 30 \text{mA}$		2.1	2.4		V
I_{GSS}	Gate – Source Leakage Current	$V_{GS} = 20 \text{ V}, V_{DS} = 0 \text{ V}$				1.8	μA

Q1 to Q4 Dynamic Characteristics (per SiC MOSFET)

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
C_{iss} C_{oss}	Input Capacitance Output Capacitance	$V_{GS} = 0V$			8.4 0.66		пF
C_{rss}	Reverse Transfer Capacitance	$V_{DS} = 1000V$ $f = 1MHz$			0.045		III
Q_{g}	Total gate Charge	$V_{GS} = -5/+20V$		483			
Q_{gs}	Gate – Source Charge	$V_{\text{Bus}} = 800V$			138		nC
Q_{gd}	Gate – Drain Charge	$I_D = 150A$		150			
$T_{d(on)}$	Turn-on Delay Time	V - 5/±20V		35			
T_{r}	Rise Time	$V_{GS} = -5/+20V$ $V_{Bus} = 800V$ $I_D = 150A$, $T_J = 150$ °C			40		ns
$T_{d(off)}$	Turn-off Delay Time				150		
T_{f}	Fall Time	$R_L = 5.3\Omega$; $R_{Gext} =$	6.7Ω		70		
E _{on}	Turn on Energy	$ \begin{array}{c} \text{Inductive Switching} \\ V_{GS} = -5/+20V \\ V_{Bus} = 600V \\ I_D = 150A \\ R_{Gext} = 6.7\Omega \end{array} $	$T_{j} = 150^{\circ}C$		3.3		mJ
$E_{\rm off}$	Turn off Energy		$T_j = 150$ °C		1.8		mJ
R_{thJC}	Junction to Case Thermal Resistanc	e				0.135	°C/W

Body diode ratings and characteristics (per SiC MOSFET)

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
V	L Diode Forward Voltage	$V_{GS} = -5V, I_{SD} = 75A$		3.3		V
V_{SD}		$V_{GS} = -2V, I_{SD} = 75A$		3.1		v
t_{rr}	Reverse Recovery Time	1. 150A W 5W		45		ns
Q _{rr}	Reverse Recovery Charge	$I_{SD} = 150A$; $V_{GS} = -5V$ $V_{R} = 800V$; $di_{F}/dt = 3000A/\mu s$		1.2		μС
I_{rr}	Reverse Recovery Current	γη 300 γ , αιματ 3000 Α/μ3		40		Α



CR5 & CR6 SiC diode ratings and characteristics (Per SiC diode)

Symbol	Characteristic	Test Conditions			Typ	Max	Unit
V_{RRM}	Peak Repetitive Reverse Voltage					600	V
T	Daving Lasks as Comment	$V_R=600V$	$T_j = 25$ °C		220	1320	^
I_{RM}	Reverse Leakage Current	V R-000 V	$T_{j} = 175^{\circ}C$		440	6600	μА
I_F	DC Forward Current		Tc = 125°C		220		A
V_{F}	$I_{\rm F}$ Diode Forward Voltage $I_{\rm F} = 220 {\rm A}$	$T_i = 25$ °C		1.6	1.8	V	
V _F	Diode Forward Voltage	$I_F = 220A$	$T_i = 175$ °C		2	2.4	v
Q_{C}	Total Capacitive Charge		$I_F = 220A, V_R = 600V$ $di/dt = 5000A/\mu s$		616		nC
С	T 41 Compiler	$f = 1MHz, V_R =$	$f = 1MHz, V_R = 200V$		1430		"E
	Total Capacitance	$f = 1 MHz, V_R = 400 V$			1100		pF
R_{thJC}	Junction to Case Thermal Resistance	ince				0.1	°C/W

CR7 & CR8 diode ratings and characteristics (Per SiC diode)

Symbol	Characteristic	Test Conditions			Typ	Max	Unit
V_{RRM}	Peak Repetitive Reverse Voltage					1200	V
T	De come Leele e Coment	erse Leakage Current $V_R=1200V$	$T_j = 25^{\circ}C$		280	1600	4
I_{RM}	Reverse Leakage Current		$T_{j} = 175^{\circ}C$		520	3200	μA
I_F	DC Forward Current	$Tc = 125^{\circ}C$			160		A
V_{F}	Viada Famyand Valtaga I = 160A	$T_i = 25^{\circ}C$		1.5	1.8	V	
v F	Diode Forward Voltage	$I_F = 160A$	$T_i = 175^{\circ}C$		2.2	3	· ·
Qc	Total Capacitive Charge	$I_F = 160A, V_R = 1200V$ $di/dt = 4000A/\mu s$			1040		nC
С	Tatal Committee	$f = 1MHz, V_R = 400V$			744		"E
C	Total Capacitance	$f = 1MHz, V_R = 800V$			536		pF
R_{thJC}	Junction to Case Thermal Resistance					0.14	°C/W

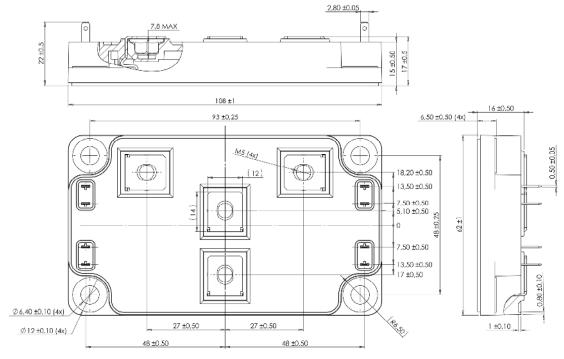
Thermal and package characteristics

Symbol	Characteristic					Max	Unit
V_{ISOL}	RMS Isolation Voltage, any terminal to o	case t=1 mir	n, 50/60)Hz	4000		V
T_{J}	()nerating unction temperature range		SiC M	OSFET	-40	150	
1 ј			SiC di	ode	-40	175	
T_{JOP}	Recommended junction temperature under switching conditions				-40	T _J max -25	°C
T_{STG}	Storage Temperature Range	e Range				125	
$T_{\rm C}$	Operating Case Temperature					125	
Torque	Mounting tongue	To heatsin	k	M6	3	5	N.m
Torque	Mounting torque For term		als	M5	2	3.5	IN.III
Wt	Package Weight					300	g

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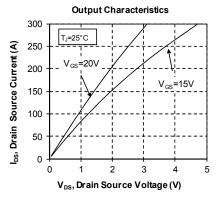
SP6 Package outline (dimensions in mm)

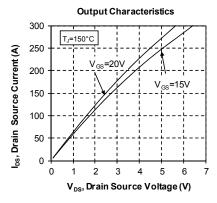


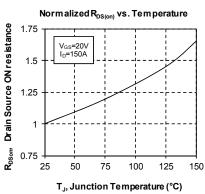
See application note APT0601 - Mounting Instructions for SP6 Power Modules on www.microsemi.com

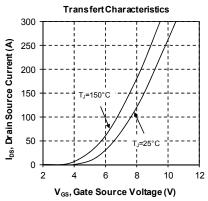


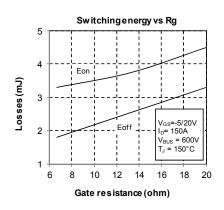
Q1 to Q4 Typical performance curve

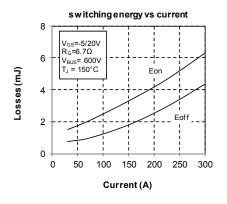


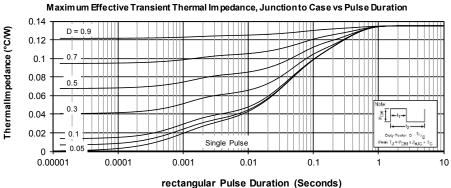






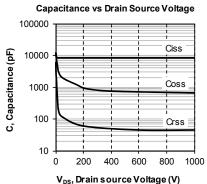


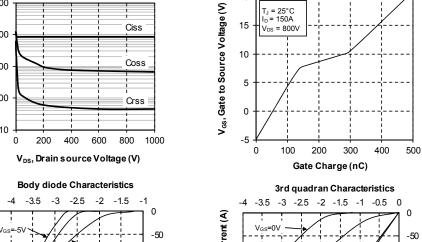






Gate Charge vs Gate Source Voltage

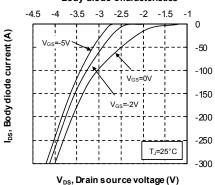


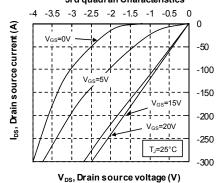


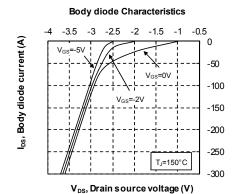
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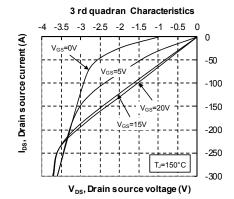
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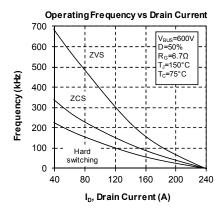
V_{DS} = 800V









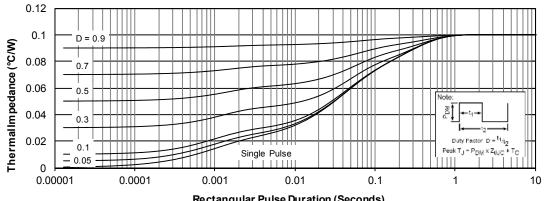


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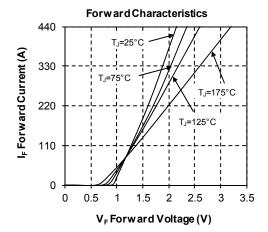


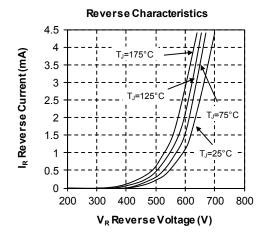
CR5 & CR6 Typical performance curve

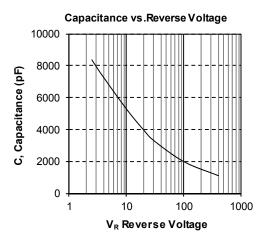
Maximum Effective Transient Thermal Impedance, Junction to Case vs Pulse Duration



Rectangular Pulse Duration (Seconds)



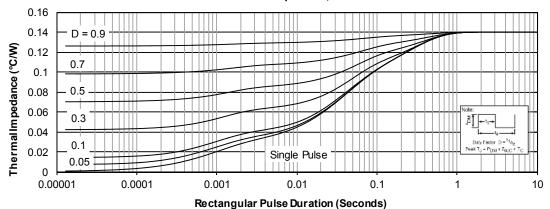


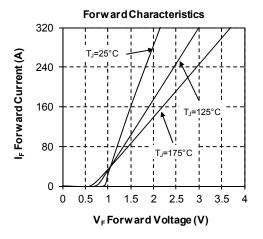


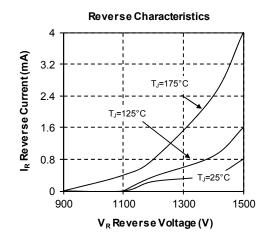


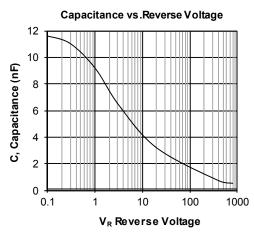
CR7 & CR8 Typical performance curve

Maximum Effective Transient Thermal Impedance, Junction to Case vs Pulse Duration









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