



4423

# PRECISION QUADRATURE OSCILLATOR

### **FEATURES**

- SINE AND COSINE OUTPUTS
- RESISTOR-PROGRAMMABLE FREQUENCY
- WIDE FREQUENCY RANGE: 0.002Hz to 20kHz
- LOW DISTORTION: 0.2% max up to 5kHz
- EASY ADJUSTMENTS
- SMALL SIZE
- LOW COST

### **DESCRIPTION**

The Model 4423 is a precision quadrature oscillator. It has two outputs 90 degrees out of phase with each other, thus providing sine and cosine wave outputs available at the same time. The 4423 is resistor programmable and is easy to use. It has low distortion (0.2% max up to 5kHz) and excellent frequency and amplitude stability.

The Model 4423 also includes an uncommitted operational amplifier which may be used as a buffer, a level shifter, or as an independent operational amplifier. The 4423 is packaged in a versatile, small, low-cost DIP package.

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# **SPECIFICATIONS**

Prices and Specifications subject to change without notice

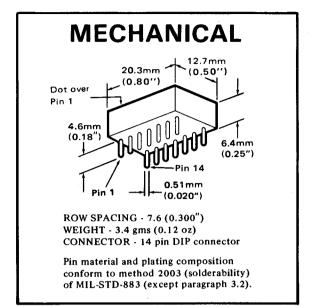
Specifications typical at 25°C and ±15VDC Power Supply Unless Otherwise Noted.

PREQUENCY		MIN	TYP	MAX	UNIT
Initial Frequency (no adjustments)   20.0k   20.5   21.0k   Hz   20k   2	FREQUENCY				
Frequency Range (using 2 R's and 2 C's)   Accuracy of Frequency Equation*   ±1	Initial Frequency (no adjustments)	20.0k	20.5	21.0k	Hz
Accuracy of Frequency Equation* Stability vs Temperature Quadrature Phase Error   ±1	Frequency Range (using 2 R's only)	2k			Hz
Stability vs Temperature Quadrature Phase Error   ±50 ±0.1   ±100   ppm/" degree   ±0.1   ±0.1     ppm/" degree   ±0.1       ppm/" degree   ±0.1     ppm/" degree   ±0.1     ppm/" degree   ±0.1     ppm/" degree   ±0.1     ppm/" degree   ±0.1     ppm/" degree   ±0.1     ppm/" degree   ±0.1     ppm/" degree   ±0.1     ppm/" degree   ppm/" degree   ±0.1   ppm/" degree   ppm/" deg		, 0.002			
Distortion   ±0.1   degree			1		
DISTORTION   Sine Output (pin 1)   0.002Hz to 5kHz   0.2   %   5kHz to 20kHz   0.5   %   %	•			±100	1
Sine Output (pin 1)   0.002Hz to 5kHz   5kHz to 20kHz   0.5		ļ	±0.1		degree
0.002Hz to 5kHz	DISTORTION				
SkHz to 20kHz	Sine Output (pin 1)				
Cosine Output (pin 7)   0.002Hz to 5kHz   0.2   0.8   %   %   %   %   %   %   %   %   %					
0.002Hz to 5kHz	5kHz to 20kHz			0.5	%
Distortion vs Temperature   0.8   %   %   %   OUTPUT	Cosine Output (pin 7)				
Distortion vs Temperature   0.015   %/°C	0.002Hz to 5kHz		1	ļ	%
OUTPUT           Amplitude (Sine)         6.5         7         7.5         V rms           vs Temperature         0.05         %/°C         vs/°C         vs/°C         vs Supply         0.4         V/V         V/V         Output Current         1.5         5         mA         OUTPUT         OUTPUT         Insumediate         1         0.05         %/°C         %/°C         %/°C         %/°C         %/°C         %/°C         %/°C         %         %/°C         %/°C         %         %/°C         %         %/°C         %          %	5kHz to 20kHz		0.8		%
Amplitude (Sine)	Distortion vs Temperature		0.015		%/°C
At 20 kHz	OUTPUT				
At 20 kHz	Amplitude (Sine)				
vs Supply         0.4         V/V           Output Current         1.5         5         mA           Output impedance         1         Ω           UNCOMMITTED OP AMP         1.5         mV           Input Offset Voltage         1.5         mV           Input Bias Current         275         nA           Input Impedance         1         MΩ           Open Loop Gain         90         dB           Output Current         5         mA           POWER SUPPLY           Rated Supply Voltage         ±15         VDC           Supply Voltage Range         ±12         ±18         VDC           Quiescent Current         ±9         ±18         mA           TEMPERATURE RANGE         5         -25         +70         °C           Operation         -25         +85         °C	· · · · · · · · · · · · · · · · · · ·	6.5	7	7.5	V rms
Output Current Output impedance         1.5         5         I MA Ω           UNCOMMITTED OP AMP         Input Offset Voltage         1.5         mV           Input Bias Current         275         nA           Input Impedance         1         MΩ           Open Loop Gain         90         dB           Output Current         5         mA           POWER SUPPLY           Rated Supply Voltage Supply Voltage Supply Voltage Range Quiescent Current         ±15         VDC           EMPERATURE RANGE         ±9         ±18         mA           TEMPERATURE RANGE           Specifications Operation         0         +70         °C           Operation         -25         +85         °C	vs Temperature		0.05		%/°C
Output impedance         1         Ω           UNCOMMITTED OP AMP         Input Offset Voltage         1.5         mV           Input Bias Current         275         nA           Input Impedance         1         MΩ           Open Loop Gain         90         dB           Output Current         5         mA           POWER SUPPLY         Rated Supply Voltage Supply Voltage Range Quiescent Current         ±15         VDC           Quiescent Current         ±9         ±18         mA           TEMPERATURE RANGE         0         +70         °C           Operation         -25         +85         °C	vs Supply	ļ	0.4		V/V
Input Offset Voltage	•	1.5	5		mA
Input Offset Voltage				1	Ω
Input Bias Current	UNCOMMITTED OP AMP	•			
Input Impedance	Input Offset Voltage		1.5		mV
Open Loop Gain Output Current         90         dB mA           POWER SUPPLY         5         VDC           Rated Supply Voltage Supply Voltage Range Quiescent Current         ±15         VDC           ±18         VDC           ±9         ±18         mA           TEMPERATURE RANGE           Specifications Operation         0         +70         °C           -25         +85         °C	Input Bias Current	1	275		nA
Output Current         5         mA           POWER SUPPLY         Rated Supply Voltage         ±15         VDC           Supply Voltage Range         ±12         ±18         VDC           Quiescent Current         ±9         ±18         mA           TEMPERATURE RANGE         Specifications         0         +70         °C           Operation         -25         +85         °C					
POWER SUPPLY	•	_	90		1
Rated Supply Voltage		5			mA
Supply Voltage Range Quiescent Current         ±12         ±18 VDC MA           TEMPERATURE RANGE         ±9         ±18 mA           Specifications Operation         0         +70 °C +85 °C	POWER SUPPLY				
Quiescent Current         ±9         ±18         mA           TEMPERATURE RANGE           Specifications         0         +70         °C           Operation         -25         +85         °C	Rated Supply Voltage		±15		VDC
TEMPERATURE RANGE           Specifications         0         +70         °C           Operation         -25         +85         °C	Supply Voltage Range	±12	1	1	VDC
Specifications         0         +70         °C           Operation         -25         +85         °C	Quiescent Current		±9	±18	mA
Operation -25 +85 °C	TEMPERATURE RANGE				
Operation -25 +85 °C	Specifications	0		+70	l °c
	•			+85	°C
		-55		+125	°C
				1	
			•		•

<sup>\*</sup> May be trimmed for better accuracy.

### PIN CONNECTIONS

- 1. E<sub>1</sub>, Sine Output
- 2. Frequency Adjustment
- 3. Frequency Adjustment
- 4. +In, Uncommitted Op Amp
- 5. -In, Uncommitted Op Amp
- 6. Output, Uncommitted Op Amp
- 7. E<sub>2</sub>, Cosine Output
- 8. Frequency Adjustment
- 9. -V<sub>cc</sub>, -15VDC
- 10. +V<sub>cc</sub>, +15VDC
- 11. Common
- 12. Frequency Adjustment
- 13. Frequency Adjustment
- 14. Frequency Adjustment



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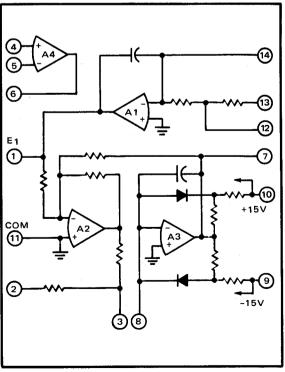
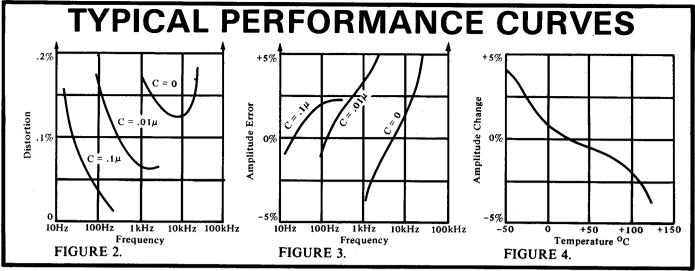


FIGURE 1. Equivalent Circuit.



#### **EXTERNAL CONNECTIONS**

#### 1. 20 kHz Quadrature Oscillator

The 4423 does not require any external component to obtain a 20 kHz quadrature oscillator. The connection diagram is as shown in Figure 5.

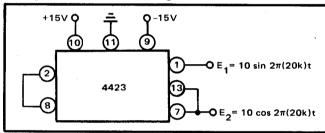


FIGURE 5.

#### 2. Resistor Programmable Quadrature Oscillator

For resistor programmable frequencies in the 2 kHz to 20 kHz frequency range, the connection diagram is shown in Figure 6. Note that only two resistors of equal value are required. The resistor R can be expressed by,

, R in  $k\Omega$ 

FIGURE 6.

# 3. Quadrature Oscillator Programmable to 0.002 Hz

For oscillator frequencies below 2000 Hz, use of two capacitors of equal value and two resistors of equal value as shown in Figure 7 is recommended. Connections shown in Figure 7 can be used to get oscillator frequency in the 0.002 Hz to 20 kHz range.

The frequency f can be expressed by:

$$f = \frac{42.05 \text{ R}}{(C + 0.001) (3.785 + 2R)}$$

where, f is in Hz C is in  $\mu$ F and R is in  $k\Omega$ 

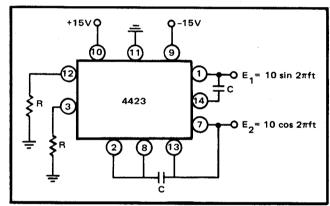


FIGURE 7.

For best results, the capacitor values shown in Table I should be selected with respect to their frequency ranges.

f	20 kHz	2 kHz	200 Hz
	to	to	to
	2 kHz	200 Hz	20 Hz
С	0	0.01μF	0.1μF
20 Hz	2 Hz	0.2 Hz	0.02 Hz
to	to	to	to
2 Hz	0.2 Hz	0.02 Hz	0.002 Hz
1μF	10µF	100μF	1000μF

TABLE I.

After selecting the capacitor for a particular frequency the value of the required resistor can be obtained by using the resistor selection curve shown in Figure 8 or by the expression:

$$R = \frac{3.785f (C + 0.001)}{42.05 - 2f (C + 0.001)} \qquad \begin{array}{c} \text{where,} \\ \text{R is in } k\Omega \\ \text{f is in } Hz \\ \text{and } C \text{ is in } \mu F \end{array}$$

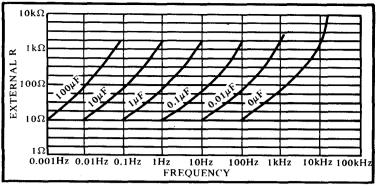


FIGURE 8.

The curves shown in Figure 8 are provided only as a nomographic design aid. The selection of capacitor values is not limited to the values shown in Figure 8. Any suitable combination of R and C values which satisfies the expression relating R, F and C as shown above, would work satisfactorily with the 4423.

#### **NOTES ON TYPES OF CAPACITORS TO USE:**

There are various kinds of capacitors available for use. There are polarized, also known as DC capacitors and non-polarized, also known as AC capacitors available. Of these two types, the polarized capacitors cannot be used with 4423 to set the frequencies.

Commonly available non-polarized capacitors include NPO ceramic, silver mica, teflon, polystyrene, polycarbonate, mylar, ceramic disc etc. A comparison is shown in Table II.

	Capacitance Range (μF)	Temperature Coefficients ppm/°C	Dissipation Factor (%)
NPO Ceramic	5pF - 0.1 μF	30	0.05
Silver Mica	5pF - 0.047 μF	60	0.05
Teflon	0.001 - 100 μF	200	0.01
Polystyrene	0.001 - 500 μF	100	0.03
Polycarbonate	0.001 - 1000 μF	90	0.08
Metalized Teflon Metalized	0.001 - 100 μF	60	0.1
Polycarbonate	0.001 - 1000 μF	10	0.4
Mylаг	0.001 - 1000 μF	700	0.7
Metalized Mylar	0.001 - 2000 μF	700	1
Ceramic Disc	5pF - 0.5 μF	10,000	3

TABLE II.

For use with the 4423 oscillator, the choice of capacitors depends mainly on the user's application, error budget and cost budget. Note that the specifications of 4423 do not include the error contribution of the external components. The errors sourced by external components normally have to be added to the 4423 specifications.

As a general selection criteria we recommend the use of the above table. Start from the top of the list in the above table. If the capacitor is found unsuitable due to it being too large in size, too expensive, or is not easily available, then move down in the list for the next best selection. In any case do not choose or use any capacitors with dissipation factors greater than 1%. Such a capacitor would stop 4423 oscillation.

#### **DISSIPATION FACTOR (DF)**

A capacitor can be modeled by an ideal capacitor in parallel with an internal resistor whose value depends on its dissipation factor (DF). Mathematically, the internal resistor R is given by,

$$R = \frac{1}{2\pi f C(DF)}$$

where R is in  $\Omega$ , f is the Hz, and C is in farads.

For example, the DF of ceramic disc capacitors is of the order of 3%, which for a 0.01  $\mu$ F capacitor would look like having an internal resistor of 530k $\Omega$  at 1 kHz. The 530 k $\Omega$  value resistor is small enough to stop the 4423 oscillator from oscillating.

Some capacitor manufacturers use the terms "Power Factor" (PF) or "Q Factor" (Q) instead of the term "Dissipation Factor". These terms are similar in meaning and are mathematically related by.

$$(PF) = \frac{(DF)}{\sqrt{1 + (DF)^2}}$$
;  $Q = \frac{1}{(DF)}$ 

#### **OSCILLATION AMPLITUDE**

It takes a finite time to build up the amplitude of the oscillation to its final full scale value. There is a relationship between the amplitude build-up time and the frequency. The lower the frequency, the longer the amplitude build-up time. For example, typically it takes 250 seconds at 1 Hz, 30 seconds at 10 Hz, 4 seconds at 100 Hz, 400 milliseconds at 1 kHz, and 40 milliseconds at 10 kHz oscillator frequencies.

There are two methods available to shorten this normal amplitude build-up time. But there is also a relationship between the amplitude build-up time and distortion at final amplitude value. When the amplitude build-up time is shortened, the distortion can get worse.

One method to shorten the amplitude build-up time is to connect a resistor between pin 3 and pin 14. The lower this resistor is the shorter will be the time to build up amplitude of the oscillation, and worse will be the distortion of the output waveform. For example, a  $100 \mathrm{k}\Omega$  resistor would shorten the amplitude build up time from 15 seconds to 1 second at 20 Hz frequency, but the distortion could be degraded from tpically 0.05% to 0.5%.

The other method is to momentarily insert a  $1k\Omega$  resistor via a reset switch betwen pin 3 and pin 14. The amplitude of oscillation is built up instantaneously when the reset switch is pushed. There will be no degradation of distortion with this method since the  $1k\Omega$  resistor does not remain in the circuit continuously.





3-Oct-2003

#### **PACKAGING INFORMATION**

ORDERABLE DEV		PACKAGE TYPE			PACKAGE QTY
4423P	NRND	PDIP	NSQ	14	50

(1) The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in

a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

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