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March 2015

FQT13N06L

N-Channel QFET[®] MOSFET 60 V, 2.8 A, 110 m Ω

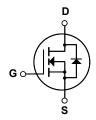
General Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor®'s proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor control, and variable switching power applications.

Features

- 2.8 A, 60 V, $R_{DS(on)}$ = 110 m Ω (Max.) @ V_{GS} = 10 V, I_D = 1.4 A
- Low Gate Charge (Typ. 4.8 nC)
- Low Crss (Typ. 17 pF)
- · 100% Avalanche Tested





Absolute Maximum Ratings T_C = 25°C unless otherwise noted

Symbol	Parameter		FQT13N06L	Unit	
V_{DSS}	Drain-Source Voltage		60	V	
I _D	Drain Current - Continuous (T _C = 25°C	()	2.8	Α	
	- Continuous (T _C = 70°C)		2.24	А	
I _{DM}	Drain Current - Pulsed	(Note 1)	11.2	Α	
V _{GSS}	Gate-Source Voltage		± 20	V	
E _{AS}	Single Pulsed Avalanche Energy	(Note 2)	85	mJ	
I _{AR}	Avalanche Current (Note 1)		2.8	Α	
E _{AR}	Repetitive Avalanche Energy	(Note 1)	0.21	mJ	
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	7.0	V/ns	
P _D	Power Dissipation (T _C = 25°C)		2.1	W	
	- Derate above 25°C		0.017	W/°C	
T _J , T _{STG}	Operating and Storage Temperature Range	je	-55 to +150	°C	
T _L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds		300	°C	

Thermal Characteristics

Symbol	Parameter	Тур	Max	Unit
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient *		60	°C/W

^{*} When mounted on the minimum pad size recommended(PCB mount).

Symbol	Parameter	Test Conditions		Min	Тур	Max	Unit
Off Cha	racteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		60			V
ΔBV _{DSS} / ΔΤ _J	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced t	o 25°C		0.05		V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 60 V, V _{GS} = 0 V				1	μА
		V _{DS} = 48 V, T _C = 125°C				10	μA
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 20 V, V _{DS} = 0 V				100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -20 V, V _{DS} = 0 V				-100	nA
On Cha	racteristics						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$		1.0	-	2.5	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 1.4 A			0.088	0.11	Ω
		$V_{GS} = 5 \text{ V}, I_D = 1.4 \text{ A}$			0.110	0.14	32
9 _{FS}	Forward Transconductance	V _{DS} = 25 V, I _D = 1.4 A	(Note 4)		4.1		S
C _{iss}	ic Characteristics Input Capacitance Output Capacitance	V _{DS} = 25 V, V _{GS} = 0 V, f = 1.0 MHz			270 95	350 125	pF pF
C _{rss}	Reverse Transfer Capacitance				17	23	pF
	ng Characteristics		ı				ı
t _{d(on)}	Turn-On Delay Time	$V_{DD} = 30 \text{ V}, I_D = 6.8 \text{ A},$			8	25	ns
t _r	Turn-On Rise Time	R _G = 25 Ω			90	190	ns
t _{d(off)}	Turn-Off Delay Time	,	N-4- 4 5\		20	50	ns
t _f	Turn-Off Fall Time	(Note 4, 5)		40	90	ns
Q _g	Total Gate Charge	V_{DS} = 48 V, I_{D} = 13.6 A,			4.8	6.4	nC
Q _{gs}	Gate-Source Charge	V _{GS} = 5 V			1.6		nC
Q_{gd}	Gate-Drain Charge	(Note 4, 5)		2.7		nC
Drain-S	Source Diode Characteristics an		,			2.8	Α
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current				11.2	A	
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 2.8 A				1.5	V
t _{rr}	Reverse Recovery Time	$V_{GS} = 0 \text{ V, } I_S = 2.6 \text{ A}$			45		ns
4TT	INCACISE INCORACIÓ HILLE	1 VGS - U V, IS - 10.0 A,			70		115

- Notes: 1. Repetitive rating : pulse-width limited by maximum junction temperature. 2. L = 12.6 mH, I_{AS} = 2.8 A, V_{DD} = 25 V, R_{G} = 25 Ω , starting T_{J} = 25°C. 3. I_{SD} ≤ 13.6 A, di/dt ≤ 300 $A/\mu s$, V_{DD} ≤ B V_{DSS} , starting T_{J} = 25°C. 4. Pulse test : pulse width ≤ 300 μs , Duty cycle ≤ 2%. 5. Essentially independent of operating temperature.

Typical Characteristics

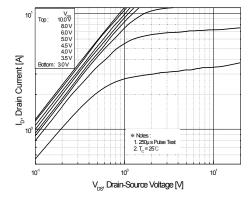


Figure 1. On-Region Characteristics

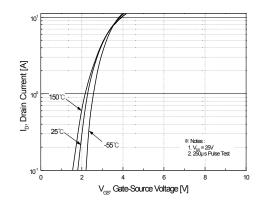


Figure 2. Transfer Characteristics

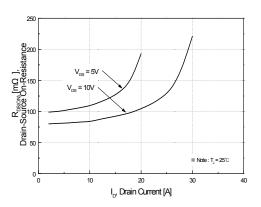


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

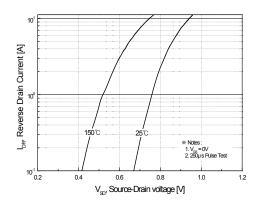


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

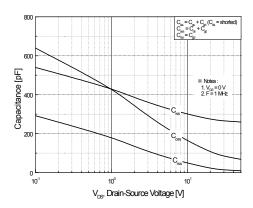


Figure 5. Capacitance Characteristics

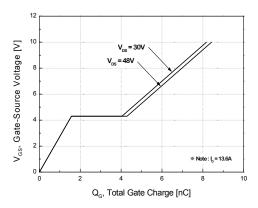
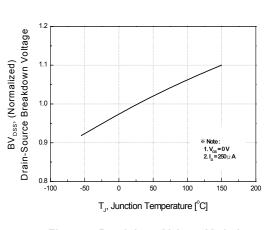


Figure 6. Gate Charge Characteristics

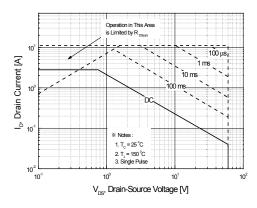


Typical Characteristics (Continued)

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Figure 7. Breakdown Voltage Variation vs Temperature

Figure 8. On-Resistance Variation vs Temperature



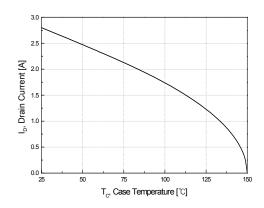


Figure 9. Maximum Safe Operating Area

Figure 10. Maximum Drain Current vs Case Temperature

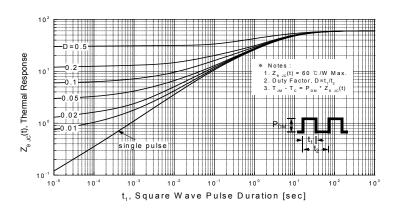


Figure 11. Transient Thermal Response Curve



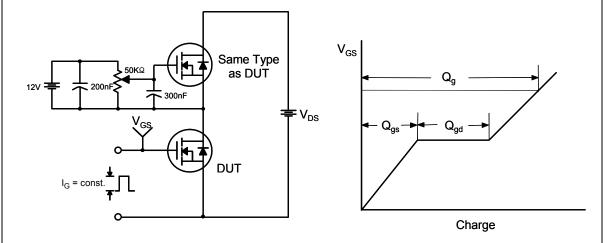


Figure 13. Resistive Switching Test Circuit & Waveforms

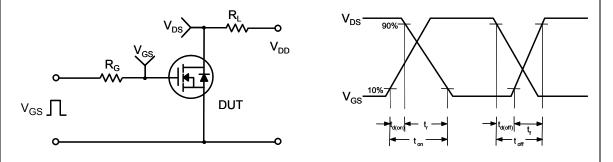
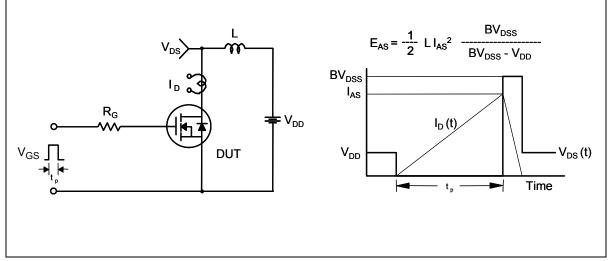
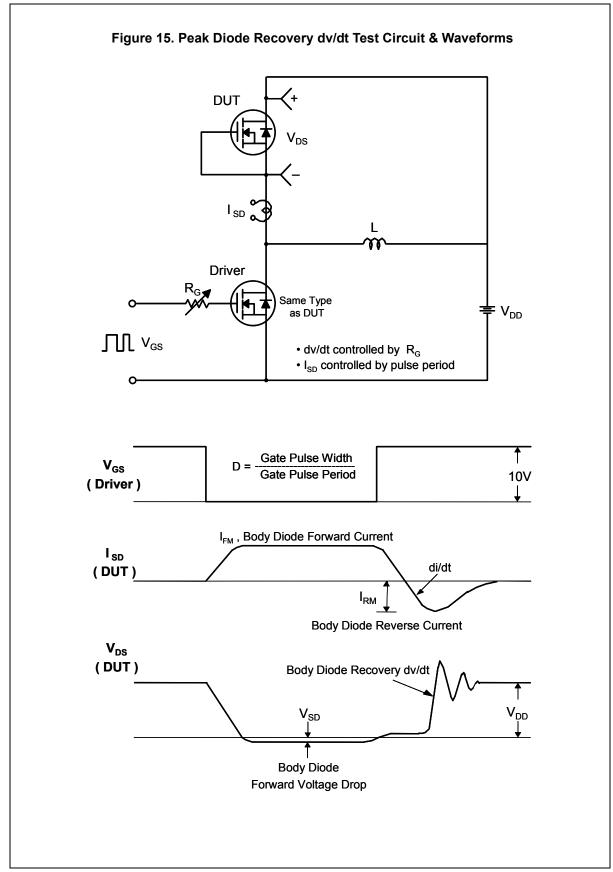
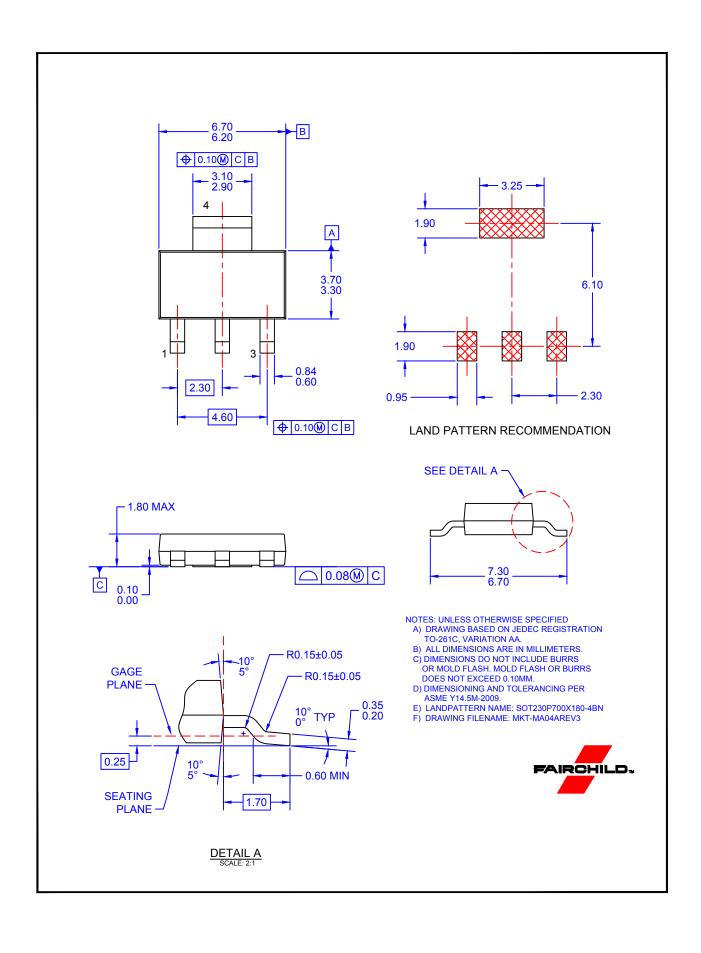


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms







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