AUTOMOTIVE

ROHS

HALOGEN

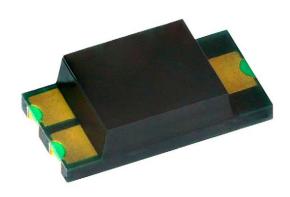
FREE

GREEN (5-2008)



### Vishay Semiconductors

### Silicon PIN Photodiode



#### **DESCRIPTION**

VEMD6110X01 is a high speed and high sensitive PIN photodiode. It is a small surface mount device (SMD) including the chip with a 0.85 mm² sensitive area and a daylight blocking filter matched with IR emitters operating at wavelength of 830 nm to 950 nm.

#### **FEATURES**

- Package type: surface-mount
- Package form: 1206
- Dimensions (L x W x H in mm): 4 x 2 x 1.05
- Radiant sensitive area (in mm<sup>2</sup>): 0.85
- · High photo sensitivity
- · High sensitivity
- Daylight blocking filter matched with 830 nm to 950 nm emitters
- Fast response times
- Angle of half sensitivity:  $\varphi = \pm 60^{\circ}$
- Floor life: 72 h, MSL 4, according to J-STD-020
- Lead (Pb)-free reflow soldering
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>



· High speed photo detector

PRODUCT SUMMARY				
COMPONENT	I <sub>ra</sub> (μΑ)	φ <b>(°)</b>	λ <sub>0.5</sub> (nm)	
VEMD6110X01	9.5	± 60	750 to 1050	

#### Note

• Test conditions see table "Basic Characteristics"

ORDERING INFORMATION					
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM		
VEMD6110X01	Tape and reel	MOQ: 3000 pcs, 3000 pcs/reel	1206		

#### Note

· MOQ: minimum order quantity

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Reverse voltage		$V_{R}$	32	V	
Power dissipation	T <sub>amb</sub> ≤ 25 °C	P <sub>V</sub>	215	mW	
Junction temperature		T <sub>j</sub>	110	°C	
Ambient temperature range		T <sub>amb</sub>	-40 to +110	°C	
Storage temperature range		T <sub>stg</sub>	-40 to +110	°C	
Soldering temperature	According to reflow solder profile Fig. 8	T <sub>sd</sub>	260	°C	
Thermal resistance junction to ambient	According to J-STD-051	R <sub>thJA</sub>	270	K/W	

<b>BASIC CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 50 mA	V <sub>F</sub>	=	1	-	V
Breakdown voltage	$I_R = 100 \mu A, E = 0$	V <sub>(BR)</sub>	32	-	-	V
Reverse dark current	V <sub>R</sub> = 10 V, E = 0	I <sub>ro</sub>	-	1	3	nA
Diode capacitance	$V_R = 0 V, f = 1 MHz, E = 0$	C <sub>D</sub>	=	12	-	pF
	$V_R = 5 \text{ V}, f = 1 \text{ MHz}, E = 0$	C <sub>D</sub>	=	3.6	-	pF
Open circuit voltage	$E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm}$	Vo	-	356	-	mV
Temperature coefficient of Vo	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 950 \text{ nm}$	TK <sub>Vo</sub>	=	-3.1	-	mV/K
Short circuit current	$E_{e} = 1 \text{ mW/cm}^{2}, \lambda = 950 \text{ nm}$	l <sub>k</sub>	=	9	-	μA
Temperature coefficient of I <sub>k</sub>	$E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm}$	TK <sub>lk</sub>	-	0.1	-	%/K
Reverse light current	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 950 \text{ nm}$ , $V_R = 5 \text{ V}$	I <sub>ra</sub>	6.7	9.5	12.4	μΑ
Angle of half sensitivity		φ	=	± 60	-	0
Wavelength of peak sensitivity		$\lambda_{p}$	-	950	-	nm
Range of spectral bandwidth		λ <sub>0.5</sub>	750	-	1050	nm
Rise time	$V_R = 10 \text{ V}, R_L = 1 \text{ k}\Omega, \lambda = 820 \text{ nm}$	t <sub>r</sub>	-	100	-	ns
Fall time	$V_R = 10 \text{ V}, R_L = 1 \text{ k}\Omega, \lambda = 820 \text{ nm}$	t <sub>f</sub>	-	100	-	ns

### **BASIC CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

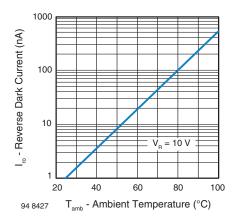


Fig. 1 - Reverse Dark Current vs. Ambient Temperature

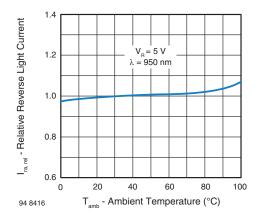


Fig. 2 - Relative Reverse Light Current vs. Ambient Temperature

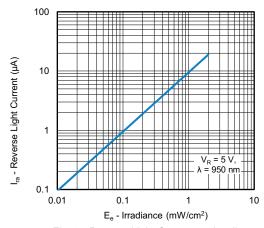


Fig. 3 - Reverse Light Current vs. Irradiance

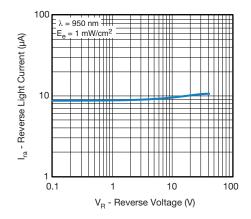


Fig. 4 - Reverse Light Current vs. Reverse Voltage

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# Vishay Semiconductors

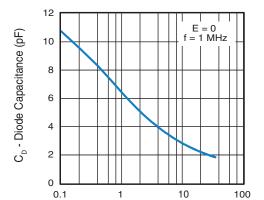


Fig. 5 - Diode Capacitance vs. Reverse Voltage

V<sub>R</sub> - Reverse Voltage (V)

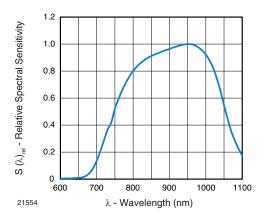


Fig. 6 - Relative Spectral Sensitivity vs. Wavelength

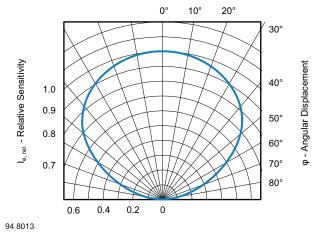


Fig. 7 - Relative Sensitivity vs. Angular Displacement

#### **REFLOW SOLDER PROFILE**

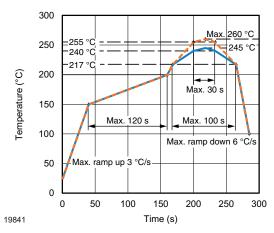


Fig. 8 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020

#### **DRYPACK**

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

#### **FLOOR LIFE**

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 72 h

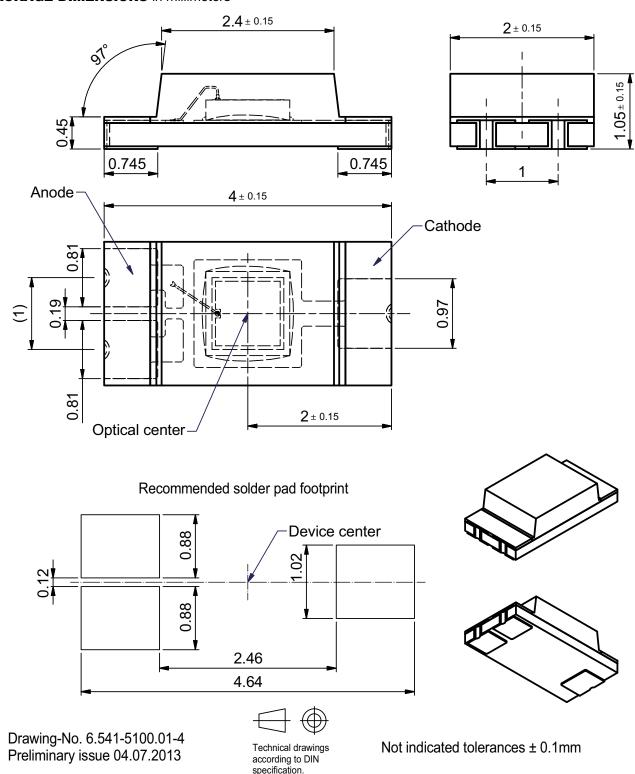
Conditions: T<sub>amb</sub> < 30 °C, RH < 60 %

Moisture sensitivity level 4, acc. to J-STD-020.

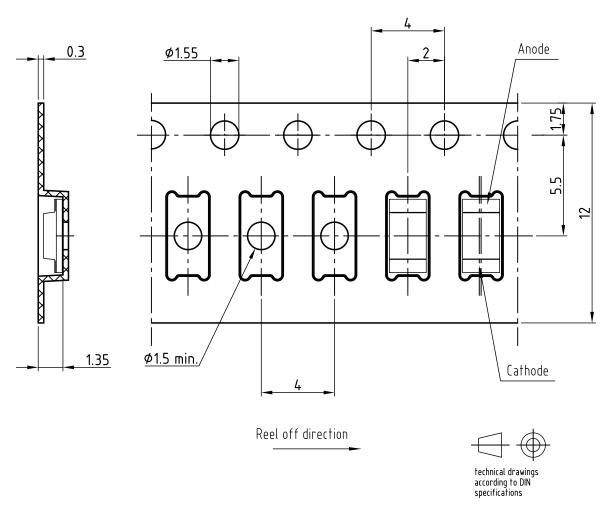
#### **DRYING**

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40  $^{\circ}$ C (+ 5  $^{\circ}$ C), RH < 5  $^{\circ}$ M.

#### **PACKAGE DIMENSIONS** in millimeters



#### **BLISTER TAPE DIMENSIONS** in millimeters



Not indicated tolerances ±0.1

Drawing refers to following Types: TEMD6010FX01

VEMD6x10X01 VEMD6x15X01

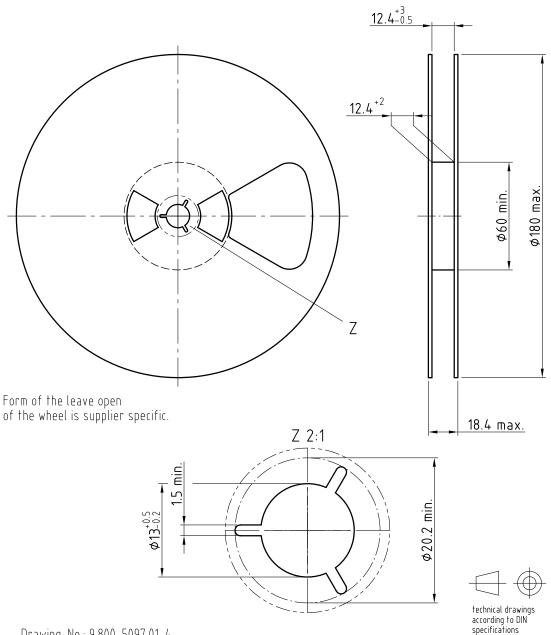
Drawing-No.: 9.700-5329.02-4

Prel Issue: 16.07.2013

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All dimensions in mm

#### **REEL DIMENSIONS** in millimeters



Drawing-No.: 9.800-5097.01-4

Issue: 1; 05.05.08

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Vishay

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