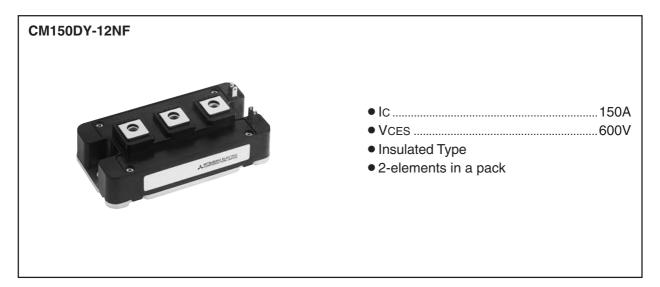
MITSUBISHI IGBT MODULES

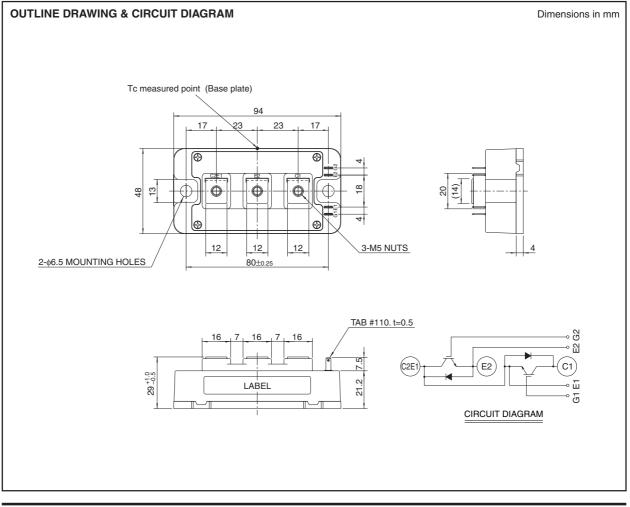
# CM150DY-12NF

HIGH POWER SWITCHING USE



### APPLICATION

General purpose inverters & Servo controls, etc





# **CM150DY-12NF**

#### **HIGH POWER SWITCHING USE**

#### MAXIMUM RATINGS (Tj = 25°C, unless otherwise specified)

Symbol	Parameter	Conditions		Ratings	Unit
VCES	Collector-emitter voltage	G-E Short		600	V
VGES	Gate-emitter voltage	C-E Short		±20	V
IC	Collector current	DC, Tc' =97°C <sup>*3</sup>		150	A
Ісм	Collector current	Pulse	(Note 2)	300	A
IE (Note 1)				150	A
IEM (Note 1)	Emitter current	Pulse	(Note 2)	300	A
PC (Note 3)	Maximum collector dissipation	$Tc = 25^{\circ}C$		590	W
Tj	Junction temperature			-40 ~ +150	°C
Tstg	Storage temperature			-40 ~ +125	°C
Viso	Isolation voltage	Terminals to base plate, f = 60Hz, AC 1 min	nute	2500	Vrms
_	Tayou atvosath	Main terminals M5 screw		2.5 ~ 3.5	N • m
	Torque strength	Mounting M6 screw		3.5 ~ 4.5	N • m
_	Weight	Typical value		310	g

#### ELECTRICAL CHARACTERISTICS (Tj = 25°C, unless otherwise specified)

Oursels at	Parameter	Test conditions		Limits			11-3
Symbol	Parameter			Min.	Тур.	Max.	Unit
ICES	Collector cutoff current	VCE = VCES, VGE = 0V			—	1	mA
VGE(th)	Gate-emitter threshold voltage	IC = 15mA, VCE = 10V		5	6	7.5	v
IGES	Gate leakage current	±VGE = VGES, VCE = 0V		_	_	0.5	μA
VCE(sat)	Collector-emitter saturation voltage	IC = 150A, VGE = 15V	Tj = 25°C	_	1.7	2.2	V
			Tj = 125°C	_	1.7	_	
Cies	Input capacitance	VCE = 10V VGE = 0V			—	23	nF
Coes	Output capacitance			_	_	2.8	nF
Cres	Reverse transfer capacitance			_	_	0.9	nF
QG	Total gate charge	VCC = 300V, IC = 150A, VGE = 15V		_	600	_	nC
td(on)	Turn-on delay time	Vcc = 300V, lc = 150A VGE = $\pm 15V$ RG = 4.2 $\Omega$ , Inductive load IE = 150A		_	_	120	ns
tr	Turn-on rise time			_	_	100	ns
td(off)	Turn-off delay time			_	_	300	ns
tf	Turn-off fall time			_	—	300	ns
trr (Note 1)	Reverse recovery time			_	—	150	ns
Qrr (Note 1)	Reverse recovery charge				2.5	—	μC
VEC(Note 1)	Emitter-collector voltage	IE = 150A, VGE = 0V		_	_	2.6	V
Rth(j-c)Q		IGBT part (1/2 module)		_	_	0.21	K/W
Rth(j-c)R	Thermal resistance <sup>*1</sup>	FWDi part (1/2 module)		_	_	0.47	K/W
Rth(c-f)	Contact thermal resistance	Case to heat sink, Thermal compound Applied <sup>*2</sup> (1/2 module)		_	0.07	_	K/W
Rth(j-c')Q	Thermal resistance	Case temperature measured point is just under the chips		_	_	0.16 <sup>*3</sup>	K/W
RG	External gate resistance			4.2	—	42	Ω

\*1 : Case temperature (Tc) measured point is shown in page OUTLINE DRAWING.
\*2 : Typical value is measured by using thermally conductive grease of λ = 0.9[W/(m • K)].
\*3 : Case temperature (Tc') measured point is just under the chips.

If you use this value, Rth(f-a) should be measured just under the chips.

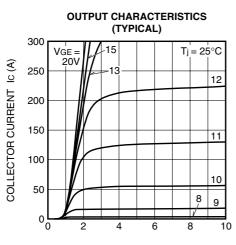
Note 1. IE, VEC, tr & Qrr represent characteristics of the anti-parallel, emitter-collector free-wheel diode (FWDi).
2. Pulse width and repetition rate should be such that the device junction temperature (Tj) does not exceed Tjmax rating.
3. Junction temperature (Tj) should not increase beyond 150°C.



### **CM150DY-12NF**

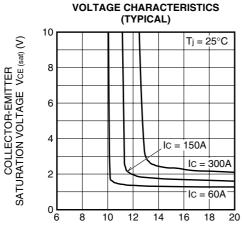
#### **HIGH POWER SWITCHING USE**



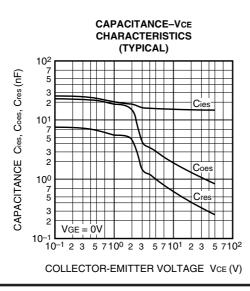


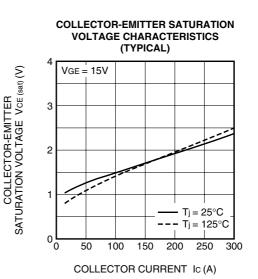
COLLECTOR-EMITTER VOLTAGE VCE (V)

**COLLECTOR-EMITTER SATURATION** 

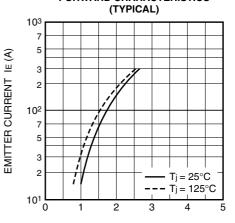


GATE-EMITTER VOLTAGE VGE (V)

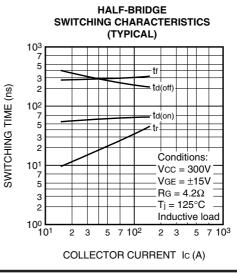




FREE-WHEEL DIODE FORWARD CHARACTERISTICS

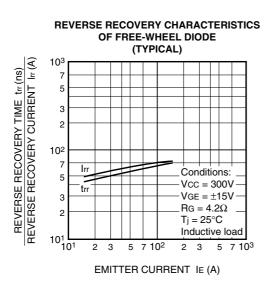


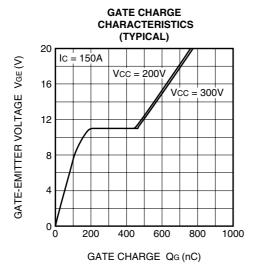
EMITTER-COLLECTOR VOLTAGE VEC (V)

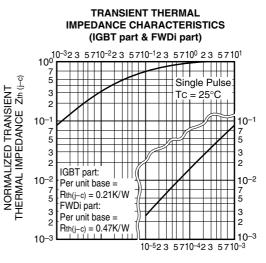


## CM150DY-12NF

#### **HIGH POWER SWITCHING USE**







TIME (s)



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