TOSHIBA

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74HC125AP, TC74HC125AF TC74HC126AP, TC74HC126AF

TC74HC125AP/AF TC74HC126AP/AF Quad Bus Buffer Quad Bus Buffer

The TC74HC125A/126A are high speed CMOS QUAD BUS BUFFERs fabricated with silicon gate C^2MOS technology. They achieve the high speed operation similar to equivalent

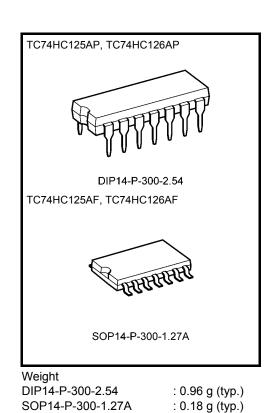
LSTTL while maintaining the CMOS low power dissipation.

The TC74HC125A requires the 3-state control input G to be set high to place the output into the high impedance state, whereas the TC74HC126A requires the control input to be set low to place the output into high impedance.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

Features

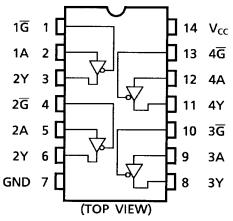
- High speed: $t_{pd} = 10 \text{ ns}$ (typ.) at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 4 \ \mu A \ (max)$ at $Ta = 25^{\circ}C$
- High noise immunity: $V_{NIH} = V_{NIL} = 28\% V_{CC}$ (min)
- Output drive capability: 15 LSTTL loads
- Symmetrical output impedance: $|I_{OH}| = I_{OL} = 6 \text{ mA} (min)$
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: V_{CC} (opr) = 2 to 6 V
- Pin and function compatible with 74LS125/126



Start of commercial production 1988-05

Pin Assignment





IEC Logic Symbol

TC74HC125A

1 <u>G</u> 1A	$\frac{(1)}{(2)}$	EN	⊳	⊽	<u>(3)</u> 1Y
1 <u>A</u> 2G 2A	<u>(4)</u> (5) (10)				<u>(6)</u> 2Y
2 <u>A</u> 3G 3A	$\frac{(10)}{(9)}$				<u>(8)</u> 3Y
3 <u>A</u> 4G 4A	(13) (12)				<u>(11)</u> 4Y

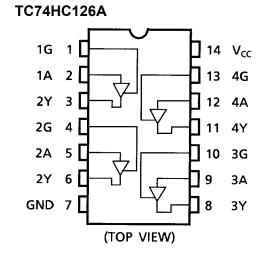
Truth Table

TC74HC125A

Inp	uts	Output		
IG	А	Y		
Н	Х	Z		
L	L	L		
L	Н	Н		

X: Don't care

Z: High impedance



TC74HC126A

1G 1A	$\frac{(1)}{(2)}$	EN	⊳	⊽	<u>(3)</u> 1Y
2G 2A	<u>(4)</u> (5)				<u>(6)</u> 2Y
3G 3A	(<u>10)</u> (<u>9)</u> (12)				(<u>8)</u> 3Y
4G 4A	(<u>13)</u> (12)				<u>(11)</u> 4Y

TC74HC126A

Inp	uts	Output
G	А	Y
L	Х	Z
Н	L	L
Н	Н	Н

X: Don't care

Z: High impedance

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	–0.5 to 7	V
DC input voltage	V _{IN}	-0.5 to V _{CC} + 0.5	V
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V
Input diode current	I _{IK}	±20	mA
Output diode current	IOK	±20	mA
DC output current	IOUT	±35	mA
DC V _{CC} /ground current	ICC	±75	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T _{stg}	–65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to 65° C. From Ta = 65 to 85° C a derating factor of -10 mW/°C shall be applied until 300 mW.

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2 to 6	V
Input voltage	V _{IN}	0 to V _{CC}	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Operating temperature	T _{opr}	-40 to 85	°C
		0 to 1000 (V _{CC} = 2.0 V)	
Input rise and fall time	t _r , t _f	0 to 500 ($V_{CC} = 4.5 \text{ V}$)	ns
		0 to 400 ($V_{CC} = 6.0 \text{ V}$)	

Operating Ranges (Note)

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition V _{CC} (V)		Ta = 25°C			Ta = -40 to 85°C		Unit	
	-			V _{CC} (V)	Min	Тур.	Max	Min	Max	
		_		2.0	1.50		_	1.50		
High-level input voltage	VIH			4.5	3.15	—	_	3.15	—	V
				6.0	4.20	—	—	4.20		
				2.0	_	—	0.50	_	0.50	
Low-level input voltage	VIL	_		4.5	—	—	1.35	—	1.35	V
				6.0	—	—	1.80	—	1.80	
	V _{OH}			2.0	1.9	2.0	_	1.9	_	
		V _{IN} = V _{IH} or V _{IL}	$I_{OH} = -20 \ \mu A$	4.5	4.4	4.5	—	4.4		
High-level output voltage				6.0	5.9	6.0	_	5.9		V
J. J			I _{OH} = -6 mA	4.5	4.18	4.31	_	4.13	_	
			$I_{OH} = -7.8 \text{ mA}$	6.0	5.68	5.80	_	5.63		
	V _{OL}	V _{IN} = V _{IH} or V _{IL}		2.0	_	0.0	0.1	_	0.1	
			$I_{OL} = 20 \ \mu A$	4.5	—	0.0	0.1	—	0.1	
Low-level output voltage				6.0	—	0.0	0.1	—	0.1	V
J. J			I _{OL} = 6 mA	4.5	_	0.17	0.26	_	0.33	
			I _{OL} = 7.8 mA	6.0	—	0.18	0.26	—	0.33	
3-state output off-state current	I _{OZ}	$V_{IN} = V_{IH} \text{ or } V_{IL}$ $V_{OUT} = V_{CC} \text{ or } GND$		6.0		_	±0.5	_	±5.0	μΑ
Input leakage current	I _{IN}	$V_{IN} = V_{CC}$ or GND		6.0		_	±0.1	_	±1.0	μA
Quiescent supply current	Icc	$V_{IN} = V_{CC}$ or GND		6.0		_	4.0	_	40.0	μA

Ta = **Test Condition** Ta = 25°C –40 to 85°C Characteristics Symbol Unit CL (pF) $V_{CC}(V)$ Min Typ. Max Min Max 2.0 20 60 75 ____ ____ t_{TLH} Output transition time 50 4.5 6 12 15 ns ____ ____ **t**THL 6.0 5 10 13 2.0 30 90 115 ____ ____ 50 4.5 11 18 23 ____ ____ 6.0 10 15 20 tpLH Propagation delay ____ ns time 165 2.0 42 130 tpHL 150 4.5 14 26 33 ____ ____ 6.0 12 22 28 ____ ____ 2.0 30 90 115 50 4.5 11 18 23 ____ ____ t_{pZL} 6.0 ____ 10 15 ____ 20 Output enable time $R_L = 1 \; k \Omega$ ns 2.0 42 130 165 t_{pZH} 150 4.5 14 26 33 6.0 12 22 28 ____ ____ 125 2.0 24 100 tpLZ Output disable time $R_L = 1 \ k\Omega$ 50 4.5 12 20 25 ns t_{pHZ} 6.0 10 17 21 Input capacitance C_{IN} 5 10 10 pF Output capacitance 10 pF COUT ____ ____ ____ ____ ____ CPD Power dissipation 41 pF ____ ____ ____ ____ capacitance (Note)

AC Characteristics (input: $t_r = t_f = 6 \text{ ns}$)

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

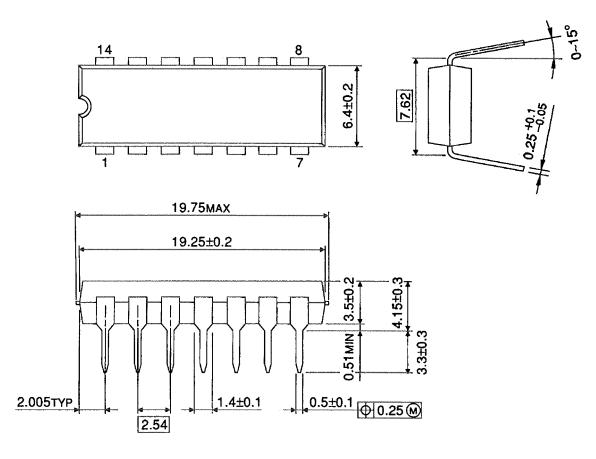
Average operating current can be obtained by the equation:

 I_{CC} (opr) = $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/4$ (per gate)

Package Dimensions

DIP14-P-300-2.54

Unit : mm



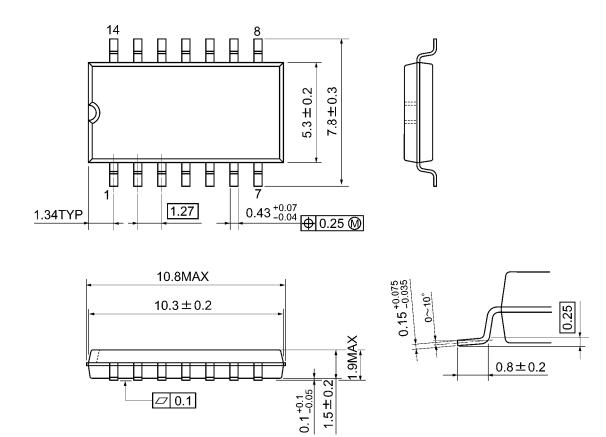
Weight: 0.96 g (typ.)



Package Dimensions

SOP14-P-300-1.27A

Unit: mm



Weight: 0.18 g (typ.)

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