



60V N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI3333-8 (Type UX)

Product Summary

BV _{DSS}	R _{DS(ON)} max	I _D max T _C = +25°C
2014	9.5mΩ @ V _{GS} = 10V	45A
60V	$13.3 \text{m}\Omega$ @ $V_{GS} = 4.5 \text{V}$	36A

Description and Applications

This new generation MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Power Management Functions
- DC-DC Converters
- Synchronous Rectifier

Features

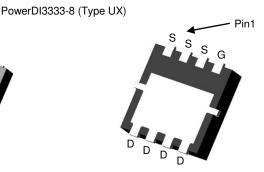
- 100% Unclamped Inductive Switching Ensures more reliable and robust end application
- Low On-Resistance
- Low Input Capacitance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

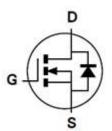
- Case: PowerDI[®]3333-8 (Type UX)
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 [®]3
- Weight: 0.072 grams (Approximate)







Bottom View



Equivalent Circuit

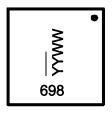
Ordering Information (Note 4)

Part Number	Case	Packaging
DMT69M8LFV-7	PowerDI3333-8 (Type UX)	2000/Tape & Reel
DMT69M8LFV-13	PowerDI3333-8 (Type UX)	3000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



698 = Product Type Marking Code
YYWW = Date Code Marking
YY = Last Two Digits of Year (ex: 18 = 2018)
WW = Week Code (01 to 53)



Maximum Ratings $(@T_A = +25^{\circ}C, \text{ unless otherwise specified.})$

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V_{DSS}	60	V	
Gate-Source Voltage		V_{GSS}	±16	V
Continuous Drain Current (Note F) V 10V	$T_C = +25$ °C $T_C = +70$ °C	ΙD	45 37	Α
Continuous Drain Current (Note 5) V _{GS} = 10V	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	l _D	11 8.9	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	60	Α	
Maximum Continuous Body Diode Forward Current (Note 5)	Is	2	Α	
Avalanche Current, L = 0.1mH	I _{AS}	20.3	Α	
Avalanche Energy, L = 0.1mH	E _{AS}	20.6	mJ	
V_{DS} Spike $t = 10 \mu s$		V _{SPIKE}	72	V

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	P_{D}	2.2	W
Thermal Resistance, Junction to Ambient (Note 5)		$R_{ hetaJA}$	57	°C/W
Total Power Dissipation (Note 5)	$T_C = +25^{\circ}C$	P_{D}	42	W
Thermal Resistance, Junction to Case (Note 5)		$R_{ heta JC}$	3	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

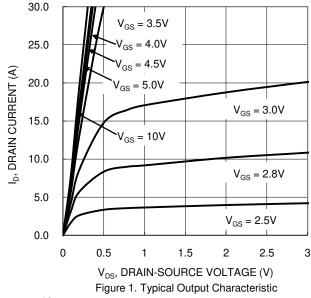
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 48V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 16V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage	V _{GS(TH)}	1	_	3	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance		_	7.5	9.5	mC	$V_{GS} = 10V, I_D = 13.5A$	
Static Drain-Source On-Nesistance	R _{DS(ON)}	_	9.9	13.3	mΩ	$V_{GS} = 4.5V, I_D = 11.5A$	
Diode Forward Voltage	V_{SD}	_	_	1.2	V	$V_{GS} = 0V, I_{S} = 13.5A$	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	Ciss	_	1925	_	pF	\/ 20\/ \/ 0\/	
Output Capacitance	Coss	_	438	_	рF	$V_{DS} = 30V, V_{GS} = 0V,$ - f = 1MHz	
Reverse Transfer Capacitance	C_{rss}	_	41	_	рF	11 = 11VIM2	
Gate Resistance	R_g	_	1.7		Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 10V)	Q_g	_	33.5	_	nC		
Total Gate Charge (V _{GS} = 4.5V)	Q_g	_	15.6		nC	$V_{DS} = 30V, I_{D} = 13.5A$	
Gate-Source Charge	Q_{gs}	_	4.7	_	nC	V _{DS} = 30V, I _D = 13.5A	
Gate-Drain Charge	Q_{gd}	_	5.3	_	nC	1	
Turn-On Delay Time	t _{D(ON)}	_	4.5	_	ns		
Turn-On Rise Time	t _R	_	8.6	_	ns	$V_{DD} = 30V, V_{GS} = 10V,$ $R_G = 6\Omega, I_D = 13.5A$	
Turn-Off Delay Time	t _{D(OFF)}	_	35.9	_	ns		
Turn-Off Fall Time	t _F	_	15.7	_	ns		
Body Diode Reverse Recovery Time	t _{RR}	_	18.2	_	ns	1. 10 EA di/dt 400 A/vo	
Body Diode Reverse Recovery Charge	Q _{RR}	_	33.1	_	nC	$I_F = 13.5A$, di/dt = 400A/ μ s	

Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.







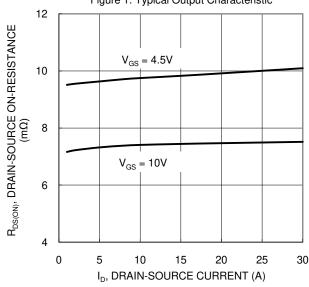


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

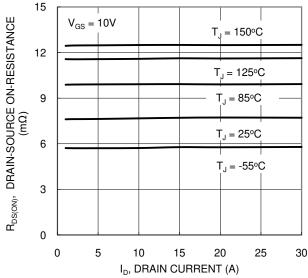


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

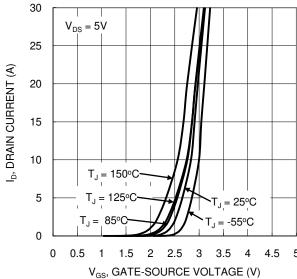


Figure 2. Typical Transfer Characteristic

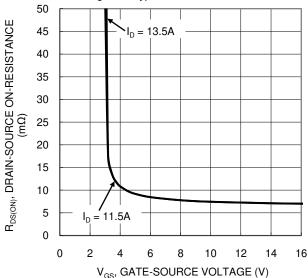


Figure 4. Typical Transfer Characteristic

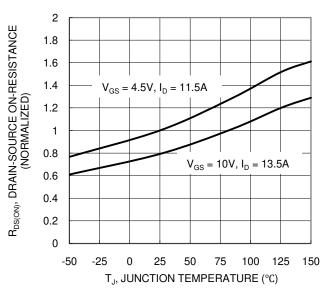


Figure 6. On-Resistance Variation with Junction Temperature





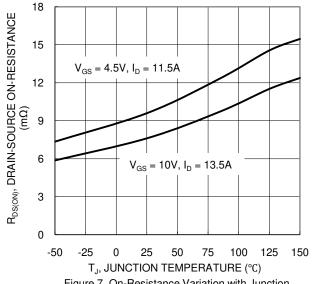


Figure 7. On-Resistance Variation with Junction Temperature

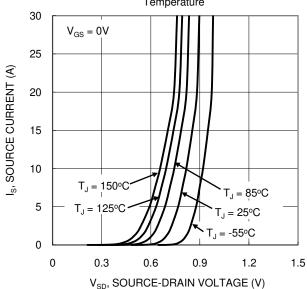
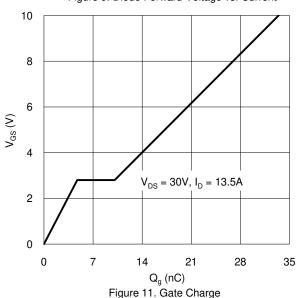


Figure 9. Diode Forward Voltage vs. Current



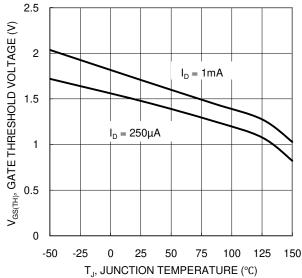


Figure 8. Gate Threshold Variation vs. Junction Temperature

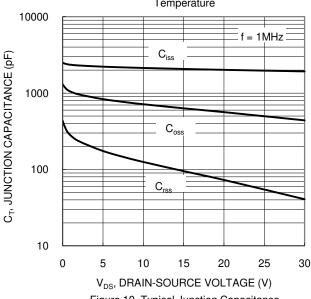
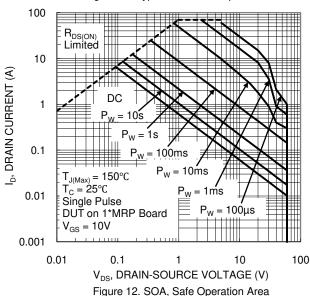


Figure 10. Typical Junction Capacitance





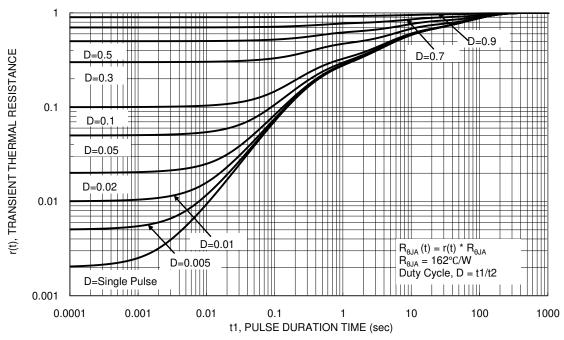


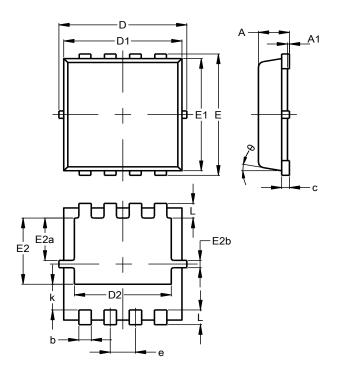
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8 (Type UX)

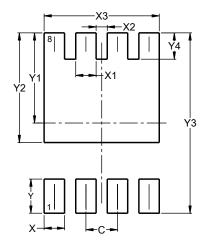


PowerDI3333-8 (Type UX)					
Dim	Min	Max Typ			
Α	0.75	0.85	0.80		
A1	0.00	0.05			
b	0.25	0.40	0.32		
С	0.10	0.25	0.15		
D	3.20	3.40	3.30		
D1	2.95	3.15	3.05		
D2	2.30	2.70	2.50		
Е	3.20	3.40	3.30		
E1	2.95	3.15	3.05		
E2	1.60	2.00	1.80		
E2a	0.95	1.35	1.15		
E2b	0.10	0.30	0.20		
е	0.65 BSC				
k	0.50	0.90	0.70		
L	0.30	0.50	0.40		
θ	0°	12°	10°		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8 (Type UX)



Dimensions	Value (in mm)
С	0.650
X	0.420
X1	0.420
X2	0.230
Х3	2.370
Υ	0.700
Y1	1.850
Y2	2.250
Y3	3.700
Y4	0.540



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