UNR92A5G

Silicon NPN epitaxial planar type

For digital circuits

■ Features

- Optimum for high-density mounting and downsizing of the equipment
- Contribute to low power consumption

■ Absolute Maximum Ratings $T_a = 25$ °C

	Parameter	Symbol	Rating	Unit	
Collector current I _C 80 mA	Collector-base voltage (Emitter open)	V _{CBO}	50	V	
	Collector-emitter voltage (Base open)	V _{CEO}	50	V	
Total power dissipation P _T 125 mW	Collector current	I_{C}	80	mA	
	Total power dissipation	P _T	125	mW	
Junction temperature T_j 125 °C	Junction temperature	T_j	125	°C	
Storage temperature T_{stg} -55 to +125 $^{\circ}$ C	Storage temperature	T _{stg}	-55 to +125	°C	

■ Package

- Code
 - SSMini3-F3
- Pin Name
 - 1: Base
 - 2: Emitter
 - 3: Collector

■ Marking Symbol: HC

■ Internal Connection

$$B \circ \longrightarrow K_1 \circ C$$

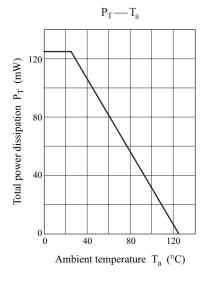
■ Electrical Characteristics $T_a = 25$ °C±3°C

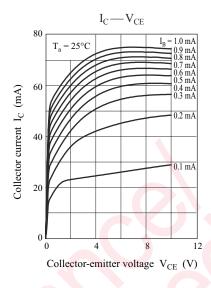
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_{\rm C} = 10 \mu\text{A}, I_{\rm E} = 0$	50			V
Collector-emitter voltage (Base open)	V _{CEO}	$I_{\rm C} = 2 \text{mA}, I_{\rm B} = 0$	50			V
Collector-base cutoff current (Emitter open)	I _{CBO}	$V_{CB} = 50 \text{ V}, I_{E} = 0$			0.1	μΑ
Collector-emitter cutoff current (Base open)	I _{CEO}	$V_{CE} = 50 \text{ V}, I_{B} = 0$			0.5	μΑ
Emitter-base cutoff current (Collector open)	I _{EBO}	$V_{EB} = 6 \text{ V, } I_{C} = 0$			0.01	mA
Forward current transfer ratio	h _{FE}	$V_{CE} = 10 \text{ V}, I_{C} = 5 \text{ mA}$	160		460	_
Collector-emitter saturation voltage	V _{CE(sat)}	$I_C = 10 \text{ mA}, I_B = 0.3 \text{ mA}$			0.25	V
Output voltage high-level	V_{OH}	$V_{CC} = 5 \text{ V}, V_{B} = 0.5 \text{ V}, R_{L} = 1 \text{ k}\Omega$	4.9			V
Output voltage low-level	V _{OL}	$V_{CC} = 5 \text{ V}, V_{B} = 2.5 \text{ V}, R_{L} = 1 \text{ k}\Omega$			0.2	V
Input resistance	R ₁		-30%	10	+30%	kΩ
Transition frequency	f_T	$V_{CB} = 10 \text{ V}, I_{E} = -2 \text{ mA}, f = 200 \text{ MHz}$		150		MHz

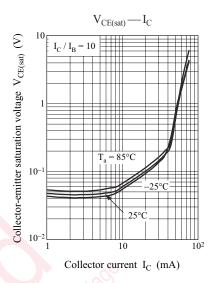
Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

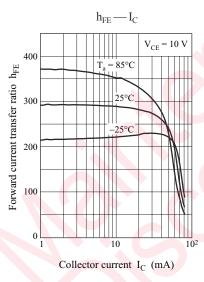
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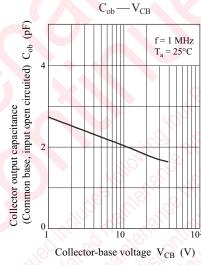
Panasonic

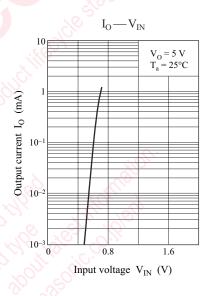


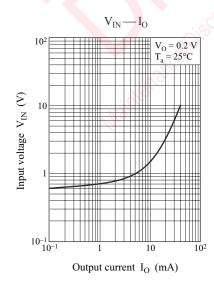










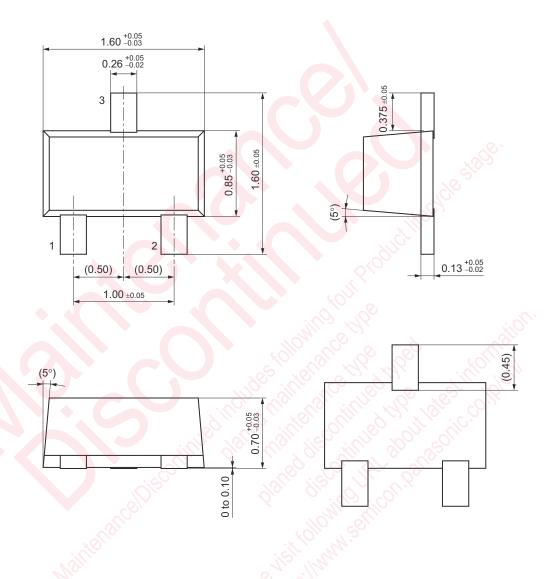


2 SJH00247AED

Unit: mm

Panasonic UNR92A5G

SSMini3-F3



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